

Use Case for IEC/IEEE 60802

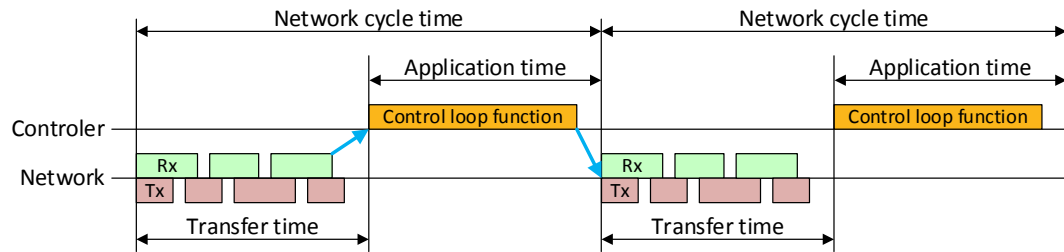
Cycle timing models in industrial applications

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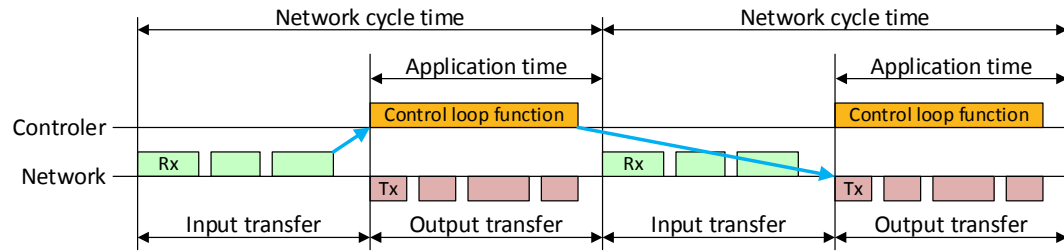
Presented by: Steve Zuponcic & Mark Hantel, Rockwell Automation

IEEE 802.1 Plenary, July 2018

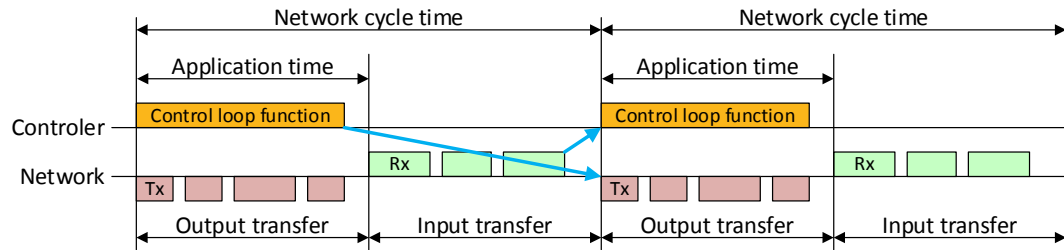
Overview



Model 1: currently depicted as use case 2 in IEC 60802

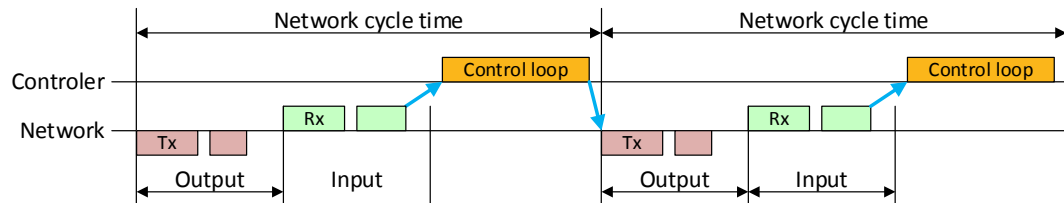
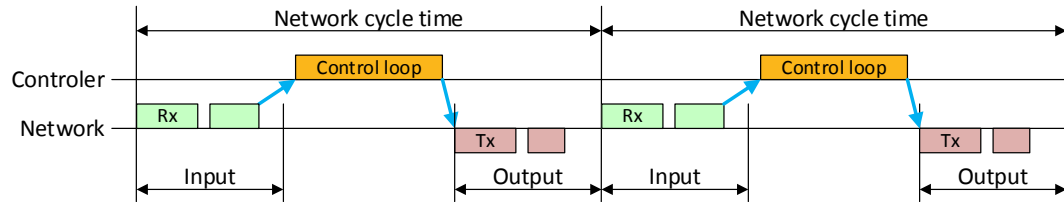
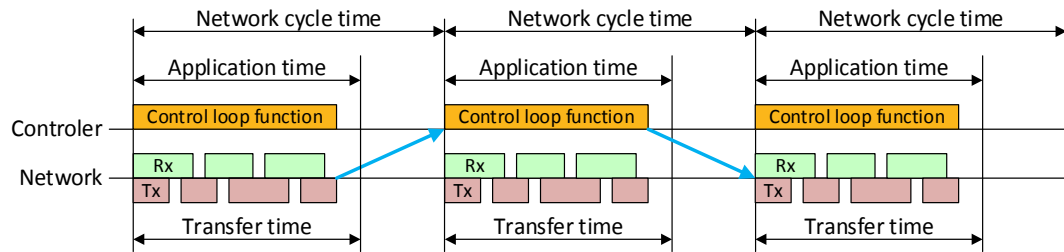


Model 2: commonly used model, also called '2 cycle timing model' in other specifications.

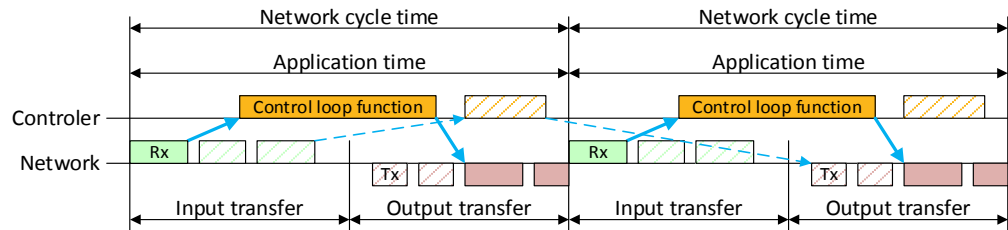


Model 3: very similar to Model 2, but shifted by 180°

Overview



Overview



In existing technologies some of the models are used in optimized ways to reduce the network cycle time and/or the IO-reaction time (sometimes also called 'makespan' or 'roundtrip delay time').

More models:

- Optimizations of listed models
- Combinations of listed models
- Additional models