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Title	Changes to Table 345d-Coexistence Control Channel Function and Frame Numbering Scheme
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Re:	Changes to Draft Standard
Abstract	Changes to Table 345-d of Draft Standard (October 2006 Version)
Purpose	Add changes to table which support sections in the draft standard
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Changes to Table 345d-Coexistence Control Channel Function and Frame Numbering Scheme

By

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Introduction

Table 345d identifies the distribution of Control Channel Slots needed to support a number of coexistence protocol Wireless MAN-CX system management functions. This table has been updated from the original to reflect the need for additional CMI slots, support Frequency Keying and Ad Hoc system discovery, and undertake Universal Timing Recovery (formerly known as GPS). Additionally, section identification information has been added to direct the reader to areas in the draft standard where the functions are discussed.

Editorial changes: Insert the following table as a replacement for the previous table in Section 10.5.3 of the Draft Standard.

Table 345d- Coexistence Control Channel Function and Frame Numbering Scheme

Function Of Control Channel	Control Channel Function Name & Chapter	CX_MAC_NO containing Control Channel for given Frame Duration			
		5 ms	10 ms	20 ms	Starting Time WRT Absolute Reference (msec)
Timing Recovery (DL)	Timing recovery based on either Geosatellite or other timing distribution schemes capable of provide a UTC standard time. Sections 15.2.1.2-4; Annex h_B	1	1	1	0
Timing Recovery (UL)		41	21	11	200
Timing Recovery (DL)		81	41	21	400
Timing Recovery (UL)		121	61	31	600
Timing Recovery (DL)		161	81	41	800
Timing Recovery (UL)		201	101	51	1000
Timing Recovery (UL)		241	121	61	1200
AT1	Section 15.4.3.2	281	141	71	1400
CX_CMI_D1	Section 15.1.4.1.2, 15.2.1.13	321	161	81	1600
CX_CMI_U1		361	181	91	1800
Spare		401	201	101	2000
No+Io (1)	Section 15.3.3.2	441	221	111	2200
AT2	Section 15.4.3.2	481	241	121	2400
CX_CMI_D2		521	261	131	2600
CX_CMI_U2		561	281	141	2800

Spare		601	301	151	3000
Freq Keying 1	Section 15.4.3.4.	641	321	161	3200
AT3	Section 15.4.3.2	681	341	171	3400
CX_CMI_D3		721	361	181	3600
CX_CMI_U3		761	381	191	3800
Spare		801	401	201	4000
AT4	Section 15.4.3.2	841	421	211	4200
CX_CMI_D4		881	441	221	4400
CX_CMI_U4		921	461	231	4600
CX_CMI_D5		961	481	241	4800
Freq Keying 2		1001	501	251	5000
CX_CMI_U5		1041	521	261	5200
CX_CMI_D6		1081	541	271	5400
No+Io (2)		1121	561	281	5600
Freq Keying3		1161	581	291	5800
Spare		1201	601	301	6000
CX_CMI_U6		1241	621	311	6200
CX_CMI_D1		1281	641	321	6400
Freq Keying 4		1321	661	331	6600
No+Io (3)		1361	681	341	6800
Spare		1401	701	351	7000
CX_CMI_U1		1441	721	361	7200
CX_CMI_D2		1481	741	371	7400
CX_CMI_U2		1521	761	381	7600
No+Io (4)		1561	781	391	7800

Spare		1601	801	401	8000
CX_CMI_D3		1641	821	411	8200
CX_CMI_U3		1681	841	421	8400
CX_CMI_D4		1721	861	431	8600
CX_CMI_U4		1761	881	441	8800
Spare		1801	901	451	9000
CX_CMI_D5		1841	921	461	9200
CX_CMI_U5		1881	941	471	9400
CX_CMI_D6		1921	961	481	9600
CX_CMI_U6		1961	981	491	9800