ARQ and HARQ inter-working for IEEE 802.16m system

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Base Contribution:

Purpose: Discussion and approval of the proposal into the IEEE 802.16m System Description Document

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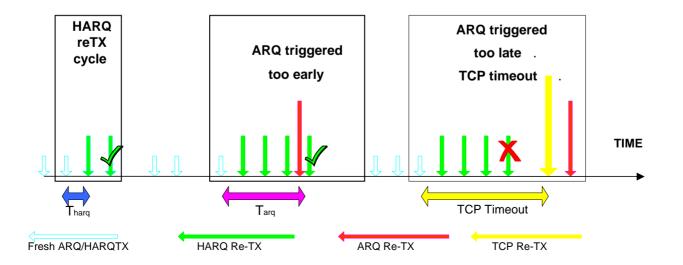
ARQ and HARQ in the reference system

HARQ

- PHY layer, fast feedback and error correction
- Residual transmission error and feedback error (10-2)

ARQ

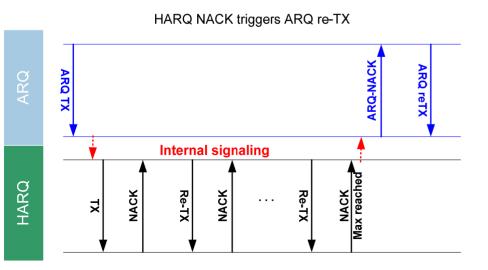
- MAC layer, (relatively slow) timer based error correction
- Feedback CRC protected, reliable communication (10⁻⁴~10⁻⁶)
- Without coupling in the reference system
 - waste resources: redundant retransmission, extra feedback
 - Delay: conservative ARQ retry timer leads to slow ARQ retransmission
 - ARQ is still necessary for TCP performance and MAC reliability

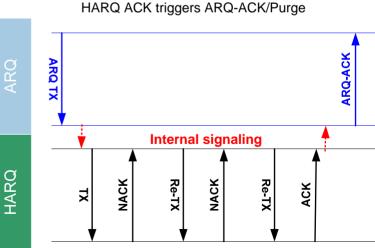


ARQ-HARQ inter-working proposal for IEEE 802.16m

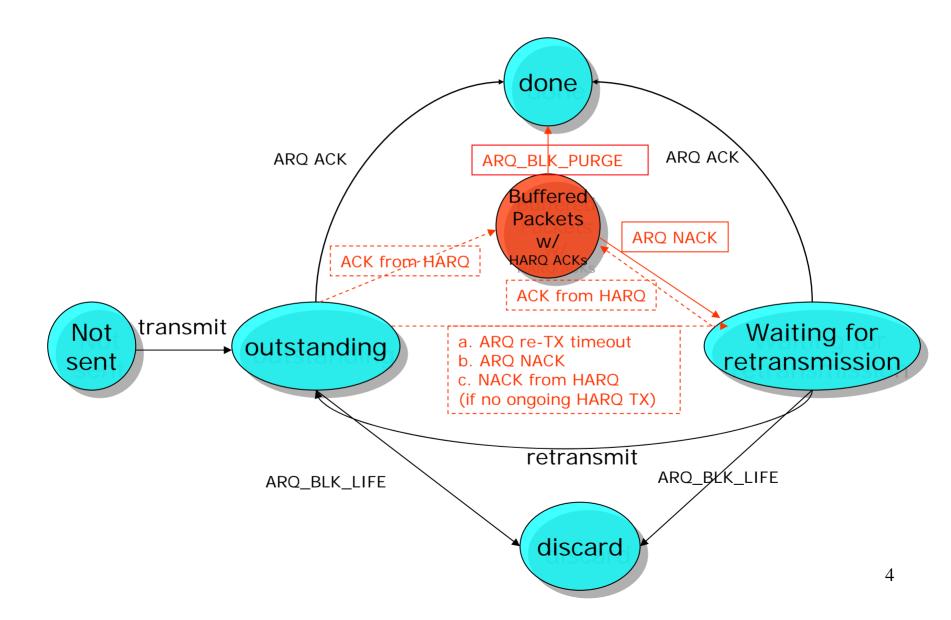
Three basic coupled HARQ-ARQ operations

- 1. ARQ NAK is internally triggered by HARQ reaching its max retransmission count, enabling ARQ fast retransmission
- 2. ARQ ACK is internally triggered by HARQ ACK to save overhead (this eliminates ARQ ACK tx on the air)
- 3. Other PHY layer errors (e.g. $NAK \rightarrow ACK$) can not be handled by HARQ
 - Detected by HARQ via early error detection
 - recovered by ARQ
- #1 may be implemented separately; #2 and #3 are associated





ARQ state diagram – TX side

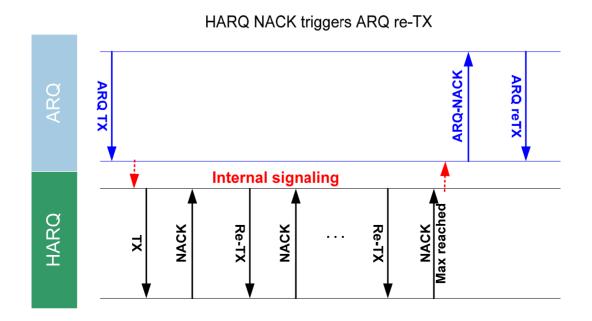


ARQ state machine – RX side

- ARQ RX schedules ACK when
 - ACK→NAK detected
 - Network entry at TBS during handover
- ARQ RX schedules feedback (NAK or NAK+ACK) when
 - NAK→ACK detected
 - HARQ residual error occurs
 - Last packet in the buffer (empty buffer or grant request)

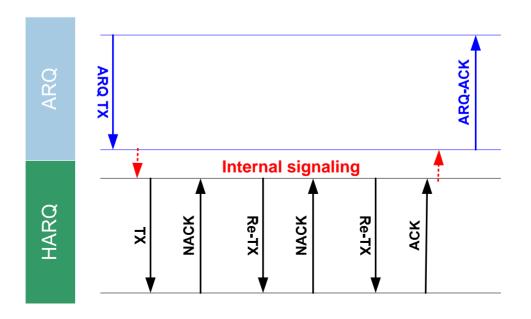
Operation #1: ARQ fast retransmission

- HARQ reaching its max retransmission count and still in error
- ARQ NACK internally triggered to enable ARQ fast retransmission
- Requirement
 - interface between HARQ ARQ
 - Mapping between ARQ buffer (e.g. BSNs) to HARQ burst
 - Optionally, explicit ACK for both DL and UL significantly simplifies design
 - HARQ transmitter (MS) fully aware of whether the max # retransmission is successful
 - MS can realize HARQ residual error without possible latency



Operation #2: Removing ARQ ACK overhead

- HARQ receiver successfully received the pkt
- ARQ receiver does not send ARQ ACK
- HARQ transmitter sends an ACK to ARQ upon HARQ-ACK
- Requirement
 - All requirements in operation #1
 - Mechanisms to handle HARQ unusual errors for E2E reliability (i.e. #3)

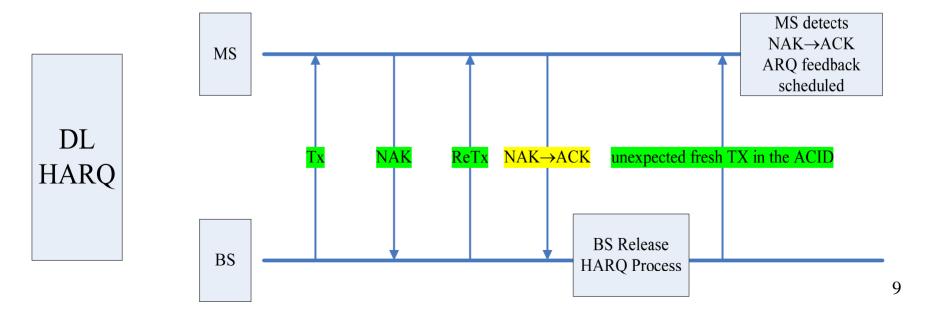


Operation #3: Enhance E2E reliability

- To further improve E2E reliability, ARQ must cover
 - HARQ feedback error
 - DL or UL
 - Ongoing traffic or the last packet
 - "<Max Re-TX count" or "= max Re-TX count"
 - DTX or DRX due to missed MAP or HARQ early termination
- ARQ original state machine still works for E2E reliability. Here are possible improvements to trigger fast error recovery.
- Requirement
 - #3 is only necessary when #2 is implemented
 - All requirements in #1, #2
 - HARQ feedback error detection
 - Special handling of the last DL pkt
- The design depends on HARQ configuration
 - implicit vs Explicit ACK (latter assumed in the sequel)
 - Synchronous vs Asynchronous HARQ (latter assumed)

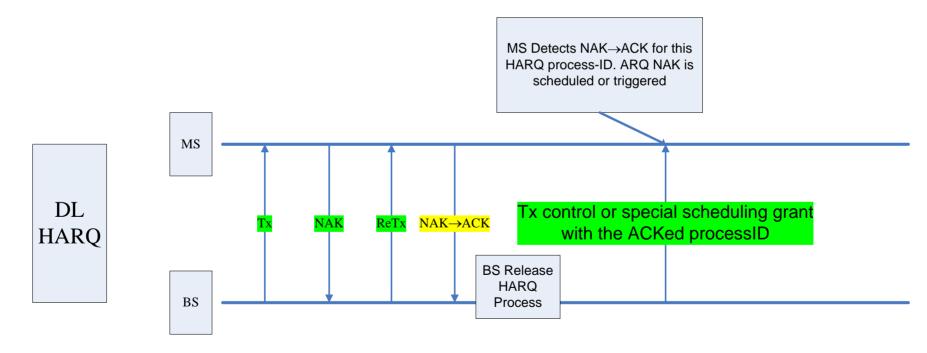
NAK → ACK: DL, #TX < MAX, more data

- NAK→ACK can be easily detected
- ARQ feedback and retransmission will be triggered
- Same if BS HARQ transmitter early terminates the HARQ



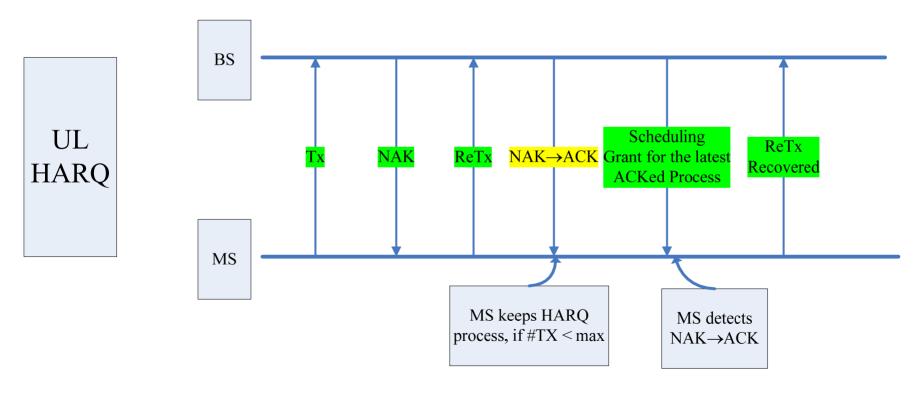
NAK → ACK: DL, #TX < MAX, no more data

- Without special handling, ARQ NAK still happens, but with extra latency
- Faster ARQ recovery possible with solution below
 - A MAC message or unsolicited scheduling grant using that ACID
 - If there is NAK \rightarrow ACK, MS detects it; otherwise MS ignores it



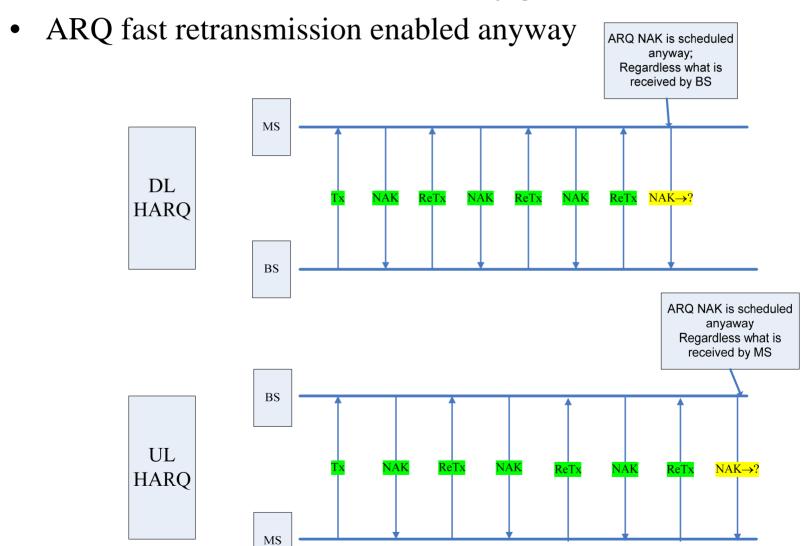
NAK → ACK: UL, #TX < MAX, regardless data

- If possible, don't recycle HARQ buffer
 - NAK→ACK can be detected and recovered at HARQ layer



NAK → ACK: DL/UL, #TX=MAX

Receiver side, ARQ NAK internally generated



SDD proposal

[Insert the following text in section 10 in IEEE 802.16m-08/003r4] ----- **Text Starts** -----

10.x ARQ

10.x.x ARQ and HARQ inter-working

When the HARQ burst fails transmission, HARQ transmitter informs the ARQ entity about the associated MAC PDU in the burst. ARQ marks these PDU as failed and prepares for retransmission.

When the HARQ burst succeed with transmission, HARQ transmitter informs the ARQ entity about the associated MAC PDU in the burst. ARQ marks these PDU as HARQ-ACK'ed and keep them in the buffer until some timer expires. ARQ receiver does not need to send ACK for the associated PDU.

IEEE 802.16m MAC entity detects HARQ feedback error and triggers corresponding recovery procedures at HARQ or ARQ layer.

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