

RPRWG Plenary Meeting Agenda March 12, 2001

Morning Parallel Sessions

9:00	2:00 Performance Ad Hoc	Khaled Amer
9:00	2:00 RPR Introduction for New Attendees	Raj Sharma

Afternoon Plenary Session

13:00	0:00 Seating	Everyone
13:00	0:15 Welcome and Introductions	Mike Takefman
13:15	0:05 Agenda Scrub	Mike Takefman
13:20	0:05 Minutes of January Meeting	Mike Takefman
13:25	0:30 802.17 Administrivia	Mike Takefman
13:55	0:10 Goals of the Meeting	Mike Takefman
1:33	0:08 IETF Report	Albert Herrera
1:41	0:35 Carrier Requirements and Operational Concerns	Shannon Silvas
2:16	0:20 SBC RPR Requirements	George Young
2:36	0:39 Excite@Home view of 802.17	Bruce Johnson
3:15	0:20 Break	
3:35	0:33 Use of SRP within SprintLink	Ted Seely
4:08	0:18 RPR Requirements from a LEC Perspective	Steven Wright
4:26	0:30 Virtual Private Lines	Roy Bynum
4:56	0:25 Network requirements for the RPR metro application	Clausio Coltro
5:21	0:30 Fairness is a 10 GE Ring of Switches	Khaled Amer
5:51	0:00 Adjourn	

Evening Perf Adhoc

20:00	3:00 Perf Adhoc	Khaled Amer
23:00	0:00	

RPRWG Plenary Meeting Agenda March 13, 2001

Morning Plenary Session

8:00	0:00 Seating	Everyone
8:00	0:05 Registration Rant 78 Pre-registered 135 Signed Sheet	Mike Takefman
8:05	0:40 RPR Mac Requirements	Bob Sultan
8:45	0:40 RPR Scope and Requirements	Steven Wood
9:25	0:40 RPR MAC Features & Requirements	Fredric Thepot
10:05	0:18 Break	
10:23	0:37 Datapath for RPR MAC: objectives	Harry Peng
11:00	0:20 Different span bandwidth issues & considerations	Pankaj Jha
11:20	0:40 TDM Traffic on RPR	Raj Sharma
12:00	1:05 Lunch	
13:05	0:18 RPR Requirements for MAN / WAN	David Zelig
13:23	0:21 RPR over SONET/SDH vs. Native RPR	Shahid Akhtar
13:44	0:16 Why we need to support lower rates than OC-48	Offer Pazy
14:00	0:40 Objectives, requirements, and strategies	David James
14:40	0:17 GFP Truth and Myth	Harry Peng
14:57	0:10 Homework Assignment 1: Objectives & Requirements	Mike Takefman
15:07	0:15 Break	
15:22	0:25 Ring/Mesh/multiple Rings MAC considerations	Pankaj Jha
15:47	0:13 Transit path requirements	Nader Vijeh & Harry Peng
16:00	0:21 A steering based Protection Mechanism	Jingsong Fu
16:21	0:30 Objectives: Wrapping vs Steering	Necdet Uzun
16:51	0:30 RPR MAC Objectives: Protection Switching	Sanjay Agarwal
17:21	0:30 Protection Switching	Lars Ramfelt
17:51	0:10 Homework Assignment 2: Terms & Definitions	Mike Takefman
18:01	0:00 Adjourn	
18:01	0:00	

RPRWG Plenary Meeting Agenda March 14, 2001

Morning Plenary Session

8:00	0:00 Seating	Everyone
8:00	0:12 Agenda Scrub	Mike Takefman
8:12	0:20 Interoperability Requirements	Nader Vijeh & Harry Peng
8:32	0:30 Interoperability Requirements	Andrew Brown
9:02	0:30 Bandwidth Management	Lars Ramfelt
9:32	0:38 Congestion Control and Fairness	Sanjay Agarwal
10:10	0:18 Break	
10:28	0:30 RPR MAC Model / Contention Resolution	B.J. Lee
10:58	0:17 What do we mean by "fairness?"	Nader Vijeh & Harry Peng
11:15	0:30 SLA Compliant Arbitration Protocols	David James
11:45	1:24 Lunch	
13:09	0:30 Dynamic Bandwidth Control	Nader Vijeh
13:39	0:33 SRP Fairness and Simulation Results	Donghui Xie
14:12	0:35 Simulation Results	Adisak Mekkittikul
14:47	0:30 Initial suggestion of the MAC client (northbound) interface	Offer Pazy
15:17	0:21 Break	
15:38	0:20 Telecommunications Institute of Vienna	Harmen Van As
15:58	0:10 Issues	Gunes Aybay
16:08	1:30 Terms and Conditions	Mike Takefman
17:38	0:45 Preparation of Requirements/ Objectives	
18:23	0:00	
18:23	0:00	
18:23	0:00	

Evening Perf Adhoc

20:00	3:00 Perf Adhoc	Khaled Amer
23:00	0:00	

