



The Finite State Machine for 802.17 Topology

Kshitij Kumar
kkumar@lanterncom.com



Why create a Topology FSM?



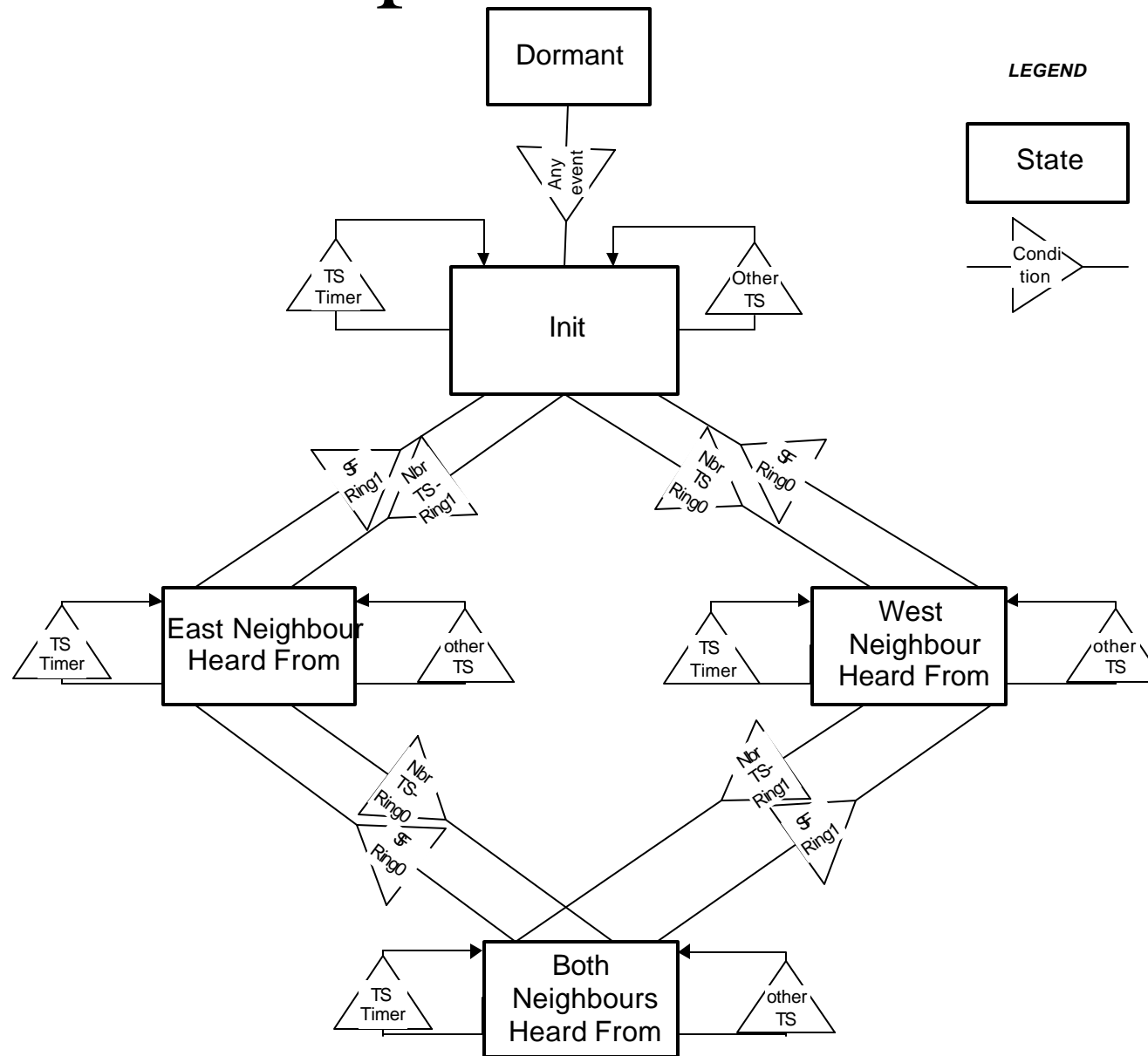
- Motivation
 - To have a Deterministic, Finite set of states, conditions and actions to define the Topology algorithm for 802.17
- Contents of this presentation:
 - A Deterministic yet simple Finite State Machine
 - Covers the behaviour of the Topology component of 802.17
 - An output of the PAH



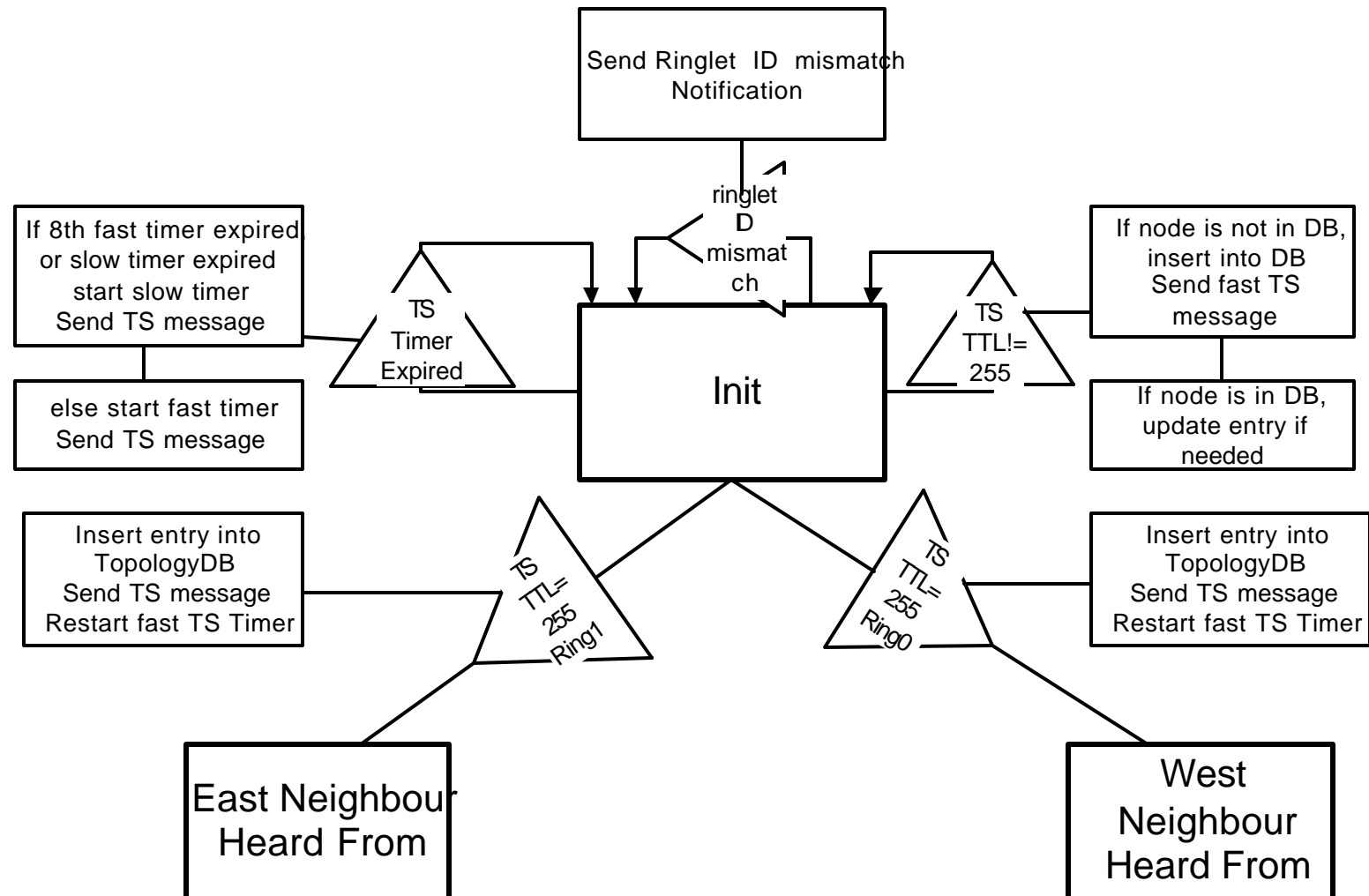
The Topology States

- Five simple states
 - Dormant (before system has started up)
 - Init
 - West Neighbour Heard From
 - East Neighbour Heard From
 - Both Neighbours Heard From (fully connected)

Simplified FSM



The INIT state





Summary

- An FSM is now available for Topology
 - Complete, gory details are in the revised Clause 10.
- Defines some interfaces:
 - With Protection
 - With OAM
- This is work-in-progress
 - Will be refined further
 - More details added as needed