

Open issues with IEEE P802.3bj

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Introduction

The purpose of this ad hoc is to highlight areas of the draft that are incomplete and warrant special attention.

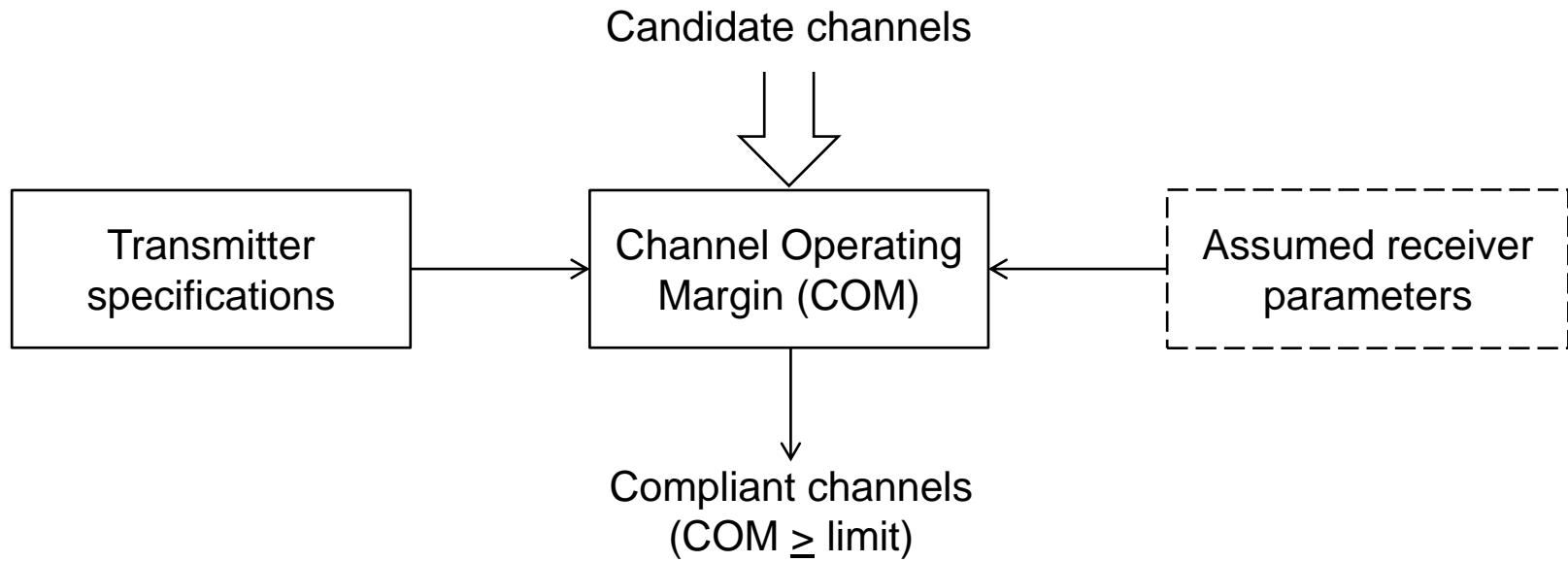
To request Working Group ballot, the document must be complete with no open technical issues.

Energy Efficient Ethernet

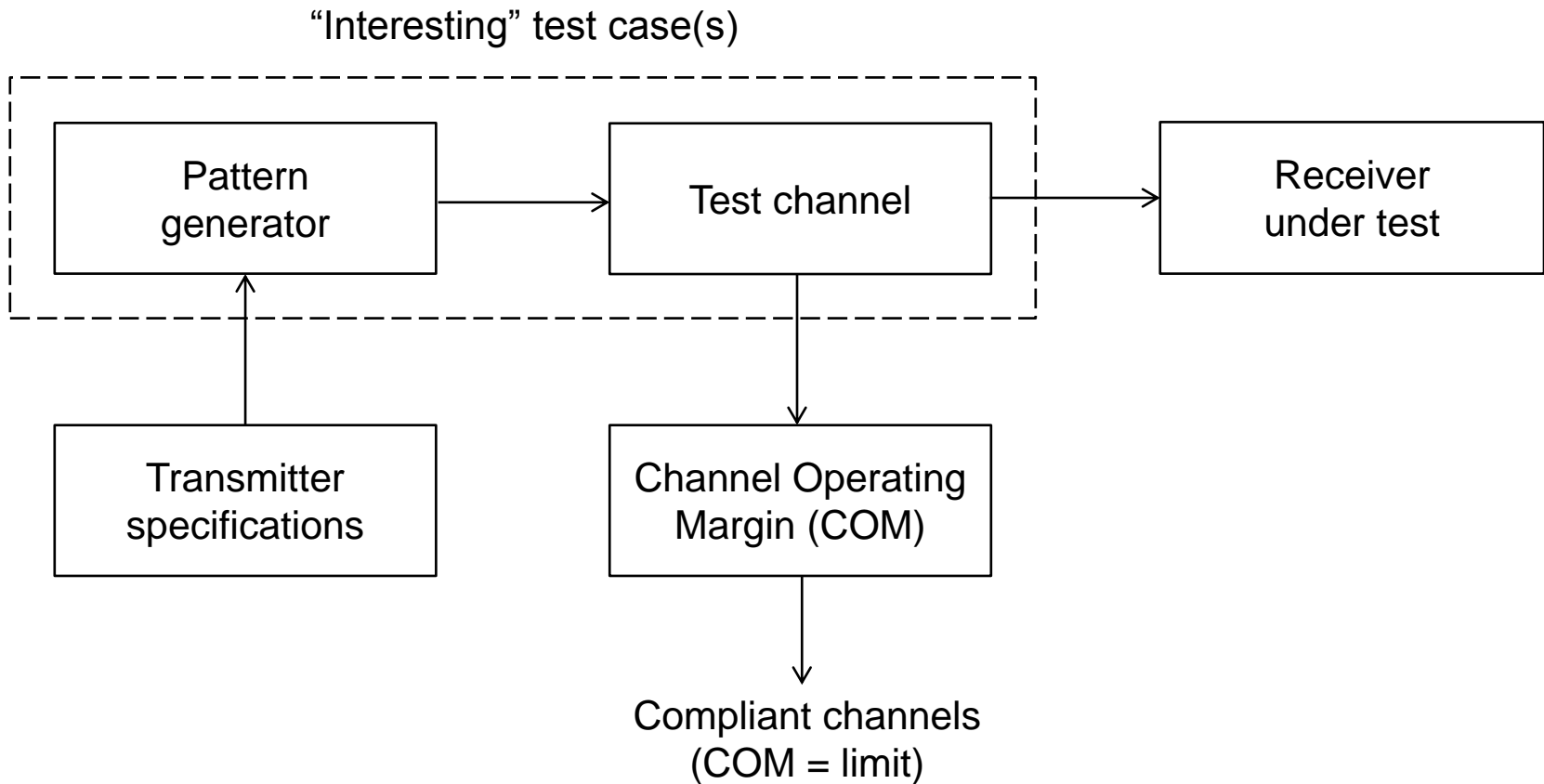
- Scrambler bypass, timing TBD's, LLDP changes
- Refer to presentation from Hugh Barrass

- RS-FEC tx_quiet_timer and rx_quiet_timer values
- PMD service interface and functional, electrical behavior (92, 93, 94)
- PMA service interface and functional behavior (94)

Channel compliance



Receiver interference tolerance



Transmitter and receiver specification and COM

- For Clause 93 and Clause 94...
- ...ensure COM parameters correspond to transmitter specifications
- ...ensure receiver stressors correspond to COM

Clause 92, Annex 92A

- Pattern generator (92.8.4.3.4)
- ...The pattern generator output amplitude is TBD
- ...Transition times of the pattern generator are TBD
- ...Equation 92-7 is TBD
- Mated test fixtures insertion loss is TBD (92.11.3.1)
- Mated test fixtures integrated crosstalk noise is TBD (92.11.3.5)
- Channel ICN is TBD (Equation (92A-7))

Clause 93

- Far-end transmitter noise reference channel (93.8.1.7)
- ...Channel ICN limits (93.9.4)
- Differential to common-mode return loss limit (93.8.2.2)

Clause 94

- Training pattern (94.3.10.8)
- ...Termination symbol initialization
- ...Training pattern seed
- ...Example training frame sequence
- Signal levels test methodology (94.3.12.3)
- Transition time value, methodology, and pattern (94.3.12.5)
- Linear fit test pattern (94.3.12.6)
- Output waveform TBD's (94.3.12.6.1 to 3)
- Far-end transmitter noise reference channel (94.3.12.7)
- Differential to common-mode return loss limit (94.3.13.2)
- Interference tolerance (94.3.13.2)

Other

- Maximum distance for 100GBASE-KP4 alignment marker match (91)
- Skew, Skew Variation, and Delay constraints (92, 94)