

Bit Error Ratio Objective

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Definitions

- Ratio
 - The relative magnitude of two quantities
- Rate
 - A magnitude or frequency relative to a time unit
- Bit error
 - A received bit from a channel that has been altered due to noise, interference, distortion or synchronization errors

• BER (rate or ratio)

 Relative magnitude or magnitude of a received bit from a channel has been altered



System

- Functions are similar
 - Transmitter
 - Interconnect
 - Connectors
 - Channels
 - Receiver
- Assumptions
 - Transmitter doesn't generate errors
 - Errors introduced by channel or receiver

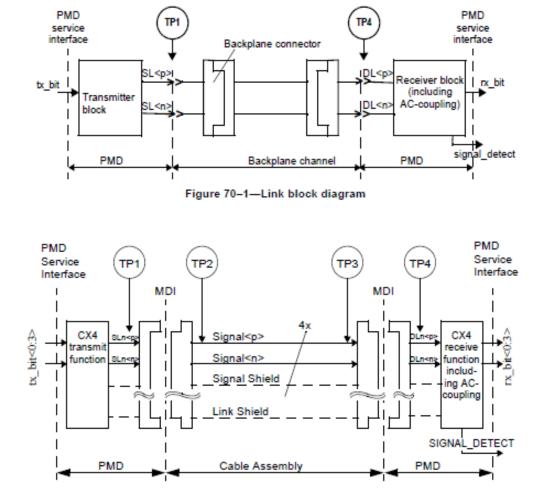
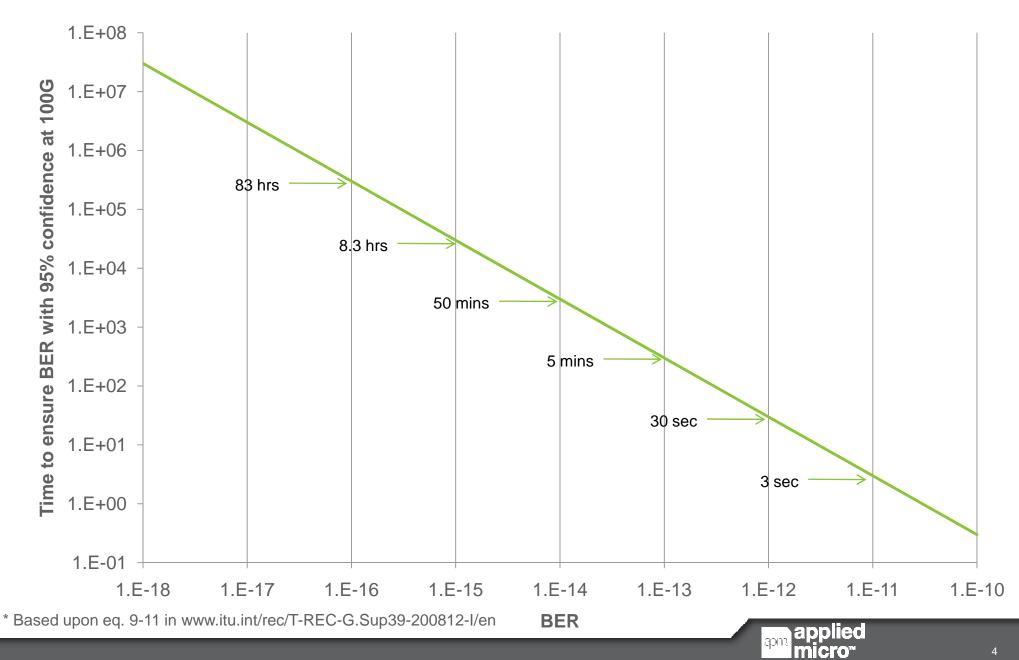


Figure 54-2-10GBASE-CX4 link (half link is shown)



Time Required to Ensure BER



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Test Environments

- Silicon
 - Controlled environment
 - Wafer probe or DUT board of all parts
 - Tester time is a valuable commodity
 - Impacts production throughput and cost
- Channel
 - Specified for cabling, not for backplane
 - Order of magnitude increase in tester time less impact than on silicon
- System
 - Uncontrolled environment
 - Longer test times are more tolerable for statistical sampling



Channel - Backplane

- Assumption
 - Components made for backplane re-used for twinax cabling
- BER primarily impacted by the channel
 - Not a mandatory part of specification
 - For 100G CU SG, recommend the same approach
- Interoperability
 - Specification of the transmitter and the receiver by 802.3ap
 - Channel provided as a reference
- Compliance testing will target PHYs
 - Silicon testing time impacts cost



BER Objective

- Guarantee a certain level of "statistical" performance reliability without adversely affecting the costs
- Previous projects
 - 1 Gb/s BER of 10⁻¹²
 - 10 Gb/s BER of 10⁻¹²
 - 100 Gb/s BER of 10⁻¹² (at MAC/PLS Service Interface)
- Decreasing BER target will require "specialization"
 - Impacts broad market potential
 - Impacts economic feasibility
- Compliance testing at 10⁻¹² BER recommended



Thanks

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Thank you!

