Performance and Implementation Updates to MB810

ChanGoo Lee*, Dae Young Kim**, Chun Sik Shin*, Hae Won Jung*, Hyeong Ho Lee*

* ETRI, ** Chungnam Nat'l Univ., Korea

e-mail: cglee@etri.re.kr, dykim@ccl.chungnam.ac.kr

Abstract

We present simulation and experiments with MB810. By lowering the cutoff frequency of LPF, we confirmed superior survival capability of MB810 coded data over NRZ. We also confirmed the power spectral property by experiment. The BER test of MB810 coded data was done after passing through LPF of a Nyquist cutoff, which resulted in virtually error free transmission. We also give implementation and VHDL simulation results. The gate number required for MB810 encoder are estimated to be under the 20K.

CONTENTS

1. Performance Simulation

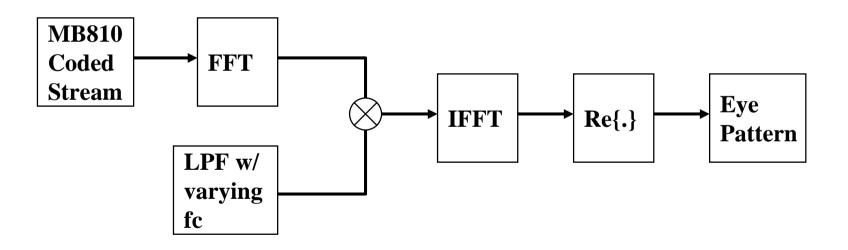
2. Measurement Results

3. Implementation

4. Conclusion

Performance Simulation

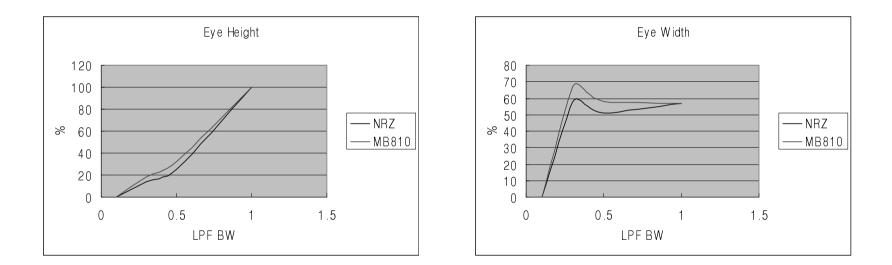
• Eye pattern simulation of MB810 coded stream with a varying cutoff frequency of the Low Pass Filter



System model for simulation of the MB810 eye pattern

Simulation Results: Excessive Drive

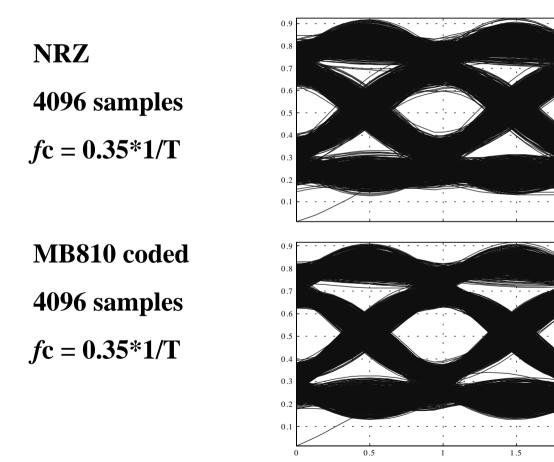
• Eye opening versus low-pass cutoff frequency



Conditions: Input data stream of 4096*5 bits.

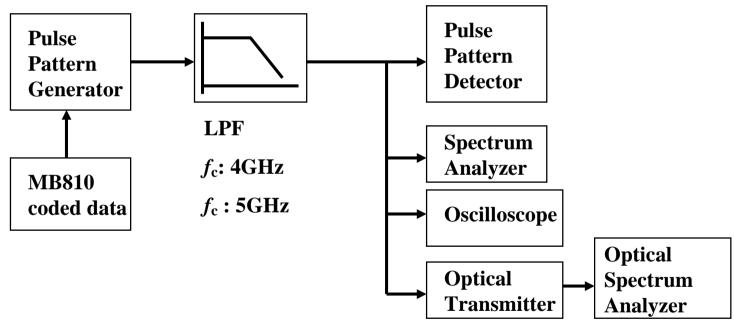
BW normalized to input bit rate. Mark ratio of **PRBS** = 0.5

Simulation Results



2

Measurement Set-up for MB810 Coded Data

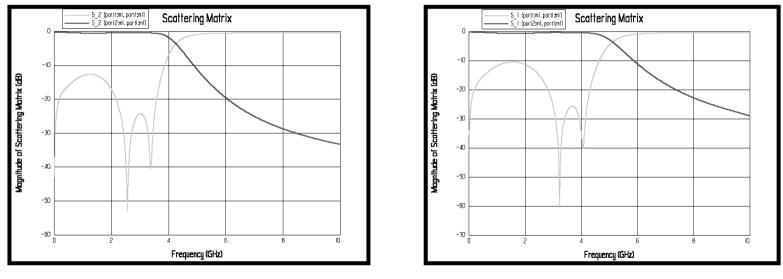


Conditions;

- Data rates used in this measurement are 10Gbps.
- MB810 coded data is generated by program.
- Input random data has mark ratio of 0.5 and sequence period of 100K.

Low Pass Filter Used in This Experiment

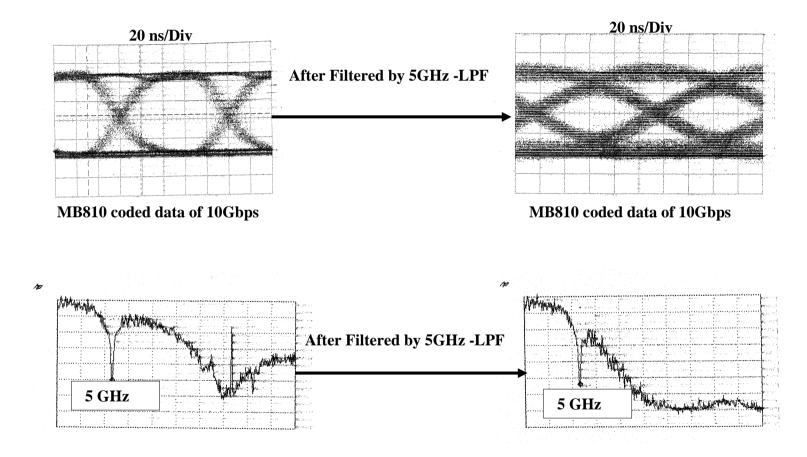
• Filter is a 5-th order Chebyshev implemented in strip-line.



Cutoff Freq. 4GHz

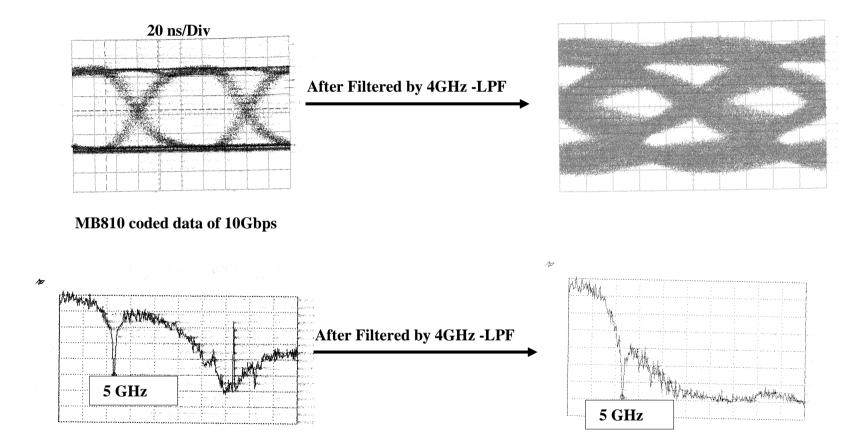
Cutoff Freq. 5GHz

Measurement Results With 5GHz-LPF



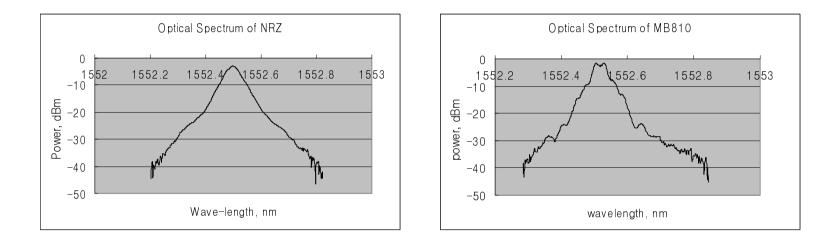
Eye pattern and spectrums are measured in error free condition

Measurement Results With 4GHz-LPF



Eye pattern and spectrums are measured in error free condition

More Measurement - Optical Spectrum

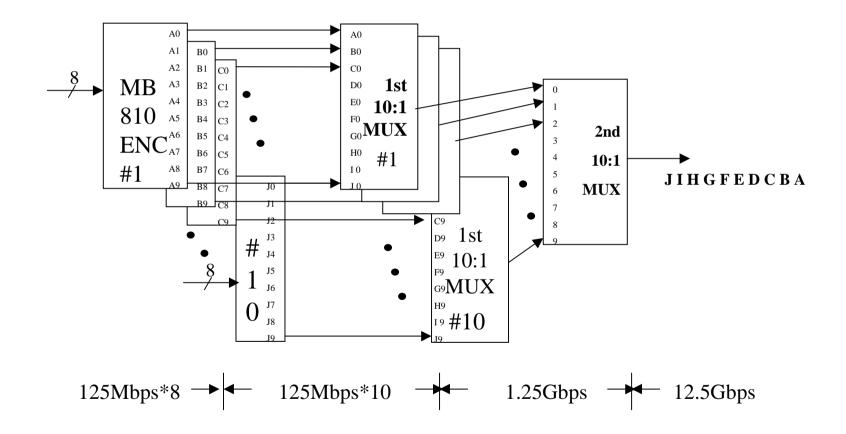


- Measured optical power spectrum of NRZ and MB810.
- The optical spectrum width @ -30dBm of MB810 is decreased by 0.197nm compared to NRZ. [MB810 coded stream passed through 5 GHz LPF are used]
- There is a valley at the center of MB810 spectrum, which contributes to the reduction optical line spectrum.

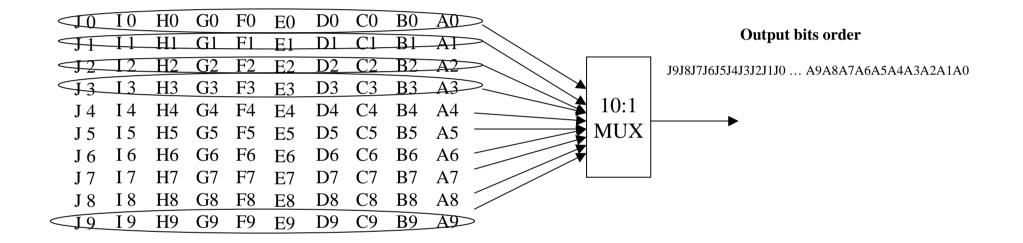
Hints from Measurement Results

- MB810 is stable when cut off set at the Nyquist frequency; 50% of the line rate and 60% of the data rate. A wide eye opening.
- Even driven by 20% more, MB810 maintained near errorfree transmission while NRZ did not. A fair-quality eye opening.
- In case of 10GbE, we need only 5GHz channel bandwidth; 40% of the line rate and 50% of the data rate.

Implementation: Double Multiplexing



Implementation - Word Mux by Bit-Interleaving



MB810 coded stream, with each column being output of the 1st 10:1 MUX.

Implementation - VHDL Simulation Results

We reduced the data speed down to a 10th for implementation with FPGA, and simulated with VHDL

Elle Signal Waw Elle Signal Waw Elle Signal Waw Elle Signal Waw	eform <u>Dev</u>	vice <u>O</u> ptions	Tools ⊻iew ₩i ຼຼຼຼ	ndow <u>H</u> elp Break	10ns	J					-8×
U2 RESET		sons poons	H SONS 200NS	250ns 300ns							
BLOCKE11		VO1 XO1		01 (110 (048) <u>χ</u> ισΒ	XOFO	XOE7	<u>) (361</u>	<u>X52</u> X358	X0A6	, (214
BLOCKE7 BLOCKE6 BLOCKE5 BLOCKE4 BLOCKE3 BLOCKE2											
BLOCKE1 BLOCKE0 DOUT1_9 (hex) DOUT2_9 (hex) DOUT3_9 (hex)	0 000	OBD XO4	sf Joed	X000 X04B	Хієв Хієв	X000 X000	Хов? Хосо	X000 X361	X358		
DOUT5_9.(hex DOUT6_9.(hex DOUT7_9.(hex DOUT7_9.(hex DOUT8_9.(hex)	0 000 0 000 0 000 0 000 0 000 0 000									Хоже)(214 X000
DOUT10_9.(her (DOUT11_9.(her (
		Ŀ≩									•
								0000 000 0 0000 0000			2.5us

- MB810 encoder implemented with hardwire only.
- Estimated gate number is under **20K** in VHDL simulation.
- The 10 to 1 MUX of 1.25Gbps used is the existing chip for GbE.
- The 10 to 1 MUX of 12.5Gbps needs to be implemented in a separate single chip for 10GbE.

Conclusion

- MB810 persisted healthy (no appreciable error) even with a LFP cutoff at 40% the line rate frequency. Needs only a 5GHz BW for 10GbE.
- Considerable reduction in channel bandwidth.
- A MB810 encoder was successfully implemented with FPGA, suggesting feasibility of high-speed and low-cost custom chips. ASIC implementation is underway.
- More facts about MB810 code including theoretical papers, programs, experimental results, and implementations are being updated at: http://routertech.etri.re.kr/English/Standard/ http://ccl.chungnam.ac.kr/LineCoding/.
- Hope our results could contribute to the success of 10GbE.