

# 25 Gb/s Study Group Architecture Ad Hoc Agenda for the August 12 meeting

Attendees names and affiliations will be taken from the Webex participants list. If your employer and affiliation are different, please send me an e-mail including the names of both.

## Proposed agenda:

- IEEE patent policy reminder
  - <http://www.ieee802.org/3/patent.html>
- Approval of the Agenda
- Presentations (see next slide)
- Discussion

# Presentations

Presentation	Presenter
25 Gb/s Ethernet over a single lane for server interconnect Study Group: status and work	Mark Nowell, Cisco
25 Gb/s Ethernet Architecture and Specification Re-use	Matt Brown, APM
25GbE Architecture Technical Feasibility	Mark Gustlin, Xilinx
Latency and FEC options for 25G Ethernet	Adee Ran, Intel
Technical Feasibility - Reuse of 802.3bj Channel Related Specifications	Chris Diminico, MC Communications/ PHY-SI LLC/Panduit