

Agenda and General Information

IEEE 802.3
400 Gb/s Ethernet Study Group

John D'Ambrosia, Dell
Geneva, Switzerland
IEEE 802 July 2013 Plenary

Agenda

- Appointment of Recording Secretary
- Welcome and Introductions
- Approve Agenda
- Goals for this meeting
- Reflector and Web
- Ground Rules
- IEEE
 - Structure
 - Bylaws and Rules
 - Guidelines for IEEE-SA Meetings
 - IEEE Standards Process
- Presentations
- Discussion, Straw Polls, Motions
- Closing Business
 - Liaisons and Communications
- Future Meetings

Task Force Decorum



- Photography or recording by permission only (2010 SASB Op Manual 5.3.3.4)
- Cell phone ringers off
- Press (i.e., anyone reporting publicly on this meeting) are to announce their presence (5.3.3.5)
- Wear your badges at all times in meeting areas
 - Help the hotel security staff improve the general security of the meeting rooms
 - **PCs HAVE BEEN STOLEN** at previous meetings
 - **DO NOT** assume that meeting areas are secure
- Please observe proper decorum in meetings

Goals for the meeting week

- Hear presentations (22) related to Objectives and 5 Criteria
- Develop consensus on PAR / Objectives
- Update on Time Sync Activities
- Potential liaison to TIA on single-mode connector return loss (see next slide).
- Lay the ground work for the next meeting

Reflector and Web

- To subscribe to the 400G reflector, send an email to:

ListServ@ieee.org

with the following in the body of the message (do not include “<>”):

*subscribe stds-802-3-400G <yourfirstname> <yourlastname>
end*

- Send 400G reflector messages to:

STDS-802-3-400G@listserv.ieee.org

- Task Force web page URL:

<http://www.ieee802.org/3/400GSG/index.html>

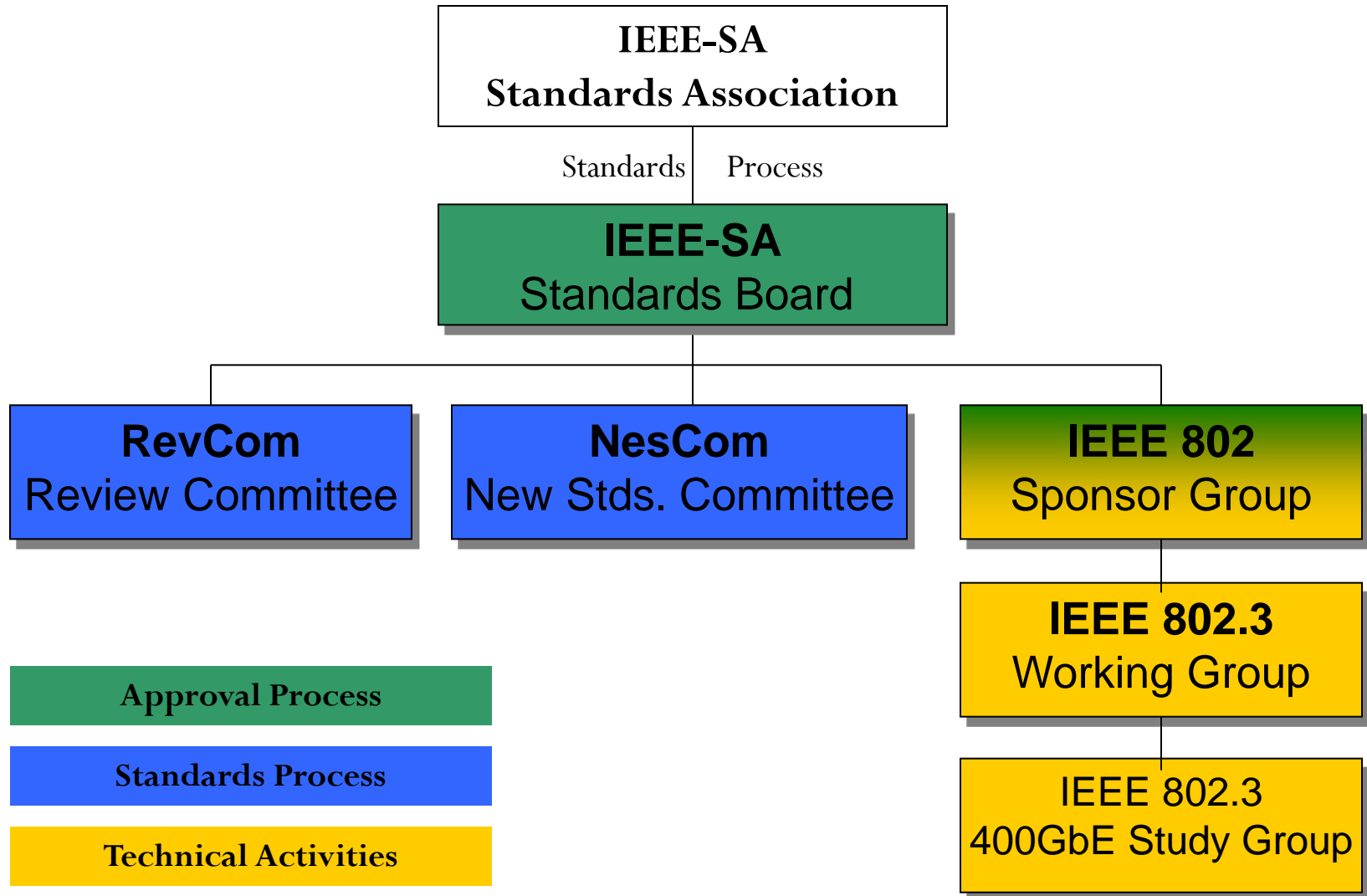
- Ad hoc area URL:

<http://www.ieee802.org/3/400GSG/public/adhoc/index.shtml>

Ground Rules

- Based upon IEEE 802.3 Rules
 - Foundation based upon Robert's Rules of Order
 - Anyone in the room may speak
 - Anyone in the room may vote
- **RESPECT**... give it, get it
- NO product pitches
- NO corporate pitches
- NO prices!!!
 - This includes costs, ASPs, etc. no matter what the currency
- NO restrictive notices

IEEE Structure



Important Bylaws, Rules, & References

- **IEEE-SA Operations Manual**
<http://standards.ieee.org/sa/sa-om.pdf>
- **IEEE-SA Standards Board Bylaws**
<http://standards.ieee.org/guides/bylaws/sb-bylaws.pdf>
- **IEEE-SA Standards Board Operations Manual**
<http://standards.ieee.org/guides/opman/sb-om.pdf>
- **IEEE 802 LAN/MAN Standards Committee (LMSC) Policies and Procedures**
<http://standards.ieee.org/about/sasb/audcom/pnp/LMSC.pdf>
- **IEEE 802 LAN/MAN Standards Committee (LMSC) Operations Manual**
http://www.ieee802.org/PNP/2010-07/IEEE_802_LMSC_OM_approved_100716.pdf
- **IEEE 802 LAN/MAN Standards Committee (LMSC) Working Group (WG) Policies and Procedures**
http://www.ieee802.org/PNP/2010-07/IEEE_802_LMSC_WG_PandP_approved_100716.pdf
- **IEEE 802.3 Working Group Operating Rules**
http://ieee802.org/3/rules/P802_3_rules.pdf
- **“Promoting Competition and Innovation: What You Need to Know about the IEEE Standards Association's Antitrust and Competition Policy”**
<http://standards.ieee.org/develop/policies/antitrust.pdf>

Guidelines for IEEE-SA Meetings

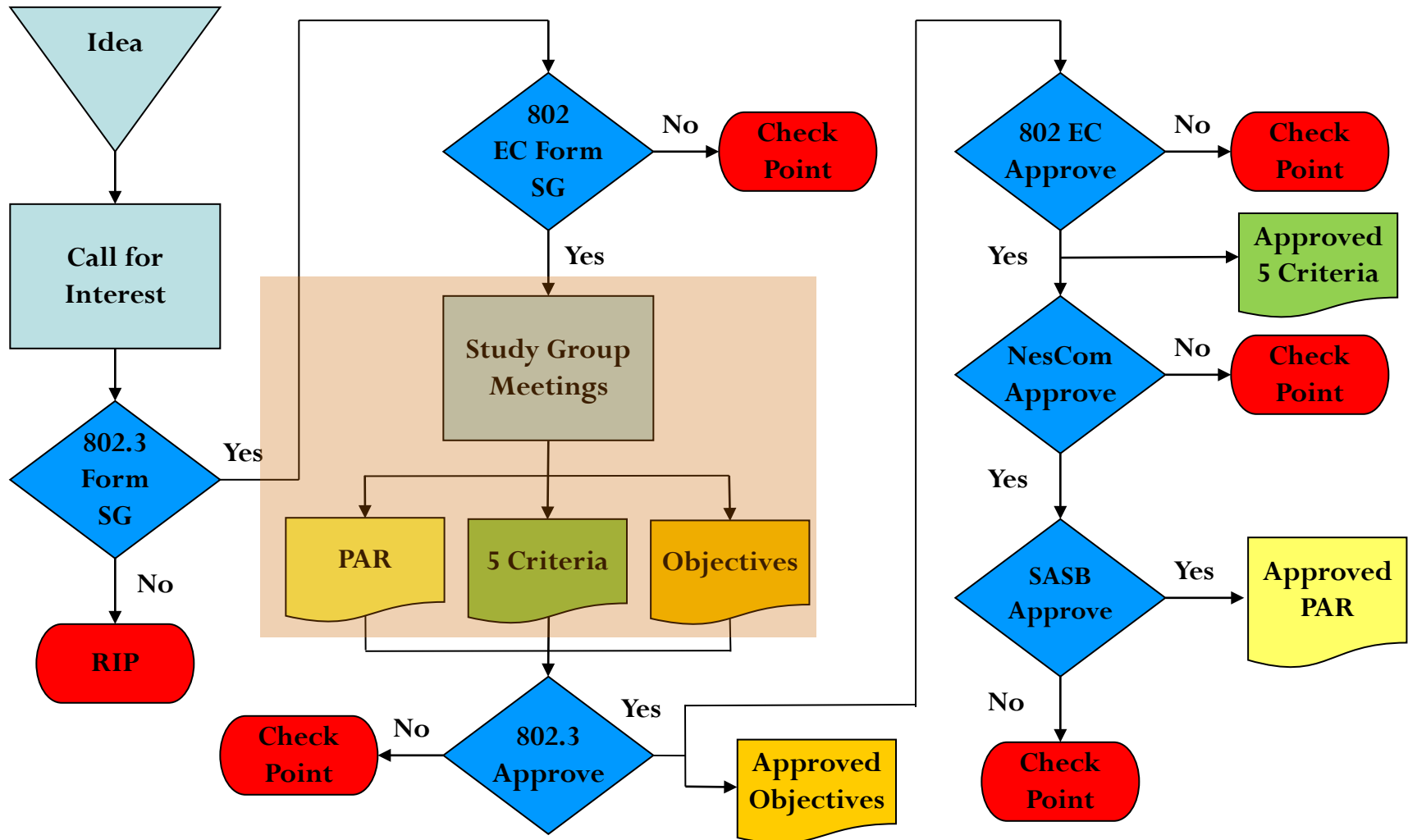
- All IEEE-SA standards meetings shall be conducted in compliance with all applicable laws, including antitrust and competition laws.
- Don't discuss the interpretation, validity, or essentiality of patents/patent claims.
- Don't discuss specific license rates, terms, or conditions.
 - Relative costs, including licensing costs of essential patent claims, of different technical approaches may be discussed in standards development meetings.
 - Technical considerations remain primary focus
- Don't discuss or engage in the fixing of product prices, allocation of customers, or division of sales markets.
- Don't discuss the status or substance of ongoing or threatened litigation.
- Don't be silent if inappropriate topics are discussed... do formally object.

If you have questions, contact the IEEE-SA Standards Board Patent Committee Administrator at patcom@ieee.org or visit <http://standards.ieee.org/about/sasb/patcom/index.html>

See *IEEE-SA Standards Board Operations Manual*, clause 5.3.10 and "Promoting Competition and Innovation: What You Need to Know about the IEEE Standards Association's Antitrust and Competition Policy" for more details.

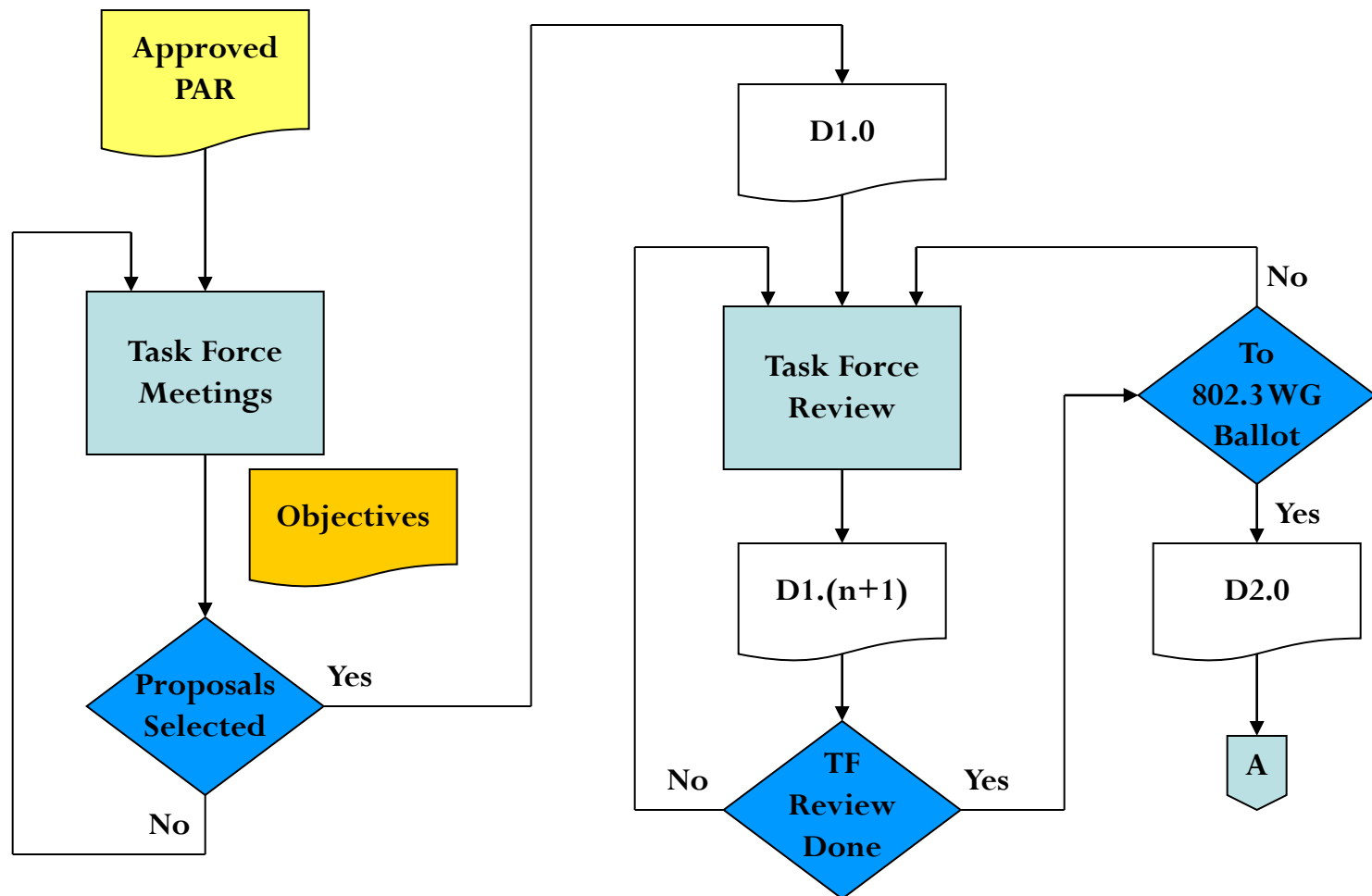
This slide set is available
at <https://development.standards.ieee.org/myproject/Public/mytools/mob/slideset.ppt>

Overview of IEEE 802.3 Standards Process (1/5)- Study Group Phase

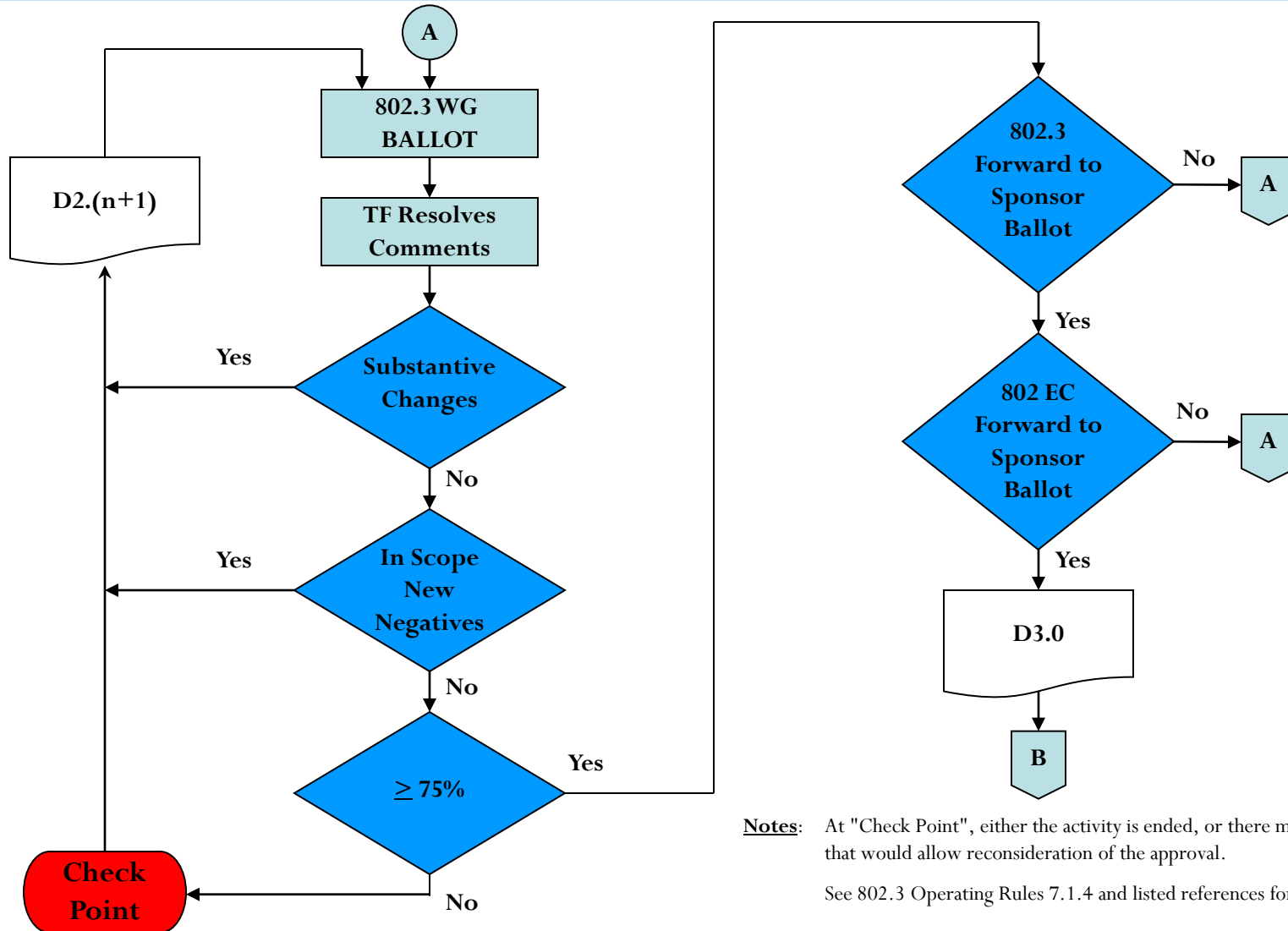


Note: At "Check Point", either the activity is ended, or there may be various options that would allow reconsideration of the approval.

Overview of IEEE 802.3 Standards Process (2/5) – Task Force Comment Phase



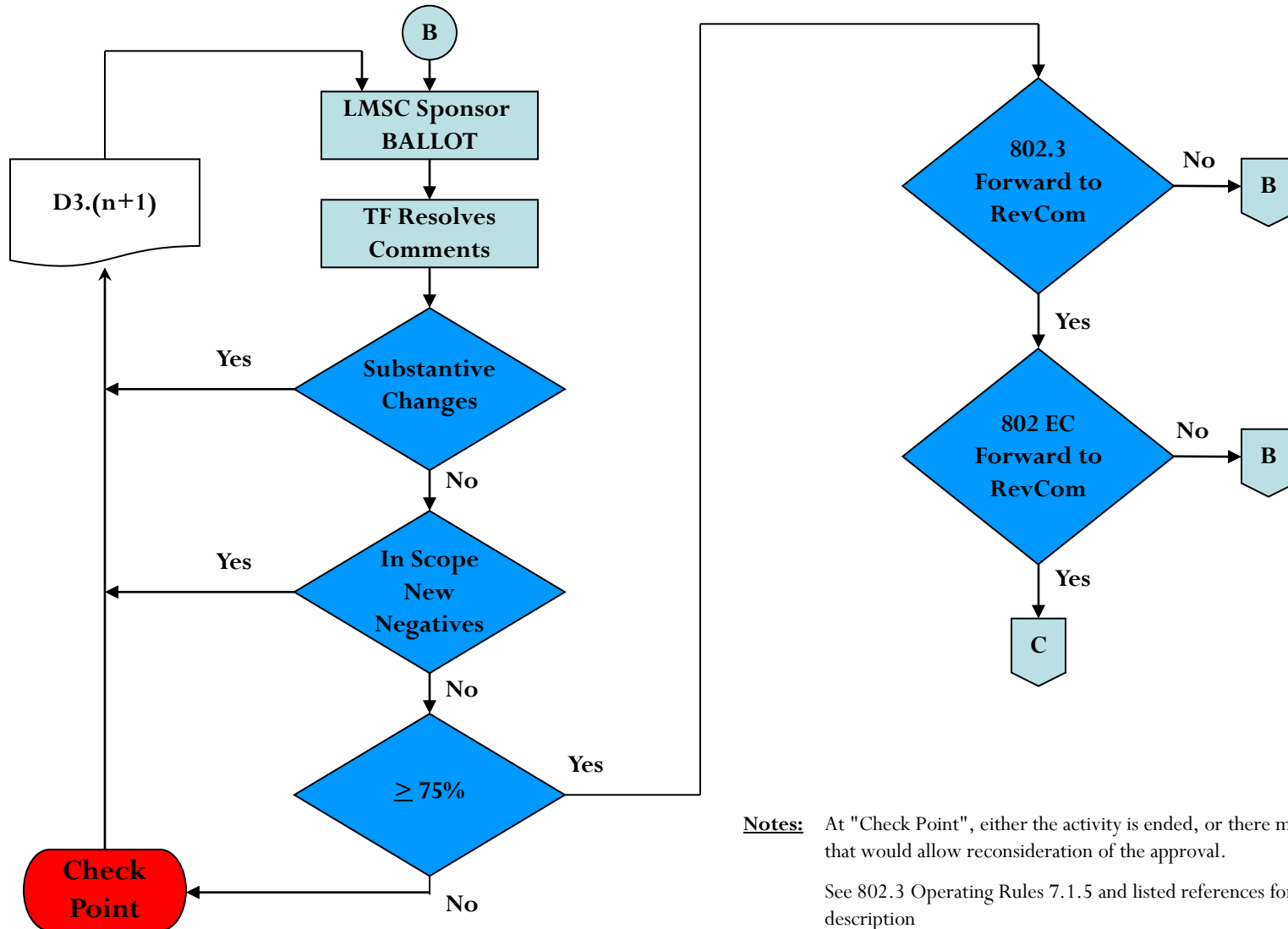
Overview of IEEE 802.3 Standards Process (3/5) – Working Group Ballot Phase



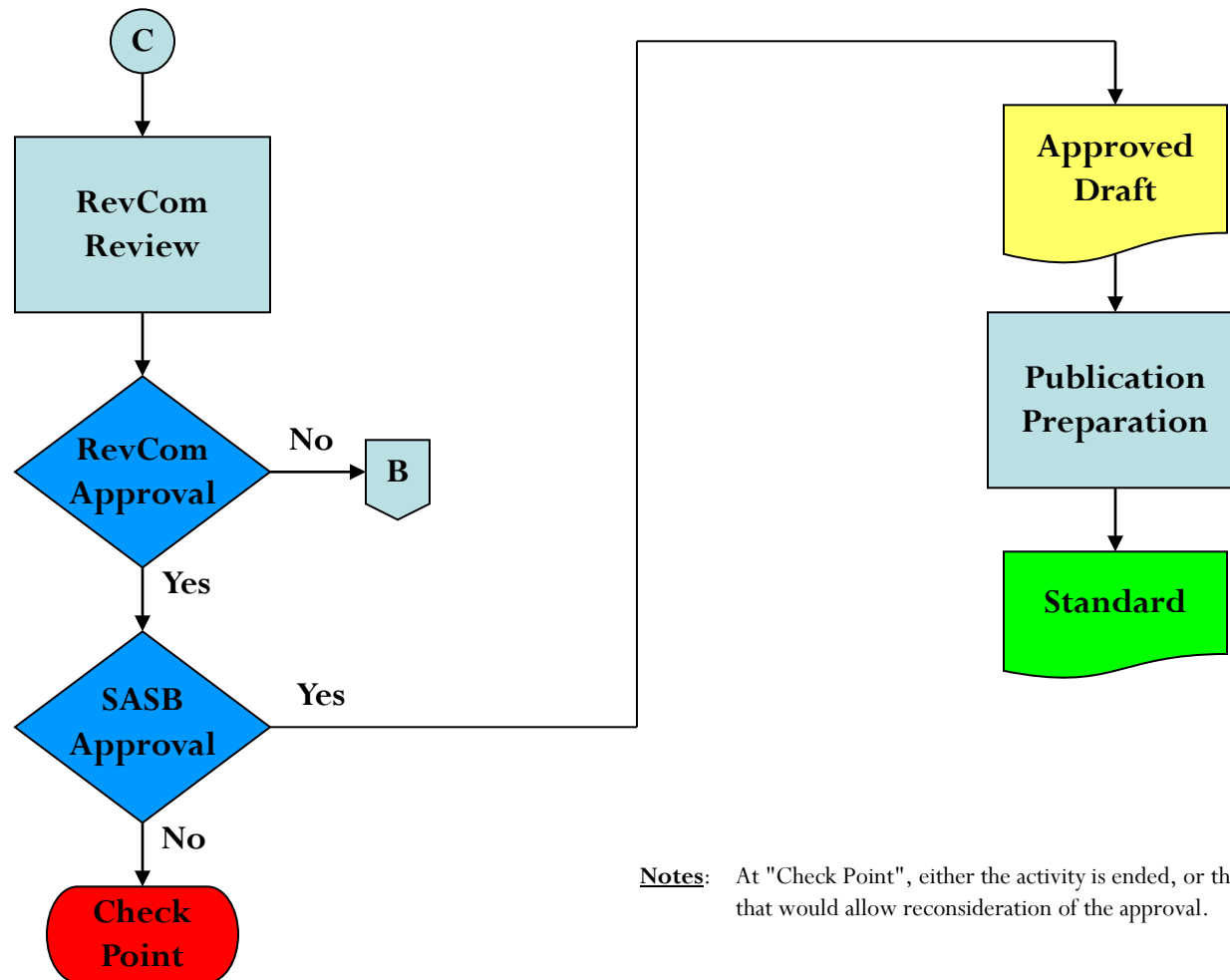
Notes: At "Check Point", either the activity is ended, or there may be various options that would allow reconsideration of the approval.

See 802.3 Operating Rules 7.1.4 and listed references for complete description

Overview of IEEE 802.3 Standards Process (4/5)- Sponsor Ballot Phase



Overview of IEEE 802.3 Standards Process (5/5) – Final Approvals / Standard Release

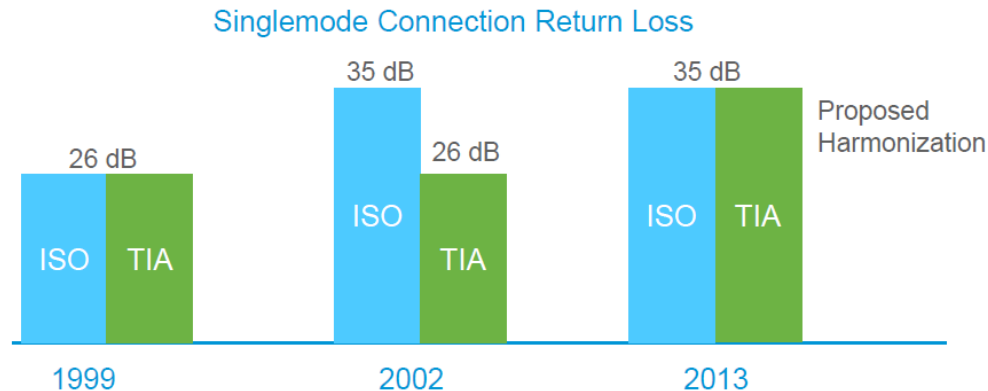


Notes: At "Check Point", either the activity is ended, or there may be various options that would allow reconsideration of the approval.

Liaisons and Communications

- None received
- A liaison to TIA has been proposed to address “Harmonizing Singlemode Connection Return Loss.”
 - Reference [bhatt_400_01_0713.pdf](#)

Harmonizing Singlemode Connection Return Loss



- There is a need to harmonize single-mode connection return loss spec of TIA (26 dB) with that of ISO/IEC (35 dB).
- For 400 Gb/s and higher Ethernet rates, MPI Noise resulting from Return Loss will be a key source of performance impairment.
- The state of the art today comfortably achieves 35+ dB.
- Therefore, it is prudent for us to take the initiative of recommending to the TIA to consider such harmonization.
- Reference: [bhatt_400_01_0713.pdf](#).

Tuesday

Time	Presenter	Affiliation	Title	File Name
8:30 AM	John D'Ambrosia	Dell	Agenda and General Information	agenda_400_01_0713.pdf
9:00 AM	Mark Gustlin	Xilinx	Logic Ad hoc Update	gustlin_400_01_0713.pdf
9:15 AM	Vipul Bhatt	Cisco	Harmonizing Connector Return Loss	bhatt_400_01_0713.pdf
9:45 AM	Arien Vijn	AMS-IX	End user perspective on 400Gb/s Ethernet	vijn_400_01_0713.pdf
10:15 AM	Break			
10:30 AM	Randy Rannow	APIC	400G: Physics, Economics, & Business	rannow_400_01_0713.pdf
11:00 AM	Tom Palkert	Luxtera	Requirements for Broad Market Potential	palkert_400_01_0713.pdf
11:30 AM	Lunch			
1:00 PM	Song Xiaolu	Huawei	Perspective and Scenario of Core Router on 400GbE	song_400_01_0713.pdf
1:30 PM	David Ofelt	Juniper	BER Objective for 400GE	ofelt_400_01_0713.pdf
2:00 PM	Steve Trowbridge	Alcatel-Lucent	OTN Support: What is it why is it important?	trowbridge_400_01_0713.pdf
2:30 PM	Zhao Wenyu	China Academy of Telecom. Research (CATR), MIIT	400GE SMF considerations	zhao_400_01_0713.pdf
3:00 PM	Break			
3:15 PM	Riu Hirai	Hitachi	Initial thought about Modulation format &	hirai_400_01_0713.pdf
3:45 PM	Toshiki Tanaka	Fujitsu	Considerations on transmission distance of	takahara_400_01_0713.pdf
4:15 PM	Wael Diab	Broadcom	EEE Overview and Proposal for an Objective	diab_400_01_0713.pdf
4:45 PM	Jeff Maki	Juniper	Long Shelf-Life Electrical Interfaces	maki_400_01_0713.pdf
5:15 PM	Jack Jewell	Independent / CommScope	MMF Capabilities for 400-Gigabit Ethernet, and Beyond	jewell_400_01_0713.pdf
5:45 PM	Break for Day			

Note –Times listed are subject to change.

Wednesday

8:30 AM	John D'Ambrosia	Dell	Day's Opening Comments	
8:40 AM	Ali Ghiasi	Broadcom	Scalable 400 GbE Aritecture	ghiasi_400_01_0713.pdf
9:10 AM	Mark Gustlin	Xilinx	400 Gb/s PCS architectural options	gustlin_400_02_0713.pdf
9:40 AM	zhongfeng Wang	<u>Broadcom</u>	400GbE Lane Configurations vs. FEC Options	wang_400_01_0713.pdf
10:10 AM	Break			
10:25 AM	Gary Nichol	Cisco	Musings on FEC Objective for 400 Gb/s Ethernet	nichol_400_01_0713.pdf
11:00 AM	Michael Teener	Broadcom	Time Synch	teener_400_01_0713.pdf
12:00 PM	Lunch			
1:30 PM	Suping Zhai	Huawei	The Requirement Analysis of 400GE FEC for Gen1 PMDs	zhai_400_01_0713.pdf
2:00 PM	John D'Ambrosia	Dell	Architecture and FEC	dambrosia_400_02_0713.pdf
2:30 PM	Brad Booth	Independent	PSM in the Data Centre Survey	flatman_400_01_0713.pdf
3:00 PM	Break			
3:15 PM	Brian Holden	Kandou Bus	An exploration of the technical feasibility of the major technology options for 400GE backplanes	holden_400_01_0713.pdf
3:45 PM	Nathan Tracy	TE Connectivity	Backplane Channels Using Embedded Capacitor Connectors for Data Rates 25Gbps or higher	tracy_400_01_0713.pdf
4:15 PM	David Law	HP	Proposed PAR	dambrosia_400_01_0713.pdf
4:45 PM	Discussion & Strawpolls			
6:00 PM	Break for Day			

Note –Times listed are subject to change.

Thursday

9:00 AM	John D'Ambrosia	Dell	Day's Opening Comments	
9:10 AM	Discussion, Strawpolls Motions			
10:30 AM	Break			
10:40 AM	Discussion, Strawpolls Motions			
11:40 AM	Closing Business			
12:00 PM	Adjourn			

Note –Times listed are subject to change.

Future Meetings

- See: <http://www.ieee802.org/3/interims/index.html>
- Sept 2013 Interim (hosted by ADVA)
 - Week of Sept 2
 - York, UK
- Nov 2013 Plenary
 - Week of Nov 10
 - Hyatt Regency, Dallas, TX, USA
- January 2014 Interim
 - Week of January 20
 - Hyatt Regency Indian Wells, CA
- Anyone interested in hosting a meeting contact me.

Thank You!