Suggested Remedy for comments 133, 84, 244, 230

1. Change Table 33-11 item 4 as follows:

|  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- |
| Item | Parameter | Symbol | Unit | Min | Max | PSE Type | Additional Information |
| 4 | Continuous output current capability in POWER\_ON state over both pairsets | Icon | A | Pclass/Vport\_PSE-2P | 1,2,3,4 |  | See 33.2.7.4 |
|  |  |  |  |
|  |  |  |  |
| 4a | Pair current due to E2ERunb within E2ERunb range | ICont-2P-unb | A |  | 0.668 | 3 | See 33.2.7.4a |
|  | 0.931 | 4 |

2. Delete comment 1 below table 33-11.

3. Make the following changes to 33.2.7.4:

33.2.7.4 Continuous output current capability in the POWER\_ON state

***Change the text of 33.2.7.4 as follows:***

For Type 3 and Type 4 PSEs, ICon as specified in Table 33-11 shall be met. During normal POWER ON state, the maximum pairset current shall not exceed ICon-2P\_UNB as specified by Table 33-11 item 4a. ICon is the total current of both pairs with the same polarity that a PSE has to support. ICon-2P\_unb is the maximum current over one of the pairs of the same polarity under E2EP2PRunb conditions at normal operating power mode (i.e. not during overload or short load conditions that are covered by Icut-2P and ILIM-2P). In addition to IConas specified in Table 33–11, the PSE shall support the following AC current waveform parameters perpairset, while within the operating voltage range of VPort\_PSE-2P:

4. Make the following changes to 33.2.7.4a:

33.2.7.4a PSE PI Pair-to-Ppair-to-pairair resistance and current unbalance

Type 3 and Type 4 PSEs operating over 4-pair are subject to unbalance requirements in this section. The contribution of PSE PI pair to pair effective resistance unbalance (PSE\_P2PRunb) to the whole effective system end to end resistance unbalance (E2EP2PRunb), is specified by PSE maximum (RPair\_max) and minimum (RPair\_min) common mode effective resistance in the powered pairs of same polarity. The PSE\_P2PRunb determined by RPair\_max and RPair\_min ensures that along with any other parts of the system - i.e. channel (cables and connectors) and the PD, the maximum pair current due to E2EP2PRunb, is not exceeding Icon-2P-unb as defined in Table 33–11 during normal powering operating conditions..

The following is the Table after addressing other additional comment: To be discussed later.

|  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- |
| Item | Parameter | Symbol | Unit | Min | Max | PSE Type | Additional Information |
| 4 | Continuous output current capability in POWER\_ON state over both pairsets | Icon | A | Pclass/Vport\_PSE-2P |  | 1,2,3,4 | See 33.2.7.4, 33.2.7.4a |
| 4a | Pair set current due to E2ERunb within E2ERunb range | Icon-2P-unb | A |  | TBD | 3 | Applicable for Class 5.  See 33.2.7.4a |
| 0.668 | 3 | Applicable for Class 6.  See 33.2.7.4a |
| TBD | 4 | Applicable for Class 7.  See 33.2.7.4a |
| 0.931 | 4 | Applicable for Class 8.  See 33.2.7.4a |