

802.3BT 4P-ID AD HOC APPROVED AGENDA & MINUTES  
12 May 2015

Participants are encouraged to review IEEE meeting guidelines available at the following URL -  
<https://development.standards.ieee.org/myproject/Public/mytools/mob/preparslides.pdf>

The approved agenda for the meeting follows.  
approved agenda George started shorter posted presentation

**8:06 AM** Pacific Time meeting start (60 minute meeting planned)

Meeting opened 8:06:

Chris Bullock, Cisco  
Dave Dwelley, LTC  
Jeff Heath, LTC  
David Abramson, TI  
Lennart Yseboodt, Phillips  
Miklos Lukacs, SiLabs  
Yair Darshan, Microsemi  
Ken Bennett, Sifos  
Dylan Walker, Cisco  
George Zimmerman, CME Consulting / LTC (Chair)  
Chad Jones, Cisco (802.3bt Chair), left shortly into the call

1. Roll call : Please send an email indicating your attendance, employer and affiliation to  
<mailto:george@cmeconsulting.onmicrosoft.com?subject=802.3bt%254PID%20ad%20hoc%20attendance%2012May15>
2. Reminder of IEEE patent policy  
[www.ieee802.org/3/patent.html](http://www.ieee802.org/3/patent.html)  
Call for patents was made, there were no responses.
3. Housekeeping  
Agenda was approved as below, without objection or changes, no prior minutes to approve
4. Old business from previous ad hoc meetings:  
None
5. New business at this meeting:

Name of presenter: George Zimmerman, CME Consulting /LTC  
Title of presentation: 4PID – Concepts for Baseline Text (zimmerman\_3btah\_01\_0515.pdf)  
Abstract: Discusses concepts and issue to be resolved to present baseline text.

(Dependent on discussion, a follow up presentation with baseline text was offered, but discussion focused on building consensus on the concepts, and the second presentation was withdrawn)

Discussion:

Slide 3: Yair suggested “invalid connection check” to avoid confusion with invalid detection - George agreed

Slide 4: Yair asked “if x is always true, why do we need connection check” - not answered

Slide 6 invited alternate proposals: (see updated presentation zimmerman\_3btah\_01a\_0515.pdf)

Yair suggested a proposal with LLDP and “additional information” included for X.

George proposed a revised version of Yair’s proposal Dave D pointed out that LLDP won’t work as part of a physical layer test since power is not yet applied in most cases.

Chris B asked Yair to elaborate about a proposed management bit.

George suggested the following proposal 3, which gathered no consensus.

Proposal 3:

$X = (!\text{Deny\_dual\_sig\_bit\_set}) * [ (\text{class\_power\_OK}) + (\text{vendor override}) (+ \text{LLDP\_override implied}) ]$

Based on discussion between Yair and Dave, George suggested the mechanism below, where there was a physical layer 4PID, which was not the final decision to maintain 4 pair power, but that it was OK to initially provide 4 pair power for at least a limited time. A decision to maintain 4 pair power might be made after power had been applied and, for example, LLDP had come up.

George then codified this into proposal 4, which seemed to be the basis of some consensus:

Proposal 4: [seems we have something workable]

4PID – physical layer (no LLDP) :  $x = \text{TRUE}$  , alternatively,  $x = !\text{Deny\_dual\_sig\_4P\_Power}$ .

$\text{Maintain\_4P\_power (beyond time TBD)} = (\text{4PID} = \text{TRUE}) * (\text{LLDP} \neq \text{FALSE}) + (\text{LLDP} = \text{TRUE}) * [ \text{Class\_power\_OK} ]$

NOTE: if Maintain\_4P\_power is false, then power must be removed from at least one pair set.  
x may be disabled by vendor-discretionary bit.

The group then discussed the question of whether we need a standard register bit ?

Chris stated that if we do not have the bit in the standard, we then need automatic tests, and we may need 2 tests (PSE must do one or the other) which substitute for it.

There was consensus that we should make best efforts to make it implementation independent.

Having reached some consensus, the following action items remained:

George Zimmerman: Write up baseline text around the new proposal 4 and circulate.

Dave Dwelley: Send to reflector equations and thoughts around the need (or lack of need) for a register bit.

Yair Darshan: Consider what the time “TBD” we might use for the maintain power decision. (George was to provide Yair with some 10GBASE-T time-to-link data presented by Pete Cibula of Intel – the most up to date of this can be found at:

[http://www.ieee802.org/3/NGEBASET/public/archadhoc/cibula\\_3bz\\_ArchAdHoc01\\_0515.pdf](http://www.ieee802.org/3/NGEBASET/public/archadhoc/cibula_3bz_ArchAdHoc01_0515.pdf)

6. Next meeting time: None selected

7. Adjournment: 9:20AM Pacific Time