

## Comment (#321 AND #164, #88, #396):

#164 (Yair) #88 (Ken) : 145.3.8.2 , Page 184, L11.

#396 (Lennart) : 145.3.8 Page 182, L1.

In the text "PClass\_PD and PClass\_PD-2P in Table 145–28 are determined per the assigned Class. PClass\_PD values for each Class are shown in Table 145–24, PClass\_PD-2P values for each Class are shown in Table 145–25."

PClass\_PD and PClass\_PD-2P are not defined in Table 145-28.

- (1) Ken in #88 suggest to restore them back to Table 145-28
- (2) Lennart in #396 suggest to restore them back to Table 145-28
- (3) In addition some information regarding the conditions that PClass\_PD and PClass\_PD-2P should be met are missing and are in 145.3.8.10.

This is about PPort\_PD, not PClass\_PD.

#321 (Lennart) : 145.3.8.10, Page 191, L20.

In the text: "Under all operating states, dual-signature PDs shall not exceed I Con-2P as defined in Equation (145-8) for longer than T CUT-2P min as defined in Table 145-16 on any pair when PD PI pairs of the same polarity are connected to all possible common source voltage in the range of V Port\_PSE-2P through two common mode resistances, Rsource\_min and R source\_max, as defined in Equation (145-32) and shown in Figure 145-34."

This is a troublesome statement for a few reasons:

- dual-sig PDs are already required not to exceed PClass\_PD-2P (which equates to I con-2P) under any circumstance.
- I con-2P is a PSE parameter, unknowable to the PD.

-Yair: If you test it under the conditions of 145.3.8.10 i.e. with a voltage source and Rsource that the current will be I con-2P which is a common parameter for the PD and PSE due to the fact that it is current – the same current:  $I_{con-2P} = P_{class} / V_{pse} = P_{class\_PD} / V_{pd}$  + the conditions of 145.3.8.10.

-Lennart looking at your remedy options;

Option1: Is no good. In this case the conditions to meet I con-2P are not specified.

Option 3: Is no good. Missing "shall" for the conditions to meet I con-2P.

Option 3:

-Yes, you can replace it with  $P_{class\_PD} / V_{pd}$  but not all relevant parameters are specified in Table 124-25 e.g. Vpd.

- PClass\_PD-2P / VPD is accurate but kind of odd description It is better  $I_{con-2P} = \{P_{class\_PD-2P} / VPD\}_A$

- What this really tries to do is qualify that PClass\_PD-2P shall to only apply to PDs connected to a channel with acceptable unbalance. Yair: Correct.

## Proposed Remedy:

[Baseline starts here]

1. Re-instate PPort\_PD and PPort\_PD-2P as they were in D2.2 OK

2. Make the following changes below.

### 145.3.8.2 Input average power

PClass\_PD and PClass\_PD-2P in Table 145–28 are determined per the assigned Class. PClass\_PD values for each Class are shown in Table 145–24, PClass\_PD-2P values for each Class are shown in Table 145–25. The assigned PSE Class is determined by the number of class events and the requested Class by the PD, as shown in Table 145–11. PClass\_PD is the maximum average PI power and applies to single-signature PDs. PClass\_PD-2P is the maximum average power on a pairset and applies to dual-signature PDs.

The maximum average power, PClass\_PD or PClass\_PD-2P in Table 145–24, Table 145–25, and Table 145–28 or PDMaxPowerValue in 145.5.3.3, is calculated over a 1 second interval under the conditions specified in 145.3.8.10. PDs may dynamically adjust their maximum required operating power below PClass\_PD or PClass\_PD-2P as described in 145.5. PDs may also adjust their maximum required operating power below PClass\_PD or PClass\_PD-2P by using Autoclass (see

145.3.6.2). PDs that have successfully completed DLL classification, shall not exceed a power consumption of PDMaxPowerValue as defined in 145.5.3.3.

### 145.3.8.10 PD pair-to-pair current unbalance

Under all operating states, single-signature PDs shall not.....

Under all operating states, dual-signature PDs shall not exceed ~~I<sub>Con-2P</sub>~~  $I_{con-2P} = \left\{ \frac{P_{Class\_PD-2P}}{V_{pd}} \right\}_A$  as defined in [Table 145-](#)

[25 Equation \(145-8\)](#) for longer than TCUT-2P min as defined in Table 145-16 on any pair when PD PI pairs of the same polarity are connected to all possible common source voltage in the range of VPort\_PSE-2P through two common mode resistances, Rsource\_min and Rsource\_max, as defined in Equation (145-32) and shown in Figure 145-34.

**[Lennart: We can move down the new equation and add "where" list and make it nicer to read.]**

**[Baseline ends here]**

I'm doing this in my baseline already, remove this to avoid clashing baseline.