LLDP & Beyond 71.3 W

79.3.2.5 PD requested power value

Add the following sentence at the end of the paragraph:

A value higher than 713 requires the PSE to support a power level higher than P_{Class_PD} at the PD PI. See 145.2.8 and 145.3.8.2.1.

79.3.2.6 PSE allocated power value

Add the following sentence at the end of the paragraph:

A value higher than 713 indicates that the PSE is capable of supporting a power level beyond P_{Class_PD} at the PD PI. This may require an output power level higher than P_{Type} min. See 145.2.8.

145.5.3.2.2 Variables

Add the following sentence at the end of the description of PSEAllocatedPowerValue:

A value higher than 713 indicates that the PSE is capable of supporting a power level beyond P_{Class_PD} at the PD PI. This may require an output power level higher than P_{Type} min.

145.5.3.3.1 Variables

Add the following sentence at the end of the description of PDMaxPowerValue:

See 145.3.8.2.1 for values higher than 713.

Add the following sentence at the end of the description of PDRequestedPowerValue:

A value higher than 713 requires the PSE to support a power level higher than P_{Class_PD} at the PD PI.

Make similar changes to the equivalent dual-signature fields.

> **Info (not part of baseline)**
>
> Note to self: make DLAW comment changes first (add new variables to dual0-sig).

145.5.4 Power requests and allocations

Add to the end of this subclause:

Power requests and allocations greater than 713 (for single-signature PDs) or greater than 356 (for dual-signature PDs) are only used after the PSE and PD are in sync using the initial values that are assigned in INITIALIZE, as defined Figure 145–38 through Figure 145–45.