PSE SD

PSE SD

<i>Cl</i> 33 Stover, D	33 SC 33.1.3 P 44 L 1 # [492] over, David Linear Technology						RChan-2P is the actual DC loop resistance of a pairset from the viewpoint of the PSE PI and the PD PI. Rchan-2P has a maximum value of RCh.									
Commen	nt Type T	Comment Status D			Cabling	C/ 33	SC	33.2.5.11		P 83	L 6	#	26			
The	text carefully distin	iguishes between DC loop i	resistance and I	DC pair loo	p resistance,	Picard, Je	ean		٦	Texas Instru	ments					
statii	ng this clause uses	s only DC pair loop resistan	ce.			Comment	Туре	TR	Comment St	atus D			PSE			
Furth actua	nermore the resista ally the round trip p	ance is described as the pa bath.	th from the PSE	E PI to the	PD PI. It is	Using stagg	one u ered de	nique PD_4	4pair_cand varia	able can hel	p simplify the sta	ate diagrar	n, even if			
Ther	the text refers to	the wrong one				Suggeste	dReme	dy								
"The	"The cable references use "DC loop resistance," which refers to a single conductor. This							Replace "PD_4pair_cand_pri <= TRUE" with "PD_4pair_cand <= TRUE" Replace "PD_4pair_cand_pri <= FALSE" with "PD_4pair_cand <= FALSE"								
clause uses "DC pair loop resistance," which refers to a pair of conductors in parallel. Therefore, RCh is related to, but not equivalent to, the "DC loop resistance" called out in the cable references.						Proposed Response Response Status W										
						TFTD	CB									
RCh	an is the actual DC	C loop resistance between t	he PI of the PS	E and the I	PI of the PD.	C/ 33	SC	33.2.5.11		P 85	L 6	#	27			
RChan has a maximum value of RCh/2 when operating in 4-pair mode.						Picard, Je	ean		Г	Fexas Instrur	ments					
RCh	f the PSE PI	Comment	Туре	TR	Comment St	atus X			PSE							
and	and the PD PI. RChan-2P has a maximum value of RCh.") One u ered de	nique PD_4	4pair_cand varia	able can hel	p simplify the sta	ate diagrar	n, even if			
Char	nde					Suggeste	dReme	dy								
RCh	Change RChan is the actual DC loop resistance between the PI of the PSE and the PI of the PD.			PI of the PD.	Repla Repla	ace "PD ace "PD	_4pair_car _4pair_car	nd_sec <= TRU nd_sec <= FAL	IE" with "PD SE" with "PI	_4pair_cand <= D_4pair_cand <	≟ TRUE" ≔ FALSE"					
RCh RCh and	an has a maximun an-2P is the actua the PD PI.	n value of RCh/2 when open I DC loop resistance of a pa	rating in 4-pair r airset from the v	node. viewpoint o	f the PSE PI	Proposed TFTD	l Respo) CB	nse	Response Sta	atus W						
RCh	an-2P has a maxir	num value of RCh.				Sec)e									
to						See 2	20									
						C/ 33	SC	33.2.5.12		P 86	L 22	#	254			
RCh PD a	an is the actual DC	C loop pair resistance betwe E PL RChan has a maximu	een the PI of the	PSE and	the PI of the	Darshan,	Yair		Ν	Aicrosemi						
pair	mode.					Comment	Туре	TR	Comment St	atus X		I	Pres: Darsha			
RCh PL ar	an-2P is the actua	I DC loop pair resistance of	a pairset from	the viewpo	int of the PSE	The F	PSE sta	te machine	e part for single	signature wh	hen it needs to k	now class	code by			
RCh	an-2P has a maxir	num value of RCh.				to gei	nerate d	only one fin	iger etc.							
Propose	d Response	Response Status W				This i	s cover	ed by the t	ext but not in th	e state mach	hine.					
PRO	POSED ACCEPT					Suggeste	dReme	dy								
тсти						Add t	he miss	sing state n	nachine part in o	darshan_08_	_0916.pdf.					
1611	DFSLI					Proposed	Respo	nse	Response Sta	atus W						
RCh	an is the actual DC	C loop resistance between t	he PI of the PS	I of the PSE and the PI of the PD		TFTD)									
and mod	back to the PSE P e.	PI. RChan has a maximum value of RCh		when operating in 4-pair		WFP										
TYPE: T	R/technical require NT STATUS: D/dis	ed ER/editorial required GF spatched A/accepted R/rej	R/general requir	ed T/techi ONSE STA	nical E/editorial G/ ATUS: O/open W/w	general vritten C/close	d U/un	satisfied Z	Z/withdrawn	Pa 8 Li 2	6 2	F	Page 1 of 9 9/15/2016 7:			

SORT ORDER: Page, Line

Page 1 of 9 9/15/2016 7:05:30 PM

Pres: Darshan8

C/ 33 SC 33.2.6.7 P 94 L 28 # 290 Schindler, Fred Seen Simply, Broadco # 290 #	C/ 33 SC 33.2.7.2 P 98 L 29 # 40 Wendt, Matthias Philips Lighting							
Comment Type TR Comment Status X 4PID	Comment Type T Comment Status D Pres: Yseboodt7							
This section covers what establishes PD_4pair_cand. The state diagrams Figures 33-16, and 33-17 may do this as well, but they do not match. These diagrams do use the variable and xxx_pri and xxx_sec. The single-signature state diagram Figure 33-15 does not use PD_4pair_cand. Nothing in the state diagrams establishes pd_4pair_cand for certain.	If during autoclass a PD changes its class signature to something other than '0' during TACS behavior is undefined as already pinpointed in yseboodt_03_0716_class. It would be beneficial to define this for future use.							
SuggestedRemedy	SuggestedRemedy							
See related comment marked COMMENT-3 for a solution.	adopt yseboodt_03_0716_class							
Proposed Response Response Status W TFTD CB	Proposed Response Response Status W PROPOSED ACCEPT.							
Need to align pd_4pair_cand with pd_4pair_cand_pri and _sec.	TFTD FS LY DS							
C/ 33 SC 33.2.7 P 96 L 43 # 407 Yseboodt, Lennart Philips	C/ 33 SC 33.2.8 P 104 L 49 # 510 Stover, David Linear Technology							
Comment Type TR Comment Status X Pres: Yseboodt5 Unlike Type 2, Type 3 and Type 4 devices have a lot of parameters that are different depending on the Assigned Class. An initial assigned class is set up during Physical Layer classification. Pres: Yseboodt5	Comment Type T Comment Status X Unbalance Intra-pair current unbalance I_unb is specified as 3% I_Peak for Type 2, 3, and 4 PSEs. For higher Class PDs, this may preclude low-speed data implementations due to higher inductance requirements on those magnetics Image: Class PDs and the preclude low speed data implementations due to higher inductance requirements on those magnetics							
Using DLL the PD and PSE are able to change the allocated power. It makes sense that the assigned Class 'follows' the PSEAllocatedPower variable.	SuggestedRemedy TFTD. Especially looking for opinions from magnetics vendors here.							
SuggestedRemedy	Proposed Response Response Status W							
Adopt yseboodt_05_0916_dllclasschange.pdf	TFTD							
Proposed Response Response Status W TFTD								

WFP

Pa **104** Li **49**

C/ 33 SC 33.2.8.2 Picard, Jean	P 105 Texas Instrume	L 51 nts	# 28	<i>Cl</i> 33 Darshan, Yai	SC 33.2.8.5 r	P 109 Microsemi	L 43	# 249
Picard, Jean Comment Type TR To ensure acceptable s circumstances longer th SuggestedRemedy Add the following note a "PSE should avoid caus steps with the exception ensure system robustne Proposed Response PROPOSED ACCEPT I Add the following note a "PSE should avoid caus	Texas Instrument <i>Comment Status</i> D teady-state operating condition teady-state operating condition at the end of 33.2.8.2. sing such long duration (> 2500 of rare circumstances involving teas." <i>Response Status</i> W IN PRINCIPLE. at the end of 33.2.8.2. sing such long duration (> 2500	nts ns, we need to cant voltage ste us) transients o ng switchover o us) transients o	PSE Power explain in which eps may be expected. or significant voltage of power supplies to	Darshan, Yai Comment Ty (This is in darshan_ same. Th places to Equation http://ww the straw See upda SuggestedRe Addopt d Proposed Re	r pe TR dentical comm 02_0916.pdf ne only differe be consisten 33-15 can be w.ieee802.org poll in last m ated version o pemedy arshan_02_07 sponse	Microsemi Comment Status D nent to other one that I sent. He insted darshan_01_0716.pdf fro nces are in the Annex where "In t with the rest of the document.) e simplified per the work done in g/3/bt/public/jul16/darshan_01_0 eeting to be used in D2.0. f it (baseline was not changed) 916.pdf for D2.0. Response Status W	re I have upd om July which n' was chang) 0716.pdf and in darshan_0	Pres: Darshan2 ated the file to h its base line is the es to "Imax" in few was accepted according 2_0916.pdf.
Steps with the exception supplies to ensure syste TFTD YD C/ 33 SC 33.2.8.4 Stover, David	n of rare circumstances such a em robustness." <i>P</i> 108 Linear Technolo	s those involvi	# 512	PROPOS TFTD LY WFP C/ 33	SED ACCEPT DS SC 33.2.8.8	P 114	L 44	# 441
Comment Type ER "P_Peak_PD-2P is the f anywhere (captured in a SuggestedRemedy Correct reference to Tal Proposed Response PROPOSED ACCEPT I Adopt changes in darsh TFTD DS YD	Comment Status D total peak power see Table 3 another comment), but if it were ble 33-28. <i>Response Status</i> W IN PRINCIPLE. an_16_0916.pdf	33-25". P_Peal e, it would live	Editorial <_PD-2P is not defined in Table 33-28.	Yseboodt, Le Comment Ty, "The PSI below V Or in the SuggestedRe "The PSI the pairs Proposed Re PROPOS Remove TFTD CE	Innart pe T E remains in t Off max." DISABLED s <i>emedy</i> E remains in t et is below V (<i>sponse</i> SED ACCEPT sentence. 3	Philips <i>Comment Status</i> D he IDLE state as long as the av tate he IDLE or DISABLED state as Off max." <i>Response Status</i> W ' IN PRINCIPLE.	erage voltage	PSE Power e across the pairset is

Pa **114** Li **44**

CI 33	SC 33.3.3.10	P 1 :	29	L 1	# 454	C/ 33	SC	33.3.9	P 129	L 11	# 210				
Yseboodt, L	ennart	Philip	S			Darshan, Ya	air		Microsemi						
Comment Ty	/ре Т	Comment Status	Х		Pres: Yseboodt3	Comment T	ype	TR	Comment Status X		Pres: Darshan12				
The PD We have SuggestedR	inrush specifica e now adopted a Remedy	tion is mismatched l ccurate inrush text	between the in 33.3.8.3, th	text and the state ne SD should refl	e diagram. ect this.	The subject is: Figure 33-32 (PD single signature state diagram), dll_power_type, dll_power_level and the synch with Figure 33-50 which is currently is good only for Type 1 and Type 2. Background: PD Type 1/2 state machine: In page 1/2 state machine:									
Adopt y	seboodt_03_091	6_pdinrushsd.pdf													
Proposed R TFTD	esponse	Response Status	W			and 2 st The pse	tate m e_dll_p	achine in p ower_type	page 124 line 30 at the exit from is used in the PD power contr	m MDI_PWR1. rol state diagram	(LLDP) Figure 33-				
WFP						50. So far a	ll is g	ood.							
						Single Signature PD Type 3/4 state machine: In page 127 line 11 we have a definition for pse_dll_power_level that should be used in the single-signature PD Type 3 and 4 state machine on page 129 line 11 at the exit from MDI_PWR1 but instead there is pse_dll_power_type there as was in Type 1/2 PD state machine. The pse_dll_power_type is required in the PD power control state diagram (LLDP) Figure									
						33-50 but is not defined in the variable list (what is defined is only pse_dll_power_level.									
				 I ne problems are: For Type 3 and 4 single-signature PD: It needs to be pse_dll_power_level and not pse_dll_power_type. Type 3 and 4 single-signature PD state diagram and variable list should be sync with Figure 33-50 that historically needs pse_dll_power_Type only for Type 1 and 2. We need figure 33-50 to work with Legacy and new single-signature PDs. 											
						SuggestedRemedy									
				Adopt darshan_12_0916.pdf if available for the meeting. If not, To add Editor Note to page 129: "Editor Note: (1) To make changes in Figure 33-50 so it can work with Type 1 and 2 by using the existing variables in Figure 33-50 and work with dll_power_level when it is Type 3 and Type 4 PDs. (2) Type 3 and 4 single-signature PD state diagram and variable list should be sync with Figure 33-50 "											
						Proposed R	espor	nse	Response Status W						
						TFTD									
						WFP									
						See 296	6								

Pa **129** Li **11**

Cl 33 SC 33.	3.3.10	P 129 Texas Instru	L 15	# 31	Cl 33 Stover Da	SC 33.3.8	P 146 Linear Tech	L 44	# 524					
Comment Type TR Comment Status X Pres: Yseboodt3 The PD behavior during inrush is not fully described in the state diagram, referring to 33.3.8.3. For example, Single-signature PDs assigned to Class 1, 2, or 3 shall conform to PClass_PD and PPeak_PD within TInrush-2P min. Another example is that it has to meet inrush requirements with the PSE behavior as defined in 33.2.8.5. SuggestedRemedy Add an editor's note to review the PD state diagram to cover inrush behavior.						Comment Type T Comment Status X Pres: Darshan1 P_Peak_PD-2P (used in section 33.3.8.5, which references this table) is missing. SuggestedRemedy Define P_Peak_PD-2P (TFTD). Proposed Response Response Status W TFTD as requested								
Proposed Response TFTD WEP	Response	e Status W	n to cover infusn	benavior.	CI 33 Yseboodt, Comment	SC 33.3.8. Lennart <i>Type</i> E	3 P 149 Philips Comment Status X	L 21	# <u>385</u> Pres: Yseboodt9					
Cl 33 SC 33.3.6 P 141 L 21 # 373 Yseboodt, Lennart Philips Comment Type T Comment Status D Pres: Yseboodt9 " shall conform to Type 1 PD power restrictions and shall provide the user with an active indication is undersourced. The method of active indication is left to the implementer."						I guess the intent was to say "PD only needs to meet the inrush requirements if the PSE complies to 33.2.8.5". Do we really need to say this ? The same applies to nearly every other PD parameter as well. Also, the earlier shalls are not conditional upon this one, so it has no effect in its current form								
The 'active indica - untestable - out of scope fo	ation' shall is: r an interoperabilit	y standard			Suggestee Remo 33.2.8	dRemedy ive "The PD sh 3.5."	all meet the inrush requiremer	nts with the PSE	behavior described in					
SuggestedRemedy " shall conform	to Type 1 PD pow	ver restrictions."			Proposed TFTD	Response	Response Status W							
Proposed Response PROPOSED RE This is legacy tex	Response JECT. tt and has was deb	e <i>Status</i> W pated heavily (fr	om what I have h	eard).	l knov PSE o of issu	v that this sente current capabili ues in the field)	ence was added to make sure ties at different voltage levels	that PD impleme (something that h	enters are aware of the nas caused a great deal					
TFTD														

Pa **149** Li **21**

CI 33 Bennett, Ke	SC 33.3.8.5	P 151 Sifos Te	L 31 Chnologies, In	# 50		C/ 33 Bennett, ł	SC 33.3.8.10	P 155 Sifos Techno	L 30 blogies, In	# 53			
Comment T	Туре Т	Comment Status	(PD Power	Comment	Туре Т	Comment Status X	-	Pres: Bennett1			
Figures describ these u	s 33-37, 33-38, a bed as operating upperbound tem	and 33-39 show PD upp masks, and a normativ plates.	perbound templates ve shall states the P	. These are also Ds must operate	o e below	Section	on 33.3.8.10 descrissary for interopera	ibes a test set-up to meet I ability.	con-2P and Icor	n-2P_unb, which are			
The fig power TCut-2	ures are valid up level. The figure P min (see 5% c	o to TCut-2P min for a set are not valid for mult duty cycle in 33.3.8.4).	single peak rising at tiple peaks that are s	bove the PClass	_PD than	which Icon-2P and Icon-2P_unb must be met. There are deficiences in this approach which can result in interoperability problems.							
Sugaested	Remedv	,				Suggeste	aRemeay Roppott 01 0016 r	. df					
Change existing	e the NOTE as f g notes where th	ollows and put it under ey appear):	each respective ter	nplate (replacing	the	Proposed TFTD	Response	Response Status W					
NOTE	- Figure 33-## a	pplies to a single peak	which exceeds the	PClass_PD pow	er value.								
Proposed F	Response	Response Status V	N			WFP							
TFTD						CI 33	SC 33.3.8.10	P 156	L 9	# 244			
remove	e figures and ass	sociated text.				Darshan,	Yair	Microsemi					
C/ 33 Bennett, Ke Comment T The ter	SC 33.3.8.5 en <i>Type</i> E mplates show a	P 151 Sifos Te Comment Status E second upperbound ste fall below before PSE	L L 32 echnologies, In D ep after Tcut-2P mir	# 51	PD Power ne power	See darshan_04_0916.pdf for the correct drawing. In figure 33-40, all Resistors are marked as Rsource_max which is incorrect. It should start with Rsource_min from top, and then Rsource_max, Rsource_min and Rsource_max in this order. See darshan_04_0916.pdf for the correct drawing.							
tilat a p	beak puise musi		rout unning is reset			SuggestedRemedy							
After a	Peak lasting TC	Cut-2P min ends, the ins	stantaneous power	must stay below	the	See c	arshan_04_0916.p	odf for the correct drawing.					
step af	ter droppin belov	w the PClass_PD powe	er level.	may exceed the	Second	Proposed TFTD	Response	Response Status W					
The alv	ways-valid portio	n of the second step is	the transition at TC	ut-2P-min.		W/ED							
Suggested	Remedy					VVIT							
For cla 1/8 of t	rity, shorten the heir existing leng	duration of the second gth.	step in Figures 33-3	37, 33-38, 33-39	to 1/4 or								
Proposed F	Response	Response Status V	N										
PROP	OSED ACCEPT	IN PRINCIPLE.											
l believ the Pcl	re what Ken wou ass_PD(-2P) lin	Id like is to shorten (in e.	time) the horiztonal	line that extends	s along								
If corre	ct, make the cha	ange. If incorrect, Ken	to comment.										
TFTD F	FS LY CJ YD												

TYPE: TR/technical required ER/editorial required GR/general required T/technical E/editorial G/general COMMENT STATUS: D/dispatched A/accepted R/rejected RESPONSE STATUS: O/open W/written C/closed U/unsatisfied Z/withdrawn SORT ORDER: Page, Line Pa **156** Li **9**

Cl 33 Darshan, Y	SC 33.6 ′air	P 177 Microsemi	L 40	# 239	<i>Cl</i> 33 Darshan, `	SC 33.6 Yair	P 177 Microsemi	L 40	# 214					
Comment	Type TR	Comment Status X		Pres: Darshan11	Comment	Type TR	Comment Status X		Pres: Darshan11					
Type 3 power The ma 1. Figu pse_dl 2. Dup	and Type 4 sii management ii ain issues are: ire 33-50 is not I_power_level a licate variables	ngle signature state machine is n clause 33.6. supporting Type 3 and Type 4 and pse_dll_power_type) sused in 33.6 and 33.3.3.7 (e.g	not complete single-signatu pse_dll_powe	and contradicts DLL ire PDs. (need to support er_level)	 33.6 Data Link Layer classification need to be updated in order to: 1. support dual-signature PD. 2. To fix some error regarding the sync between variable names in PD state machine and its variable list, PD DLL power state maching and its variable list and figure 33-50 mainly and maybe Figure 33-49 as well. 3. In addition clause 33.6 needs to be in sync with PD single and dual signature state machines and their variable list. 									
Add "F	ditor Note: clau	use 33.6 and 33.3.3.7 need to h	e in sync		Suggested	dRemedy								
The fol 1. Figu pse_dl 2. Dup	llowing issues r ire 33-50 is not I_power_level a licate variables	need to be adressed: supporting Type 3 and Type 4 and pse_dll_power_type) s used in 33.6 and 33.3.3.7 (e.g	single-signatu pse_dll_powe	re PDs. (need to support	Adopt the be "Edito 1. sup 2. To f	darshan_11_0 gining of clause r Note: 33.6 Da port dual-signa fix some error re	915.pdf if ready for the meeting e 33.6: ta Link Layer classification nee- ture PD. ecarding the sync between vari	. If not, add the d to be update able names in	e following editor note to d in order to: PD state machine and					
Proposea I	Response	Response Status W			its var	iable list, PD DI	LL power state maching and its	variable list ar	nd figure 33-50 mainly					
IFID					and m	aybe Figure 33 c 33 6 with PD	-49 as well. single and dual signature state.	machines and	their variable list "					
l don't this po	I don't think adding editor's notes pointing out technical incompleteness are a good idea this point. We need actual soluitions.				Proposed Response Response Status W									
					WFP									
					C/ 33	SC 33.6	P 177	L 40	# 304					
					Schindler,	Fred	Seen Simply,	Broadco						
					Comment	Type TR	Comment Status X		Pres: Darshan11					
					A DLL may b	subject matter e required and	expert should add text covering a LLDP attribute map would also	g dual-signatur to then be requ	re PDs. A state diagram uired.					
					Suggested	dRemedy								
					Add o dual-s solutic	n line 40, "Edito ignature PDs." on is provided to	or's Note: readers are encourag This comment should not be c addess the comment made.	ed to improve onsidered sati	the DLL to encorporate sfied until an acceptable					
					Proposed	Response	Response Status W							
					TFTD									
					l don't this po	think adding ed bint. We need a	ditor's notes pointing out technic actual soluitions.	cal incomplete	ness are a good idea at					

Pa **177** Li **40**

CI 33	SC 33.6.3.	2 <i>P</i> 179	L 18	# 305	C/ 33	SC 33.6.5	P 18	6 L 4	# 476
Schindler, F	Fred	Seen Simply,	Broadco		Yseboodt,	Lennart	Philips		
Comment 7	Type TR	Comment Status X		Pres: Schindler	Comment	Type TR	Comment Status	х	Pres: Yseboodt1
Variable	e parameter_	type is determined only by Type	e 1 and 2 function	on set_parameter_type,	DLL A	utoclass sectio	n is missing content.		
thereto	re it will only fere and is req	ave values 1 and 2. Variable uired to determine PSE INITIA	od_allocated_po	ower is not assigned	Suggested	Remedy			
Suggested	Remedy				Adopt	yseboodt_01_0	0916_dllautoclass.pdf		
The sol	lution is provi	ded in schindler_3bt_01_0916.			Proposed	Response	Response Status	w	
Proposed F	Response	Response Status W			TFTD				
, TFTD	,				WFP				
WFP					C/ 33	SC 33.6.5	P 18	6 L 13	# 54
CL 22	SC 22 6 2	D D 170	/ 10	# 475	Bennett, K	en	Sifos 1	echnologies, In	
Vseboodt I	ennart	2 F 179 Philips	L 19	# 475	Comment	Туре Е	Comment Status	x	Pres: Yseboodt1
Commont 7		Commont Status V		Drage Vachaadta	Table	33-60 describe	s transactions using "Ll	DP Frame". All ot	her data link classification
	nstant PSF II	UITIAL VALUE needs to be init	ialized but the	way this is done is	transa	ctions in the sta	andard use the more sp	ecific terms: "Powe	er via MDI TLV", "LLDPDU",
differen Since v variable	nt for Type 1/2 we want to ave e that is causi	and Type 3/4. bid splitting the DLL state diagrang trouble, we should initialize	ams, and this is t differently dep	(for now) the only bending on PSE Type.	There specifi	isn't a formal "I cally defined in	LDP Frame" definition section 33.6.1.	in Clause 33, wher	reas "TLV Frame" is
Suggested	Remedy								
Adopt y	/seboodt_02_	0916_pseinitialvalue.pdf			Suggested	Remedy			
Proposed F	Response	Response Status W			Chang	e all instances	of "LLDP Frame" in tab	le 33-60 to:	
TFTD					"TLV F	rame" or "LLD	PDU"		
WFP					Proposed	Response	Response Status	w	
0.00	00 00 0 5	D 400		# 040	TFTD				
Schindler, F	Fred	P 186 Seen Simply,	L 4 Broadco	# 316	WFP				
Comment T	Type TR	Comment Status X		Pres: Yseboodt1	C/ 79	SC 79.3.7.4	P 22	2 L 20	# 69
An auto	oclass subject	matter expert should add text	covering this to	pic. A state diagram	Ran, Adee		Intel		
may be related	to other com	a LLDP attribute map would al nents marked COMMENT-2.	so then be requ	lired. This comment is	Comment	Type TR	Comment Status	х	LLDP
Sugaested	Remedv				Does '	should" here m	nean it is only a recomm	endation? Is it OK	to have more than one?
Add on	line 5, "Edito	r's Note: readers are encourage	ed to improve A	utoclass information by	Also a	pplies to 79.3.2	2.7. although it is in the	pase document.	
adding	text and state	diagrams as approporiate." T	his comment sh	nould not be considered	Sugaested	IRemedv	,		
Satistie	a until an acc	Preserves Status	addess the com	ment made.	Chang	e to "shall" unl	ess there is no problem	with having more	than one.
TETD	response	Response Status W			Proposed	Response	Response Status	w	
					TFTD	,			
WFP									
TYPE: TR/t COMMENT	echnical requ	ired ER/editorial required GR/ dispatched A/accepted R/reje	general require	d T/technical E/editorial G/g NSE STATUS: O/open W/w	general ritten C/closed	U/unsatisfied	Z/withdrawn	Pa 222 Li 20	Page 8 of 9 9/15/2016 7:05:30 Pl

SORT ORDER: Page, Line

C/ 33B Haiduczer	SC 33B.1 nia. Marek	P 237 Charter Comr	L 8	# 118	C/ 33 Darshan Ya	SC Annex	33B	P 237 Microsemi	L 16	# 250		
Comment No su Suggested	<i>Type</i> ER bclause numbers dRemedy	Comment Status A		Editorial	Comment T (See da Annex 3 Rload_1	/pe TR rshan_06_09 3B directs th nin/max whe	Comm 16.pdf) e reader to re derived a	nent Status X Annex 33D to find ir nd other parts that a	mportant inform are pair to pair	Pres: Darshan6 native data to how related. This Annex is		
Please Response ACCE There first or Editor other s	e add subclause r PT IN PRINCIPL are annex numbe ne, 33B.1 (line 50 to renumber Ann subclause numbe YD	numbers in Annex 33B <i>Response Status</i> U E. ers, there is just a bunch of to). ex 33B to put introductory m rs.	ext and a drawir aterial into 33B.	g before you get to the 1 and increment all	missing and should be added as planned. Annex D is needed since all the parts of pair to pair unbalance are spread all over the spec and it is hard to see the whole picture. I find it very useful to have short summary that show the whole spec explained in short in 1.5 pages and it was planned to be there long time ago. Annex D content was reviewed many times in the original contribution (see the reference at the end) and base on it, the whole spec was built. SuggestedRemedy See proposed remedy in darshan_06_0916.pdf for Annex D. Proposed Response Response Status W TETD							
C/ 33 Darshan, `	SC Annex 33 Yair	B P 237 Microsemi	L 16	# 193	WFP							
Comment (See C Annex Rload Suggested See p Proposed TFTD	Type TR darshan_06_0916 (33B directs the r _min/max where dRemedy roposed remedy i Response	Comment Status X 5.pdf) reader to Annex 33D to find i derived. This Annex is missin n darshan_06_0916.pdf for <i>i</i> <i>Response Status</i> W	<i>Pres: Darshan6</i> ative data to how e added as planned.	Cl 33 Darshan, Ya Comment T Figure 3 1. The 6 and Vdi 2. The a sufficien for the i	SC 33B.1 P 238 L 30 # 204 air Microsemi Pres: Darsha wype TR Comment Status X Pres: Darsha 33B-2: drawing looks like broken on the left side at the connections to Vport_pse, Vdiff1 Ff2. arrows marking the point of measuring Veff1, Veff1, Veff3 abd Veff4 are not nttly clear where they are pointing. Follow the original drawing darshan_03_0916.pc							
WFP					SuggestedH Editor t 1. Fix th See ref 2. To al Proposed R TFTD WFP	remedy c: e broken col erence in dai gn the arrow esponse	nection in F shan_03_09 s to the corr <i>Respor</i>	Figure 33B-2. 916.pdf. rect position as exac nse Status W	ctly as shown ir	adarshan_03_0916.pdf.		

Pa **238** Li **30**