

Extended Power baseline proposal

v220

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Goal

Objective: $P_{\text{Class}} - P_{\text{Class_PD}}$ is power reserved for cable losses. The goal is to make this power available to the PD if it has additional information about actual channel losses. The amount of power available depends on actual channel losses & PSE power allocation policy.

Two things are needed:

- Table 33-18 Item 4 forbids PDs from exceeding $P_{\text{class_PD}}$
- PD may not cause PSE to source $> P_{\text{Class}}$

Table 33-18 -- PD power supply limits

Change in Table 33-18

Item	Parameter	Symbol	Unit	Min	Max	PD Type	Additional information
4	Input average power, Class 0 and Class 3	Pclass_PD	W		13	1	See 33.3.7.2, Table 33-1
	Input average power, Class 1				3.84	1	
	Input average power, Class 2				6.49	1	
	Input average power, Class 4				25.5	2	
	Input average power, Class 5				TBD	3	
	Input guaranteed available average power, Class 6				TBD	3	
	Input average power, Class 7				TBD	4	
	Input guaranteed available average power, Class 8				TBD	4	

33.3.7.2 Input average power

Add to section 33.3.7.2

33.3.7.2 Input average power

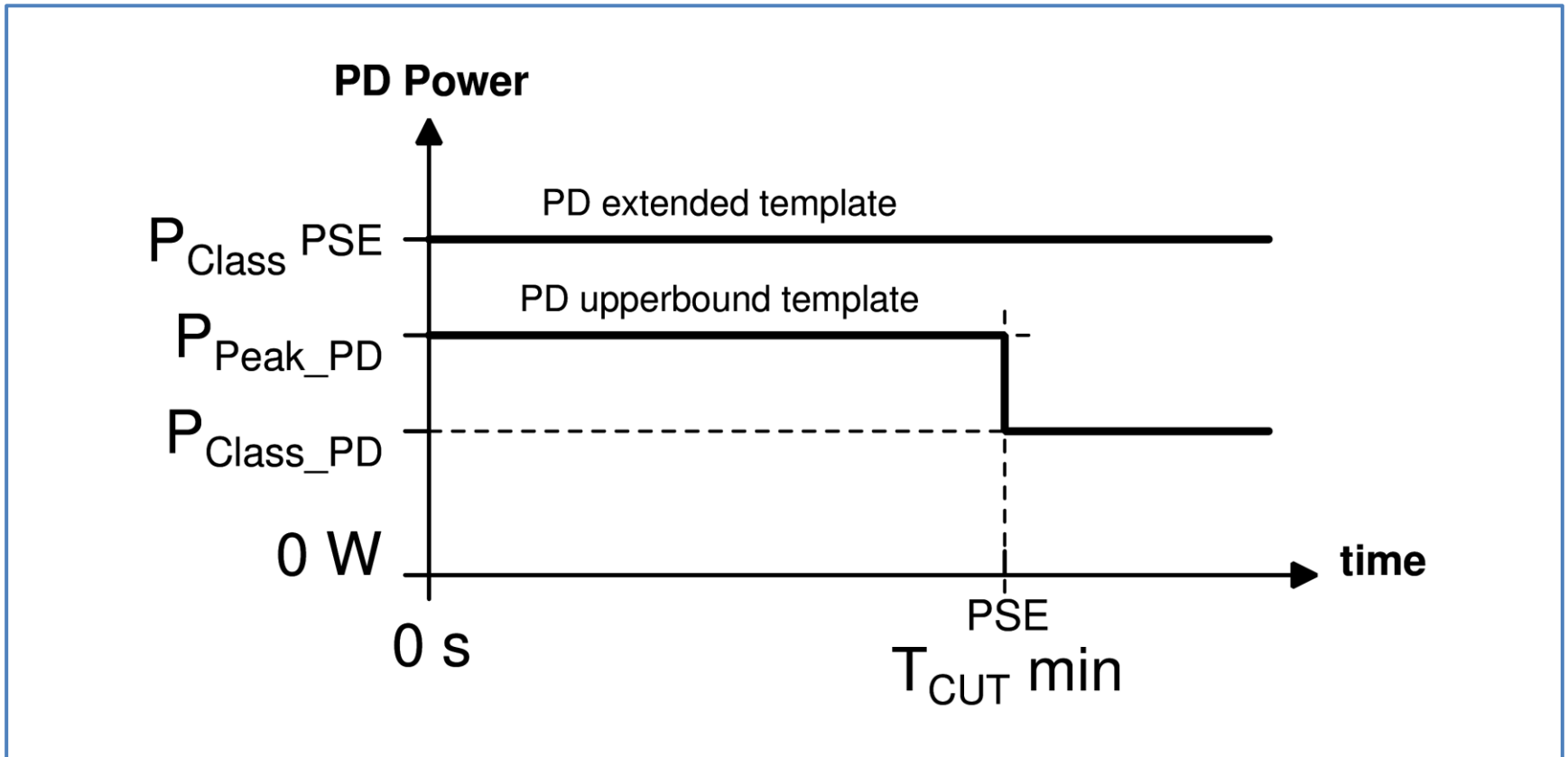
The maximum average power, $P_{\text{Class_PD}}$ in Table 33–18 or PDMaxPowerValue in 33.6.3.3, is calculated over a 1 second interval. PDs may dynamically adjust their maximum required operating power below $P_{\text{Class_PD}}$ as described in 33.6.

NOTE—Average power is calculated using any sliding window with a width of 1 s.

For Type 3 and 4 PDs the input guaranteed available average power is the maximum power the PD may consume when no additional information is available to the PD regarding actual channel DC resistance. If the PD has information about actual channel resistance then the PD may exceed the maximum guaranteed power provided that the PD does not cause the PSE to source more than P_{Class} .

33.3.7.5 Peak transient current

Replace Figure 33-18



33.3.7.5 Peak transient current

Replace:

Under normal operating conditions when there are no transients applied at the PD PI, the PD shall operate below the PD upperbound template defined in Figure 33–18.

By:

Under normal operating conditions when there are no transients applied at the PD PI, Type 3 and Type 4 PDs shall operate below the PD extended template define in Figure 33-18. Type 1 and Type 2 PDs, or PDs that cannot guarantee that the PSE will source less than P_{class} shall operate below the PD upperbound template defined in Figure 33-18.

Note: this text & figure do not allow the PD the exceed the upperbound template unconditionally. The condition is that the PSE does may not be caused to exceed sourcing P_{class} , this restriction is handled in section 33.3.7.2.

33.3.7.5 Peak transient current

Add after description of $P_{PDUT}(t)$ in 33.3.7.5

The PD extended template in Figure 33-18, P_{PDET} , is described by Equation (33-xx):

$$P_{PDET}(t) = \{P_{Class} \text{ for } (0 \leq t)\}_w$$

where

t	is the duration in seconds that the PD sinks I_{port}
P_{Peak_PD}	is the peak operating power, $P_{peak_Pd}max$, as defined in Table 33-18
P_{Class_PD}	is the maximum power, $P_{class_Pd}max$, as defined in Table 33-18
P_{Class}	is the minimum power output by the PSE, as defined in Table 33-7 and section 33.2.6