

**Comment (#113, 114):**

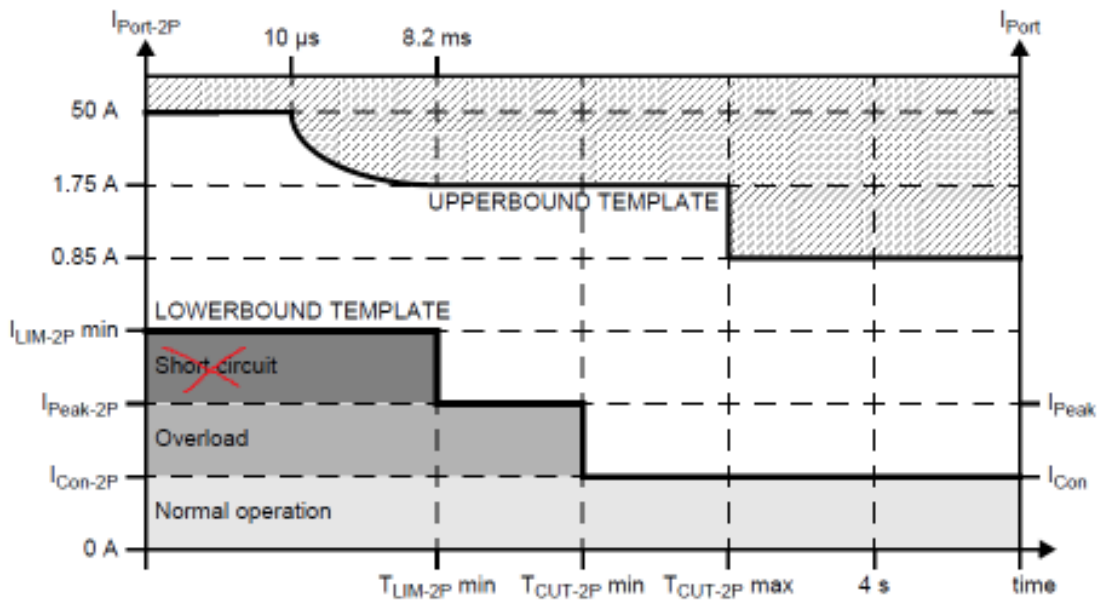
Figures 145-24 and 145-25: Short circuit conditions can't start below the lowerbound template. Specifically, the region between  $I_{peak-2P}$  to  $I_{LIM-2P}$  up to  $T_{LIM-2P}$  is not short circuit.

The  $I_{LIM-2P}$  area is between the lowerbound template and the higherbound template and is the short circuit area as explained by page 154 line 37:

"A PSE may remove power from the PI if the PI current meets or exceeds the "PSE lowerbound template" in Figure 145-24 and Figure 145-25. Power shall be removed from a pairset of a PSE before the pairset current exceeds the "PSE upperbound template" in Figure 145-24 and 145-25."

**Suggested Remedy:**

1. Delete the "Short circuit" label from the region between  $I_{peak-2P}$  and  $I_{LIM-2P\_MIN}$  in Figure 145-24 and Figure 145-25.
2. Change the color of the region between  $I_{peak-2P}$  and  $I_{LIM-2P\_MIN}$  to white.



**Figure 145-24—POWER\_ON state, per pairset operating current template for Type 3 PSEs**

-Repeat the same corrections in Figure 145-25