

**Baseline text parts for PSE PI P2PRunb.**

**Proposed update to Table 33-11**

Item	Parameter	Symbol	Unit	Min	Max	PSE Type	Additional Information
4.1	Pair current due to E2ERunb within E2ERunb range	Icont_2P_unb	A		0.668(TBD)	3	See clause 33.2.7.x.1
					0.931(TBD)	4	See clause 33.2.7.x.1

**33.2.7.x.1 PSE PI Pair to Pair Resistance/Current Unbalance Requirements**

Icont\_2P\_unb\_max is the average Icon\_2P current value with current increase to the pair due the presence of E2EP2PRunb in the system. The total sum of the current of pairs with the same polarity shall not exceed Pclass/Vpse.

Icont\_2P\_unb\_max was specified for total channel common mode pair resistance from 0.1Ω to 12.5Ω. For channels with common mode pair resistance lower than 0.1Ω, see guidelines in Annex 33B in clause TBD.

**33.2.7.x PSE PI Pair-to-Pair Resistance/Current Unbalance**

PSE PI Pair to Pair effective Runb ( PSE\_P2PRunb) contribution to the whole Effective System End to End Resistance/Current Unbalance (E2EP2PRunb), is specified by PSE Rpair\_max and PSE Rpair\_min values. See details for Rpair\_max and Rpair\_min in Annex 33-B.

The PSE\_P2PRunb determined by Rpair\_max and Rpair\_min ensures that with any other parts of the system i.e. channel (cables and connectors) and the PD, the maximum pair current due to E2EP2PRunb, shall not exceed Icont\_2P\_unb as defined in Table 33-11 item 4.1.

Icont\_2P\_unb is the pair current due to the E2EP2PRunb that is higher than Icont\_2P (specified in Table 33-11 item 4). See drawing TBD in Annex 33-B.

Rpair\_min and Rpair\_max are specified and measured under maximum Pclass sourcing conditions.

Testing methods and test conditions of Rpair\_min and Rpair\_max are described in [33.2.7.x.3 Annex 33-B](#). Conformance shall be determined with equation 33-9 that sets Rpair\_max and Rpair\_min ;

$$R_{pair\_max}(\Omega) \leq \begin{cases} 1.894 \cdot R_{pair\_min} - 0.052 & \text{for Type 3} \\ 1.760 \cdot R_{pair\_min} - 0.040 & \text{for Type 4} \end{cases} \quad (33-9)$$

[Note: All constants are temporary numbers (TBDs) to illustrate the concept and will be updated later]

Where:

Rpair\_max and Rpair\_min are maximum and minimum PSE common mode effective resistances in the powered pairs of the same polarity. The values of Rpair\_max and Rpair\_min are implementation specific and need to satisfy eq. 33-9.

Note: Information regarding equation 33-9 constants can be found in Annex 33B.

**33.2.7.x.3**

**Test setup and test conditions for Rpair\_max and Rpair\_min: TBD.**

**===== End of Baseline text =====**

## Background

### Revision 009.

#### Background (Not part of the Baseline text)

- We need to define the following parameters in Table 33-11.
  - PSE Vdiff – Done. See D0.2.
  - I<sub>max</sub>=maximum pair current due to E2ERunb. Now we have the numbers. Please see below Table 33-11 proposed update.
  - PSE PI R<sub>max</sub>, R<sub>min</sub> that meets  $R_{max} \leq U \cdot R_{min} + C_{pse}$ .
  - R<sub>max</sub> and R<sub>min</sub> will be measured by test setup TBD per Annex 33B.
  - U and K<sub>pse</sub> are constants representing worst case conditions per the curves showed for Type 3 and 4 on the positive pairs for E2ERunb.
  - This will ensure that the PSE will not exceed I<sub>max</sub> and E2ERunb<sub>max</sub> for

[For May 2015: The following address test setup for verifying Rpair\_Rmax, Rpair\_Rmin for compliance test.

The plane is:

- To make it shorter.
- To isolate the parts that is normative and informative.
- Finalize it for next meeting May 2015. Please review it until May 2015 and comment over the reflector]

**ANNEX 33B [Normative] PSE PI Pair-to-Pair Resistance/Current Unbalance** Pair-to-pair current unbalance refers to current differences in powered pairs of the same polarity. Current unbalance can occur in positive powered pairs, negative powered pairs, or both when a system uses all four pairs to 4-pair power when both PSE Alternatives provide power to both PD Modes.

Current unbalance must be met with any compliant unbalanced load, and is determined by the End-to-End Pair-to-Pair Resistance Unbalance (E2EP2PRunb).

A compliant unbalanced load consists of the channel (cables and connectors) and the PD.

Equation 33-8, specified for the PSE, assures that E2EP2PRunb will be met in a compliant 4-pair powered system. Fig. 33B-1 illustrates the relationship between PSE PI equation 33-8 and E2EP2PRunb.

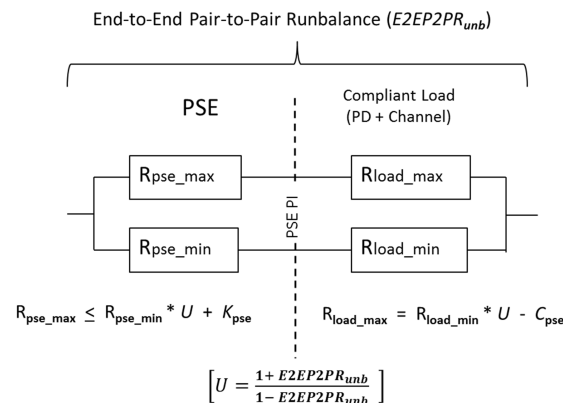


Fig. 33B-1 PSE PI Unbalance specification and E2EP2PRunb

Equation 33-9 specifies the PSE effective resistances required to meet E2EP2PRunb in the presence of all compliant, unbalanced loads attached to the PSE PI. A corresponding equation is shown for the worst case compliant pair-to-pair load. The pair-to-pair load equation is essentially the conjugate of the PSE equation. There are 3 alternate test methods for R<sub>pse\_max</sub> and R<sub>pse\_min</sub> and determining conformance to equation 33-8:

### 33B.1 direct measurements of R<sub>pse\_max</sub> and R<sub>pse\_min</sub>

If there is access to internal circuits, effective resistance may be determined by sourcing current in each path corresponding to maximum Pclass operation, and measuring the voltage across all components that contribute to the effective resistance, including circuit board traces and all components passing current to the PSE PI output connection. The effective resistance is the measured voltage divided by the current through the path ( $R=V/I$ ).

See Fig. 33B-1.

The two sections that follow, 33B.2 and 33B.3 illustrate two other possible measurements of PSE effective resistances for Rpse\_max and Rpse\_min equation 33-8 verification, if the internal circuits are not accessible.

### 33B.2 Effective Resistance Measurement Method by measurement of current unbalance under worst case pair-to-pair load conditions

Figure 33B-2 shows a possible test circuit for effective resistance measurements on a PSE port for evaluating conformance to Equation 33-8.

The Effective Resistance Test Procedure is described below:

- 1) With the PSE powered on, set the following current values
  - a.  $10\text{mA} < I_2 < 50\text{mA}$
  - b.  $I_1 = 0.5 * (\text{Pclass}_{\text{max}} / V_{\text{port}}) - I_2$ .
- 2) Measure  $V_1, V_2$ .
- 3) Reduce  $I_1$  by 20% ( $=I_1'$ ). Ensure  $I_2$  remains unchanged.
- 4) Measure  $V_1', V_2'$ .
- 5) Calculate  $R_{\text{eff1}}$ :
  - a.  $R_{\text{eff1}} = [(V_2 - V_1) - (V_2' - V_1')] / (I_1 - I_1')$
- 6) Repeat procedure for  $R_{\text{eff2}}$ , with  $I_1, I_2$  values swapped.
- 7) Repeat procedure for  $R_{\text{eff3}}, R_{\text{eff4}}$ .
- 8) Evaluate compliance with Equation 33-8.

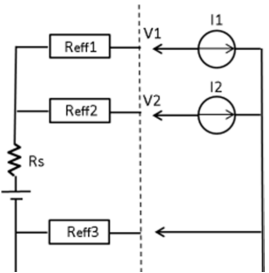


Fig. 33B-2 Effective resistance Test Circuit

The Effective resistance test method applies to the general case; if pair-to-pair balance is actively controlled in a manner that changes effective resistance to achieve balance, then the Current Unbalance Measurement Method described below should be used.

### 33B.3 Current Unbalance Measurement Method

The pair-to-pair load resistance equation from Fig. 33B-1 is shown below for reference.

$$R_{load\_max} = R_{load\_min} * U - K_{pse} \quad (33B-3)$$

Unbalanced load resistances conforming to equation in figure 33B-3 must be selected. Note that the equation only provides relative resistance values; if the selected resistances are too low, the results may be influenced by losses in the connecting hardware, and if the selected values are too high, the current unbalance will be dominated by the load and may mask the PSE unbalance. Current unbalance must be met for any pair-to-pair resistances meeting the equation; selected resistance values which provide adequate verification are dependent upon PSE circuit implementation and as such are left to the designer.

Fig. 33B-3 shows a test circuit for the current unbalance measurement.

The current unbalance test method is described below:

- 1) Select suitable  $R_{max}$  and  $R_{min}$  values which conform to equation 33B-3
- 2) With the PSE powered on, adjust the load for Max. Pclass power at the PSE
- 3) Measure  $i_1, i_2$
- 4) Swap  $R_{max}, R_{min}$ , repeat steps 1 and 2.
- 5) Repeat for  $i_3, i_4$
- 6) Verify that the current unbalance in each case does not exceed  $I_{unb\_ptp}$  limit in table 33-11.

Verification of  $I_{unb\_ptp}$  in step 6 confirms PSE conformance to Equation 33-8.

### 33B.4 Working with channel resistance with less than 0.1Ω

$I_{cont\_2P\_unb\_max}$  was specified for total channel common mode pair resistance from 0.1Ω to 12.5Ω. For channels with common mode pair resistance lower than 0.1Ω, the following guidelines may be followed:

1. PSE PI  $R_{pair\_max}$  and  $R_{min}$  need to be increased by TBD1 Ω and/or  $R_{diff} = R_{pair\_max} - R_{pair\_Rmin}$  value need to be decreased by TBD according to TBD equation.  
Equation TBD: TBD.

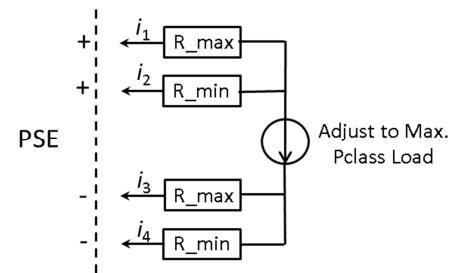


Fig. 33B-3 Current Unbalance Test Circuit