

50G and NGOATH Study Groups Ad Hoc Report

Kent Lusted, Intel
Ad Hoc Chair

Report

- 5 ad hoc calls since Dallas Plenary
 - February 3, 10, 17, 24, March 2
 - 120+ attendees
 - 12 contributions presented (see next slide)
- Meeting minutes and presentation materials:
<http://www.ieee802.org/3/50G/public/adhoc/archive/index.html>
- Further work: TBD
- Next meeting: To be announced over the email reflector (likely March 30)

Presentations

- Other
 - “50G & NGOATH SG Update”, Kent Lusted
- Architecture
 - “Architectural Options and Technical Feasibility of 100GbE”, Mark Gustlin
 - “Options of PCS Lane Rate in 50/NG 100GbE”, Tongtong Wang
 - “PCS consideration for 50 GbE and NG 100 GbE”, Ali Ghiasi
 - “50 GbE PCS supporting 2 lanes PMA”, Ali Ghiasi
 - “Compatibility Considerations for 50 and 100G - revisited”, Rob Stone
- Copper
 - “Copper Reach Targets” Joel Goergen
 - “Backplane Channels: BGA ball to BGA ball with manufacturing considerations.”, Rich Mellitz
 - “Feasibility of 30 dB channel at 50 Gb/s”, Ali Ghiasi
 - “Copper twin-axial cable”, Chris Diminico
- Optical
 - “500m PSM4”, Brian Welch
 - “200G-PSM4/DR4 Broad Market Potential”, Brian Welch
- Latency
 - “low latency schemes for 50GE”, Phil Sun
 - “Feasibility of 50GE low-latency schemes”, Phil Sun

THANKS!