

Agenda and General Information

Architecture Ad Hoc meeting of
IEEE 802.3 Backplane and Copper Study Group
“CU4HDDsg”

Acting Chair, Yong Kim
Broadcom
ad hoc call on 2015-09-10

Proposed Agenda

- Welcome and Introductions
 - IEEE patent policy reminder, and <http://www.ieee802.org/3/patent.html>
 - Meeting guidelines, <https://development.standards.ieee.org/myproject/public/mytools/mob/slideset.pdf>
 - Goals of Architecture ad hoc meetings (reminder)
 - Attendees' names and affiliations will be taken from the Webex participants list
- Approve Agenda
- Approve Minutes (9/03/2015, posted)
- Presentations (note: hard stop at 10 am PDT/USA) & Planning.

Title	Presenter(s)	Affiliation(s)	Notes
2.5G and 5G PCS Encoding	Anthony Calbone	Seagate	
PAR Draft	SG Ad hoc discussion	SG Ad hoc	
CSD Draft	SG Ad hoc discussion	SG Ad hoc	
Objectives Draft	SG Ad hoc discussion	SG Ad hoc	

Goals of Architecture ad hoc meetings

Charter:

- The charter of the 2.5G/5G Ethernet Backplane and Copper Study Group Architecture Ad Hoc is to discuss the different areas of work (RS/PCS/FEC/PMDs/AN/EEE) that will fall under the work of the study group and to prepare content, and contributions towards the study group's goal of developing the required documentation of objectives, PAR and CSD.

“Encouraged” Ad Hoc Topics

Topic	Status	Helps	Notes
CSD (Criteria for Standards Development)	Contrib		SG output
Objectives	Contrib		SG output
PAR (Project Authorization Request) Form	Started		SG output
Objectives: Bit Error Rate – diff from 10E-12?		Objectives	From CFI
Auto-Negotiation (CL73)		CSD	From CFI
Backplane Channel & Multi-rate Coexistence?		CSD	From CFI
Energy Efficiency and EEE		CSD	From CFI
Market needs for the short reach copper (“TwinAx”)		PAR, CSD	From CFI
XGMII/GMII/PCS compatibility with/to 802.3bz		CSD	From CFI
CSD: Optimal balanced system cost from the proposed rates (e.g. why 10G does not serve the market).		CSD	From reflector (SK)

Note: Not all topics needs contributions. Keeping a laundry list...

Presentations

Thank You!