IEEE 802.3

CU4HDD Study Group – Task Force Ad Hoc Minutes - 24-Mar-2016

Prepared by Dan Smith, acting chairman

Call Began on 3/24/2016, at 11:05 AM, MT.

Agenda:

- Further discussion about next steps.

Preliminaries:

Patent statement was presented with no response from the participants.

Presentation/Discussion:

- William described the presentations he gave in Macau. He has given that material to Yong Kim for rolling into the draft.
- Yong intends to have everything rolled into his draft by April 10.
- Section 1, 30, 35 are still outstanding.
- PMD clauses are the remaining heavy lifting. Section 72 is the area to extraction from.
  Annex 83E can also be used for electrical specs and testing.
- The normative PHY section is needed for the April 1 release of the draft.
- It is suggested that we take Section 72 and Annex 83E and use them as new templates for our work.
- Looking at 72 and 83E, we need to mix and match the information to get to our final content.
- 83D will be used for the informative, chip-to-chip, but that will be written last, to ensure we get the normative done first.
- All of this work needs to be specific to 2.5G and also specific to 5G.
- George advises flattening the frame source or take it as text, to prevent linkages from being carried across from the baseline spec. Pete Anslow can be contacted for any details of extraction from baseline, but we will probably use the PDF of baseline, to keep things simple, for now.
  We must stay within the 5 header max, in our draft, as we re-write.
- Dan will pull PDF from the baseline spec, for the above sections, and pull it into a Word doc, as William did, to distribute to a smaller team of people reviewing what needs to go into our PHY section for electrical and test. (Namely Seagate and WD.) If anyone else wants to join the review effort, contact Dan.
- We are planning an all-day, out- of-cycle, face-to-face for CU4HDD only, to finish the draft before the July Interim. Suggested days are: June 23, 24, 27, and 28. Either Marvell or Synopsys may host at their facility in silicon valley. Rita mentioned that PCI Express Developers Conference is meeting in Santa Clara, on the 28th and 29th, so having our meeting on Monday, June 27th, optimizes travel for those who might be attending both. And we all know, engineers like to optimize things.

- May 24 and 25 will be the official Interim meeting to work on comment resolution.

Adjourned: 12:00 PM, MT time, Next call, Thursday, March 31, 2016.

Attendees:

Name	Affiliation	Attended 3/24/2016
Calbone, Anthony	Seagate	X
Carlson, Mark	Toshiba	X
Chalupsky, David	Intel	
Chandrian, Ajith	Cisco	
Chang, Jacky	HP	
Cox, Alvin	Seagate	X
Czekalski, Marty	HGST	X
D'Ambrosia, John	DELL	
Feyh, German	Broadcom	
Ghani, Abbas	Ericsson	
Haser, Alex (Ms.)	Molex	
Healey, Adam	Broadcom	
Horner, Rita	Synopsys	X
Jones, Peter	Cisco	
Kim, Yong	Broadcom	X
Kipp, Scott	Brocade	
Law, David	HP	
Lo, William	Marvell	X
Marris, Arthur	Cadence	
McCarthy, Mick	Analog Devices	
McMillan, Larry	Western Digital	X
McSorley, Greg	Amphenol	
Mellitz, Rich	Intel	
Poh, Ed	Molex	
Parthasarathy, Vasu	Broadcom	
Rahman, Hamza	Dell	
Ran, Adee	Intel	
Schoenhals, Mark	Marvell	
Sedarat, Hossein	Aquantia	
Sinha, Santanu	Broadcom	
Skaar, Tom	Seagate	
Smith, Dan	Seagate	X
Sommers, Scott	Molex	

Name	Affiliation	Attended 3/24/2016
Souvignier, Tom	Broadcom	
Suhler, Paul	HGST	
Sun, Phil	Marvell	
Svensen, Justin	LIT	
Telang, Vivek	Broadcom	
Tsang, Dennis	Halo	
Tenea, Bogdan	Ixia	
Tracy, Nathan	TE Connectivity	
Tran, Paul	Marvell	
Walker, Dylan	Walker	
Wang, Roy	HPE	
Wassenberg, Paul	Marvell	
Wu, Peter	Marvell	
Zemach, Rami	Marvell	
Zhang, Jin	Marvell	
Zhang, Vince	Halo	
Zimmerman, George	CME Consulting, Inc.	X
Zimmerman, Yaron	Marvell	
Attendee Count:		10