

# 802.3bq Editors PHY issues to Close

George Zimmerman  
CME Consulting  
Chief Editor

# Status

---

- Draft 1.2 should be close to WG ballot
- Technical changes need to be FEW
- All technical requirements need to be filled in with accepted text
- Assumptions:
  - Editor's notes indicating something may require a close look are OK
  - Editor's notes suggesting ongoing technical work possibly producing a new technical requirement or option are not OK
  - TBDs next to technical requirements are undesired, explanatory notes may be used.
  - Informative text may have gaps in it as long as its clear the requirements are complete.

# Summary of known issues (1)

---

- Table 80-3 Maximum pause quanta needs number
  - Propose 50 pause quanta = 25600 BT/512 BT/quanta
- Whether FR should be made a mandatory capability (which can be disabled by register), remain optional, or be deleted (various places in the text)
  - A partial hole (only in 113.1), flagged by Ed Note, propose remain optional
- PBO level at which startup begins (see Ed note at 113.4.2.5.14)
  - Not a hole, flagged by Editor's note in 113.4.2.5.14, propose keep at 8 dB
  - (DEAL WITH IN FIRST WG BALLOT CYCLE – DELETE NOTE)

# Summary of known issues (2)

---

- Whether we should continue to support 8 levels of PBO up to 14 dB of PBO (maximum required to select is 6dB)
  - Not a hole, not flagged, propose keep at 14 dB
  - (DEAL WITH IN FIRST WG BALLOT CYCLE IF NECC.)
- Whether the transmitter linearity specification is needs reformulating
  - Not a hole, flagged by Ed note in 113.5.3.2, equation 113-9, no proposal but what is there is probably wrong (KEEP EDITOR'S NOTE)
- The lowest frequency we care about cabling delay
  - Not a hole, flagged by Ed note at 113.5.4.6.10 & 113.7.2.5, propose keep as is – (REMOVE EDITOR'S NOTE)

# Summary of known issues (3)

---

- Fast wake support required for 40G PHYs that support EEE
  - Not a hole, but an error, not flagged, discuss options
    - A) Add edit to clause 78 exempting BASE-T from the requirement
    - B) Add register and autoneg bits for EEE fast wake, and equate all EEE references to EEE deep sleep (which they are) (one line of text).
  - (IMPLEMENT OPTION A, AND ASK SYSTEM VENDORS TO LOOK INTO WHETHER THIS IS OK)

# Summary of known issues (4)

---

- Anything Else?