

PHY BASELINE PROPOSAL AD HOC AGENDA -  
3 April 2014

From the minutes of the November Task Force Plenary meeting:

The Chair then chartered a PHY proposal ad hoc, chaired by George Zimmerman, with the following charter/objectives:

- Identify elements necessary to form a baseline proposal
  - o Signaling bandwidth (bounds)
  - o Modulation, EQ, coding, etc.

----

Participants are encouraged to review IEEE meeting guidelines available at the following URL -  
<https://development.standards.ieee.org/myproject/Public/mytools/mob/preparslides.pdf>  
A proposed agenda for the meeting follows.

**10 AM** Pacific Time meeting start

1. Roll call : Please send an email indicating your attendance, employer and affiliation to  
<mailto:george@cmeconsulting.onmicrosoft.com?subject=802.3bq PHY ad hoc attendance 3Apr 2014>

2. Reminder of IEEE patent policy  
[www.ieee802.org/3/patent.html](http://www.ieee802.org/3/patent.html)

3. Housekeeping

Review & approve meeting agenda. :

Approve minutes from 27 February 2014 meeting

[http://www.ieee802.org/3/bq/public/phyproposal/PHY\\_BASELINE\\_PROPOSAL\\_ADHOC\\_MINUTES\\_27\\_Feb2014\\_unapproved.pdf](http://www.ieee802.org/3/bq/public/phyproposal/PHY_BASELINE_PROPOSAL_ADHOC_MINUTES_27_Feb2014_unapproved.pdf)

4. Old business from previous ad hoc meeting:

The following future contributions were planned in February and are still outstanding:

- Cable bending - End user inputs – (Dave C./Pete C.)
5. New business at this meeting:
    - Contribution from David Chalupsky, Intel, (to be) emailed to reflector: Delay Constraint Considerations for 40GBASE-T
  6. Next meeting time: Proposed Thursday April 17, **10AM** PST.
  7. Adjournment: