

Minutes IEEE 802.3 NEA 10BP AdHoc meeting 11 October 2017

Prepared by Peter Jones

Proposed Agenda:

1. Agenda/Admin Peter Jones

Presentations posted at:

http://www.ieee802.org/3/ad_hoc/ngrates/public/calls/index.html

Agenda/Admin Peter Jones:

Meeting began at 7:05am PT.

1. Reviewed the Attendance information related to the ad hoc.
2. Reminded people about pre-par slide deck & participation conditions.
<https://mentor.ieee.org/802-ec/dcn/17/ec-17-0093-05-OPNP-ieee-802-participation-slide-ppt.ppt>
<https://development.standards.ieee.org/myproject/Public/mytools/mob/preparslides.pdf>
3. Reminded participants to indicate full names and employer/affiliation correctly for the meeting minutes.
4. Next call (and last before plenary) will be October 25 7:00am PT.
5. The main reflector for this is STDS-802-3-NGECDC@LISTSERV.IEEE.ORG , meeting notices will be cced to STDS-802-3-10SPE@LISTSERV.IEEE.ORG.

Presentations/Discussion.

10BP Concensus Building	Jon Lewis Peter Jones David Tremblay Mandeep Chadha George Zimmerman	Dell EMC Cisco HPE Microsemi CME Consulting
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- Presenters walked through deck, got feedback and areas to improve.
 - General good response from the call, but still work to be done.
- Some items of note:
 - Discussion of past systems using 10MB /s Ethernet intra-system control plane, and the difference of what we want here.
 - Ask switched (fixed and modular) market numbers to match the information provided for servers.
 - Discussion of “single ended” vs “differential pair”, topic for Study Group.
 - Discussion of the right way to wordsmith the PAR/CSD. Will update slides, but this is SG work to get right.

- Reminder that we have 45 minutes scheduled for Plenary, presentation, Q&A, Straw Polls, and we are first. Need to fit within this slot.

Meeting closed – 8:10am PT

Attendees (from Webex + emails)

Name	Affiliation	Attended 10/11
Amrit Gopal	Ford	y
Arthur Marris	Cadence	y
Brett McClellan	Marvelk	y
Brian Franchuk	Emerson	y
Chad Jones	Cisco	y
Claude	Omniphy	y
Craig Gunther	Harmen	y
Dale Amason	NXP	y
Dale Borgeson	Emerson	y
Daniel Wiesmayer	draexlmaier	y
David Law	HPE	y
Dayin Xu	Rockwell	y
Geoff Thompson	GraCaSI S.A	y
George Zimmerman	CME	y
Harald Zweck	Infineon	y
Heath Stewart	Linear	y
James Withey	Fluke	y
Jay Cordaro	Broadcom	y
Jens Gottron	Siemens	y
Jim Bauer	Marvell	y
joe byrne (nxp)	NXP	y
John Yurtin	Delphi	y
Jon Lewis	Dell	y
larry Matola	Delphi	y
Laura Schweitz	Turk	y
Ludwig Winkel	Siemens	y
Mandeep Chadha	Microsemi	y
Mark Gustlin	Xilinx	y
Martin Miller	Microchip	y
martin white	Cavium	y
Masood Shariff	Commscope	y
Matthias Fritsche	Harting	y
Matthias.Jaenecke	Yazaki	y

Mehmet Tazebay	Broadcom	y
Mike Gardner	Molex	y
Oisín Ó Cuanacháin	Analog Devices	y
Peter Jones	Cisco	y
Pier Giorgio	Canovatech	y
Ron Naismith	Schneider Electric	y
Steffen Graber	Pepperl & Fuchs	y
Steve Carlson	High Speed Design	y
Trevor Barcelo	Analog Devices	y
Vimalli Raman	Yazaki	y
	total	43