

Unconfirmed minutes

IEEE 802.3 Timing performance ad hoc conference call

Wednesday, 15 November, 2017

Meeting started shortly after 20:00 UTC.

Minutes were taken by Adee Ran.

Adee Ran asked if anyone was not familiar with the patent policy. Nobody responded.

Stefano Ruffini (Rapporteur of ITU-T SG15 Q13) went through the [liaison letter](#) and explained the concerns.

Presentation #1

Title: Thoughts following ITU-T SG15 Q13 October 2017 Liaison letter

Presenter: Adee Ran, Intel

See [ran_tp_01_2017_11_15.pdf](#)

Clarifying questions and discussion following the presentation.

It was noted that links which include protocol-aware retimers and extension sublayers may not keep a constant synchronous data rate between FEC encoder input and FEC decoder output.

The presenter requested further inputs on FEC functions in PHYs other than BASE-R.

It was suggested that this topic be discussed during the joint IEEE 802.1, 802.3, ITU-T Study Group 15 Workshop on Saturday 28 January 2018.

Mr. Anslow offered to create a web page for the ad hoc in the [IEEE 802.3 ad hocs](#) web area.

Next meeting scheduled for 29 November 2017 at 12:00 noon Pacific time (20:00 UTC).

38 connections to the call (unknown number of duplicates due to phone/web access).

Identified participants:

Pete Anslow, Ciena

David Chalupsky, Intel

Rodney Cummings, National Instruments

Mark Gustlin, Xilinx

Marek Hajduczenia, Charter Communications

Mark Kimber, Semtech

David Law, Hewlett Packard Enterprise

Jeffery Maki, Juniper

Richard Mellitz, Samtec

Adee Ran, Intel

Stefano Ruffini, Ericsson

Kapil Shrikhande, Innovium

Steve Trowbridge, Nokia