



# **P802.3ae Logic Track Summary**

Ben Brown

P802.3ae TF Logic Track Chair

12-January-2001

# Clauses 1...

- No outstanding comments
- All comments closed
- Draft in good shape



# Clause 30

- No outstanding comments
- All comments closed
- Draft in good shape

# Clause 44

- Outstanding comments:
  - Clean up bit ordering figures in Annex 44A
  - Addition of similar figures for WWDM implementations
- All comments closed
- Draft in good shape

# Clause 45

- Outstanding comments:
  - Resolved interoperation with clause 22
  - Selected electricals based on JESD8-11
  - Confusing TX/RX/IN/OUT
  - MANY Fault and Loopback modes
  - Need resolution of PMA/PMD capabilities
- All comments closed
- Draft in good shape

A decorative graphic consisting of a vertical bar on the left and a horizontal bar at the bottom, both composed of multiple parallel lines in the colors of a rainbow (blue, green, yellow, orange, red). The vertical bar ends in a black arrowhead pointing downwards at the bottom-left corner.

# Clause 46

- Outstanding comments:
  - RF/LF detection rules
  - XGMII Timing - slide setup/hold window
  - XGMII Electricals - testing under a load
- All comments closed
- Draft in good shape



# XAUI Summary

- Compliance Channel Breakout
  - Agreed on loss characteristics for D2.1
  - Agreed on ISI limit for D2.1
- Jitter Breakout
  - Solidified the jitter methodology for D2.1
  - Agreed on jitter spec's
  - Assigned actions to complete test patterns by March
- Comment Resolution
  - Resolved all major comments from D2.0
  - Approved editor's responses for all minor comments
  - Approved values to replace remaining TBD's in D2.1
  - Approved addition of a Jitter Measurement subclause

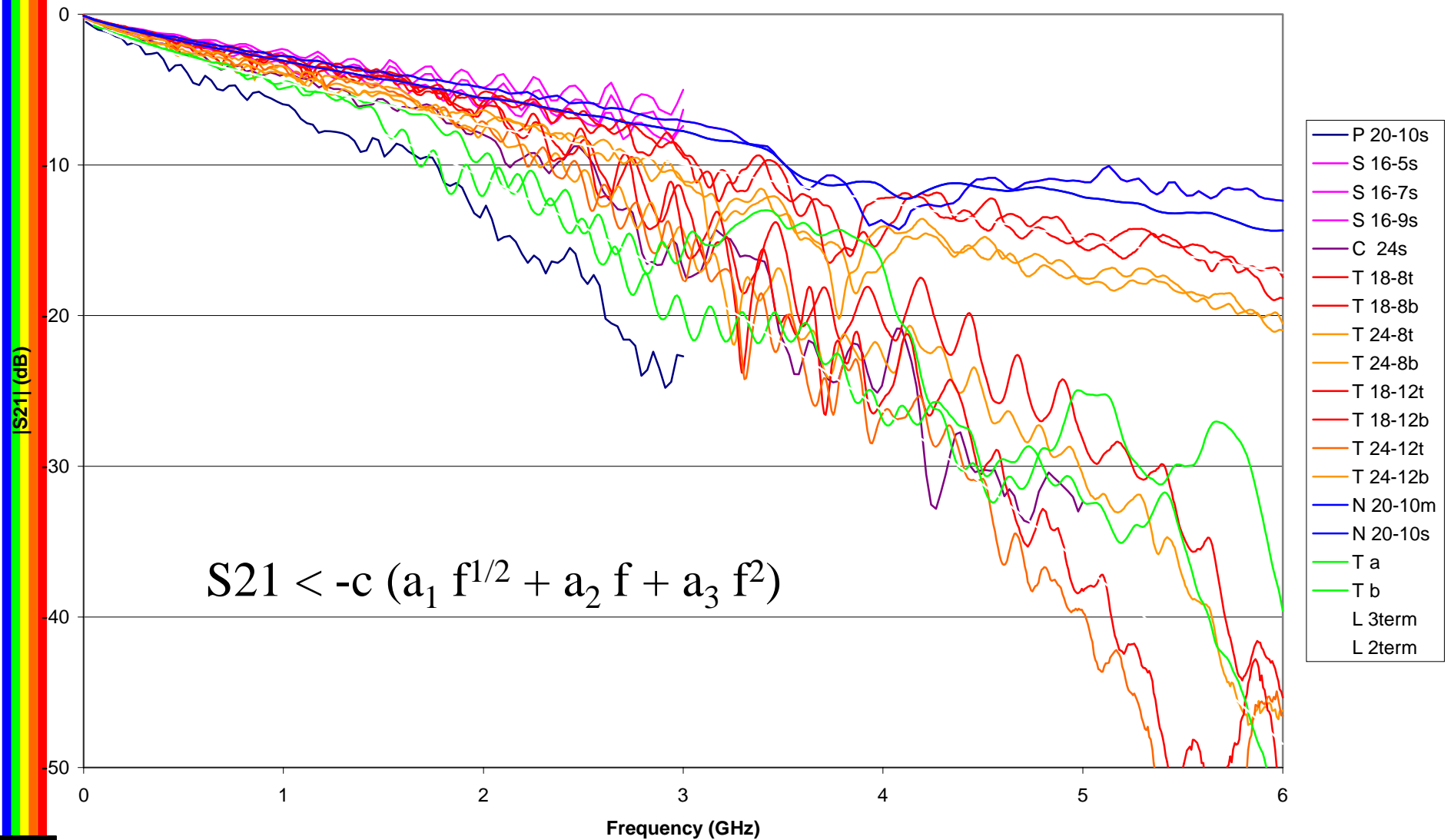


# Work to Do by the end of D2.1 Circulation (Feb. 23)

- Finalize jitter test patterns
- Confirm channel group delay spec with more data
- Complete interface simulations
  - To make sure that various interdependent spec's are consistent: jitter, connectors, return loss, compliance channel, eyes, etc.



# Overlay of All S21 Data



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# Clause 48

- Outstanding comments:
  - State machines cleaned up for simplicity
  - Pulse/Sequence Ordered Sets simply forwarded except where state machine necessary to reduce EMI
- All comments closed
- Draft in good shape

# Clause 49

- Outstanding comments:
  - State machines cleaned up for simplicity
  - Pulse/Sequence Ordered Sets simply forwarded
- All comments closed
- Draft in good shape

# Clause 50

- Outstanding comments:
  - A request to remove superfluous overhead for supported point-to-point links - ramifications are potentially large - bring up in the afternoon session
  - Need Flow Control delay numbers
    - 8000 RX
    - 6000 TX
- All comments closed
- Draft in good shape



## Motion #2

Accept in principle the concept of  
building in a test generator/checker  
into clauses 49 & 50

Moved : Bill Reysen

Seconded : Henning Lysdal

Y:      N:      A:

Voice vote, clear 75%      4:12pm 12-Jan-2001



## Motion #3

Move that the IEEE P802.3ae TF adopt the serial LAN & WAN Jitter Test Patterns as presented by Bill Reysen earlier in this session and instruct the editors to make the appropriate changes in D2.1

Moved : Bill Reysen

Seconded : Shimon Muller

Y:      N:      A: Pass by acclamation 4:28 12-Jan



## Motion #4

Move that IEEE P802.3ae TF affirm the resolution of all comments on IEEE P802.3ae/D2.0 for clauses 1 through 50 as resolved during the individual tracks

Moved : Ben Brown

Seconded : Brad Booth

Y:     N:     A: