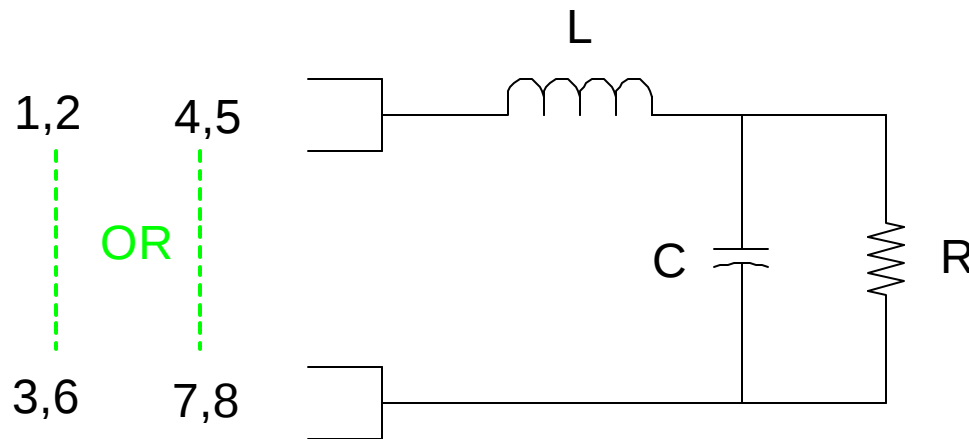


Here is what we think that the PD input impedance will look like at an input frequency of 1 kHz



For PD input voltages of 2.8 VDC to 10 VDC (discovery mode)

C: 50 nF to 100 nF
 L: 0 to 100 uH
 R: 25 Kohm +/-5%

For PD input voltages of 10 VDC to 25 VDC (classification mode)

C: 50 nF to 100 nF
 L: 0 to 100 uH
 R: greater than 500 ohm

For PD input voltages of 25 VDC to 37 VDC (UVLO transition)

C: greater than 50 nF,
 greater than 180 uF requires a PD input current limiter
 L: 0 to 100 uH
 R: greater than 105 ohm (37V at 0.35 amps continuous)

For PD input voltages of 37 VDC to 57 VDC (normal powering)

C: greater than 5 uF,
 greater than 180 uF requires a PD input current limiter
 L: 0 to 100 uH
 R: greater than 105 ohm (37V at 0.35 amps continuous)