

Table 28–8—State diagram variable to MII register mapping

State diagram variable	MII register	MDIO register
mr_np_tx[16:1]	7.15:0 Auto-Negotiation Next Page Transmit Register	<u>7.22.15:0 AN XNP Transmit</u>
<u>mr_np_tx[32:17]</u>	<u>Extended Next Pages not supported by MII register interface</u>	<u>7.23.15:0 AN XNP Transmit</u>
<u>mr_np_tx[48:33]</u>	<u>Extended Next Pages not supported by MII register interface</u>	<u>7.24.15:0 AN XNP Transmit</u>