

10GBASE-T skew delay pair B (Register 1.146)

The skew delay register reports the current skew delay on each of the pair with respect to pair A. It is reported with 1.25ns resolution to an accuracy of 2.5ns. The number reported is in two's complement notation with positive values representing delay and negative values representing advance with respect to pair A. If the delay exceed the maximum amount that can be represented by the range (-80ns to +78.75ns), the field displays the maximum respective value. The value shall be updated at least once per second.

Table 45-XX--10GBASE-T skew delay pair B register bit definitions

Bit(s)	Name	Description	Type
1.146.15	Reserved	Value always 0, writes ignored	RO
1.146.14:8	Skew delay B	Skew delay for pair B	RO
1.146.7:0	Reserved	Value always 0, writes ignored	RO

Skew delay B (1.146.6:0)

This field contains skew delay of pair B with respect to pair A.

10GBASE-T skew delay pairs C and D (Register 1.147)

The skew delay register reports the current skew delay on each of the pairs C and D with respect to pair A. It is reported with 1.25ns resolution to an accuracy of 2.5ns. The number reported is in two's complement notation with positive values representing delay and negative values representing advance with respect to pair A. If the delay exceed the maximum amount that can be represented by the range (-80ns to +78.75ns), the field displays the maximum respective value. The value shall be updated at least once per second.

Table 45-XX--10GBASE-T skew delay pairs C and D register bit definitions

Bit(s)	Name	Description	Type
1.147.15	Reserved	Value always 0, writes ignored	RO
1.147.14:8	Skew delay D	Skew delay for pair D	RO
1.147.7	Reserved	Value always 0, writes ignored	RO
1.147.6:0	Skew delay C	Skew delay for pair C	RO

Skew delay D (1.147.6:0)

This field contains skew delay of pair D with respect to pair A.

Skew delay C (1.147.6:0)

This field contains skew delay of pair C with respect to pair A.