Change to 69A2.3

The interference generator is a broadband noise generator capable of producing white Gaussian noise with adjustable amplitude. The power spectral density shall be flat to +/-3dB from f1 for the port type under test to 0.5 x the signaling speed with a crest factor of no less than 5. The noise shall be measured at the output of a filter connected to the TP4 test point. The filter for this measurement shall have a 40dB/decade rolloff and a 3dB cutoff at 0.5 x signaling speed.

In table 70.7:

Delete lines referring to EITbase, f1, and f2.

add lines:

Amplitude of white Gaussian noise source | 8.6 mV RMS
Minimum DCD jitter | 0.000UI

In table 71.7:

delete lines referring to EITbase, f1, and f2.

add lines:

Amplitude of White Gaussian Noise source | 8.1 mV RMS
Minimum DCD jitter | 0.0

In table 72.10:

delete lines referring to EITbase, f1, and f2.

add lines:

Amplitude of White Gaussian Noise | 5.2mV RMS
Minimum DCD jitter | 0.035UI

In 69A.2.1, replace:

The pattern generator shall have jitter on its output. This jitter shall consist of sinusoidal jitter at a frequency no less than 1/250 of signaling speed and random jitter. The random jitter shall be measured at the output of a single pole high pass filter with cut-off frequency at 1/250 of the signaling speed. The sinusoidal jitter shall account for at least 50% of the total jitter power. The RMS amplitude of the jitter shall be no less than the RMS amplitude specified for the port type being tested.

with:

The pattern generator shall have jitter on its output. This jitter shall consist of sinusoidal jitter at a frequency no less than 1/250 of the signaling frequency, duty cycle distortion, and random jitter. Duty cycle distortion jitter shall be no less than the amount specified for the port type under test. The sinusoidal jitter plus the duty cycle distortion shall account for at least 50% of the total jitter power. The RMS amplitude of the jitter shall be no less than the RMS amplitude specified for the port type being tested.

Replace 69A.3 with:

For 10GBASE-KR, the pattern generator shall first be configured to transmit the training pattern defined in 72.6.10.2. During this initialization period, the DUT shall configure the pattern generator equalizer, via transmitter control, to the coefficient settings it would select using the protocol described in 72.6.10. During training, the white Gaussian noise source shall have RMS amplitude at the DUT less than 1 mV.

After initialization of the receiver and pattern generator, the white noise source shall be set to the amplitude appropriate to the port type being tested. The measured BER shall be less than 10^-12.