

# Editor's Report

## IEEE P802.3ap Draft 3.1

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Intel



# Editorial Team

Editor	Affiliation	Clause(s), Annexes(s)
Ilango Ganga Chief Editor	Intel	00, 1–49, 70, 71, 74, 74A
Arthur Marris	Cadence	45
Adam Healey Charles Moore	Agere Systems Avago Technologies	69, 69A, 69B
Tom Palkert	Xilinx	72
Pat Thaler	Broadcom	73, 73A

- Thanks to all volunteers & contributors

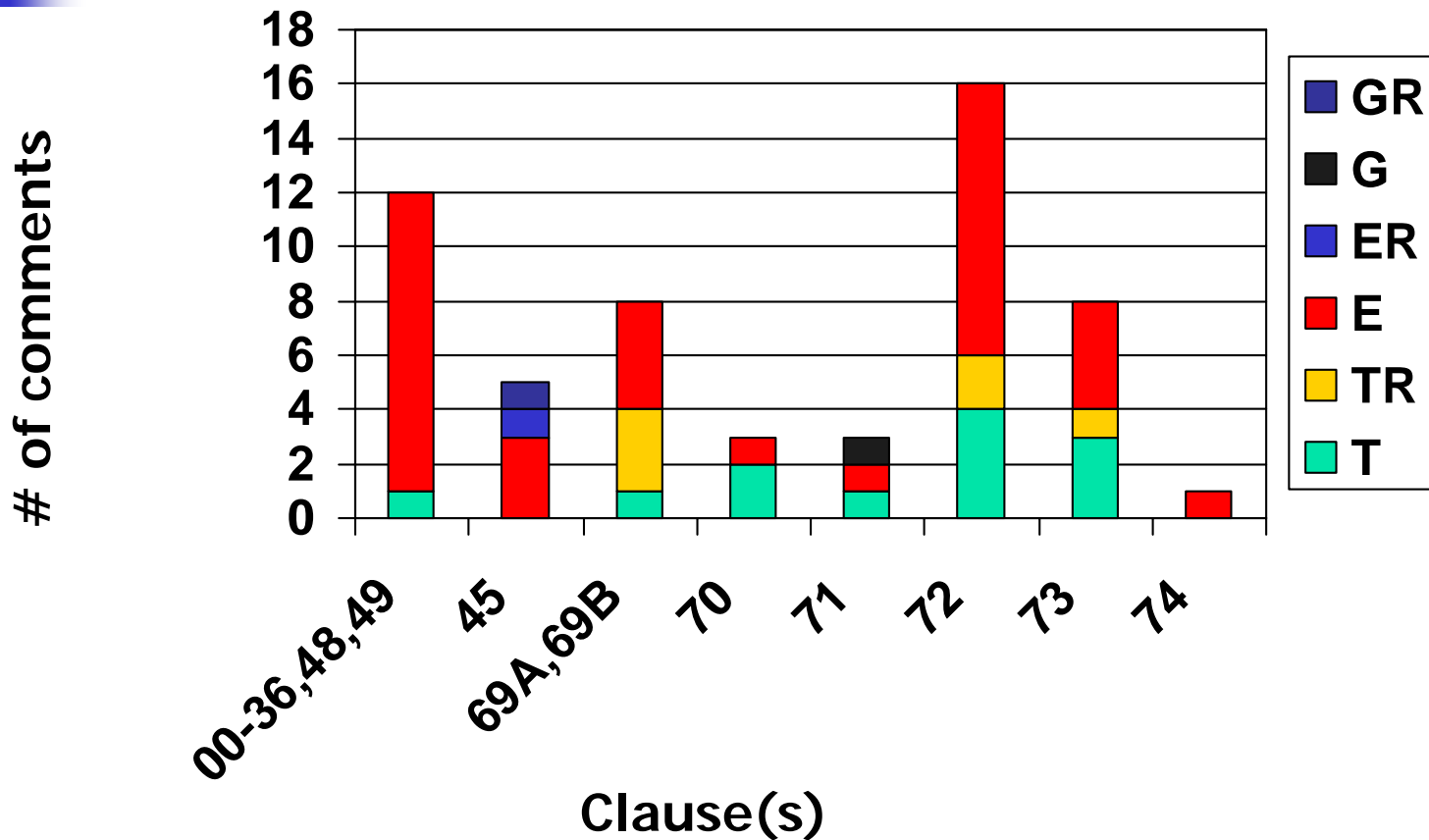


# Document & comment status

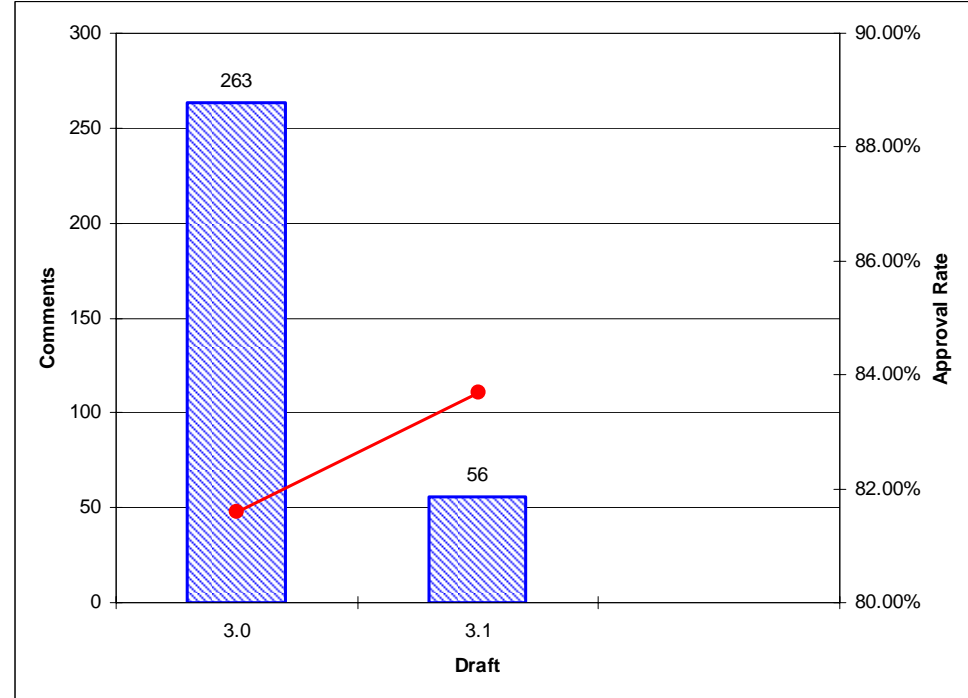
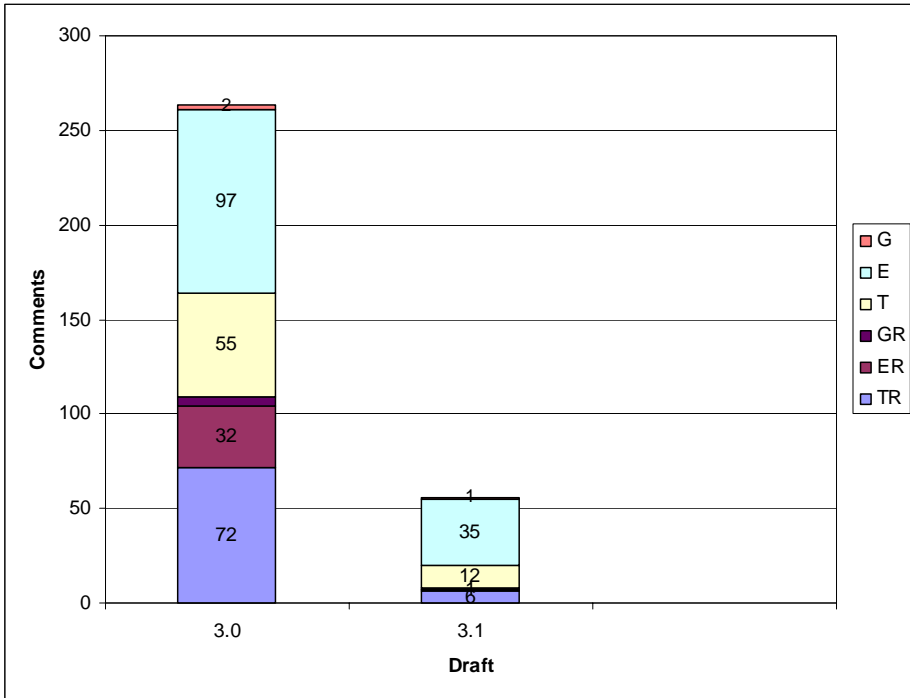
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- Generated 802.3ap/D3.1 for first sponsor ballot recirculation
  - The draft has been updated from D3.0, implemented resolution to 263 comments
  - First sponsor ballot recirculation: Oct 16 - Oct 31<sup>st</sup>
- 56 comments received on D3.1, reports posted on Nov 03rd
  - 6 TR and 12 T
  - 1 ER and 35 E
  - 1 GR and 1 G
  - Editorial team prepared proposed responses, reports posted on Sep 10th
- 18 T/TRs and 2 ER/GR to be taken up for resolution at the TF meeting
  - Notify Chief editor if any other comments need to be taken up for discussion

# Comments by clause



# Comments history



# Draft 3.1 ballot results

D3.0 Initial sponsor ballot		D3.1 sponsor ballot recirculation	
Voters	124	Voters	124
Approve	71	Approve	77
Disapprove	16	Disapprove	15
Disapprove w/o comment	1	Disapprove w/o comment	1
Abstain	7	Abstain	7
Returns	95	Returns	100
Approval rate	82%	Approval rate	84%
Abstain rate	7%	Abstain rate	7%
Response rate	77%	Response rate	81%



# Comments summary

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- Clarification of Clause 73 Auto-Neg requirements, adjust a timer value
- Clause 30 - list FEC attributes in MAU object class
- Clause 72 – training initial value, Clarification of output jitter spec
- Clarification on transition time for pattern gen, Low frequency Rx jitter tolerance test
- Channel parameters

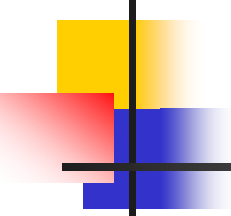


# Backup

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# D3.1 Comments summary by clause



Clause	T	TR	E	ER	G	GR	Total
00, 1-49, 30B	1		11				12
45			3	1		1	5
69, 69A, 69B	1	3	4				8
70	2		1				3
71	1		1		1		3
72	4	2	10				16
73, 73A	3	1	4				8
74			1				1
Total	12	6	35	1	1	1	56