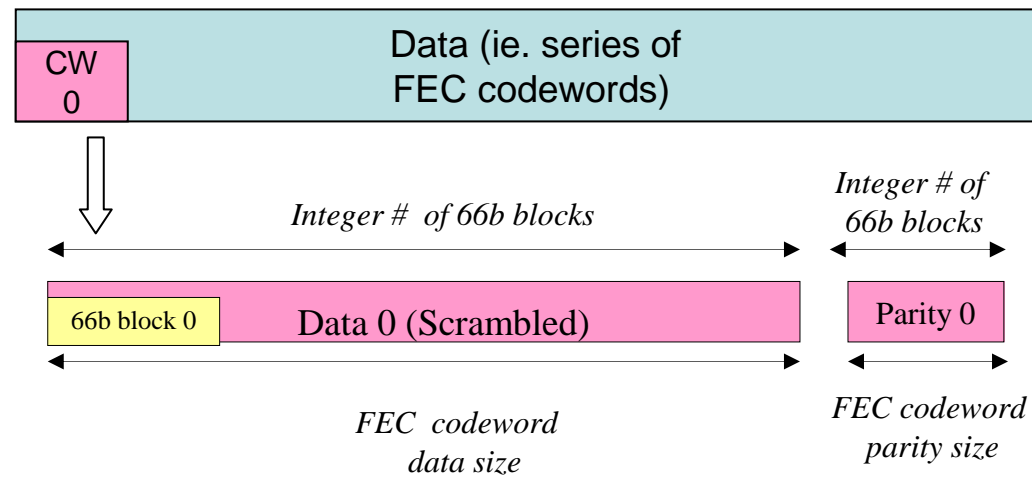


Clarification of FEC Codeword Structure

Jeff Mandin

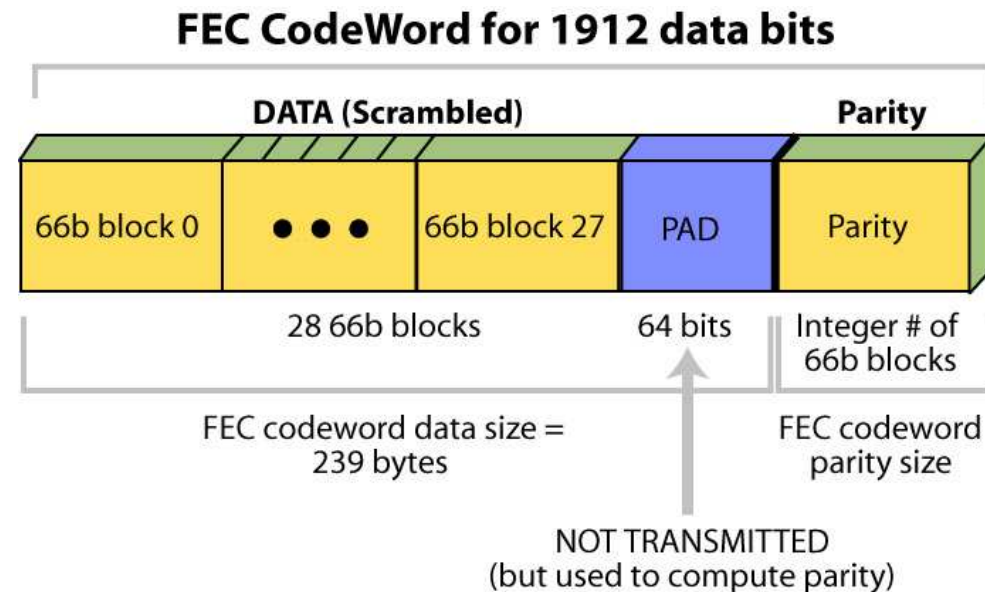
802.3av - Geneva

10GEPON datastream structure (U/S and D/S) from Baseline



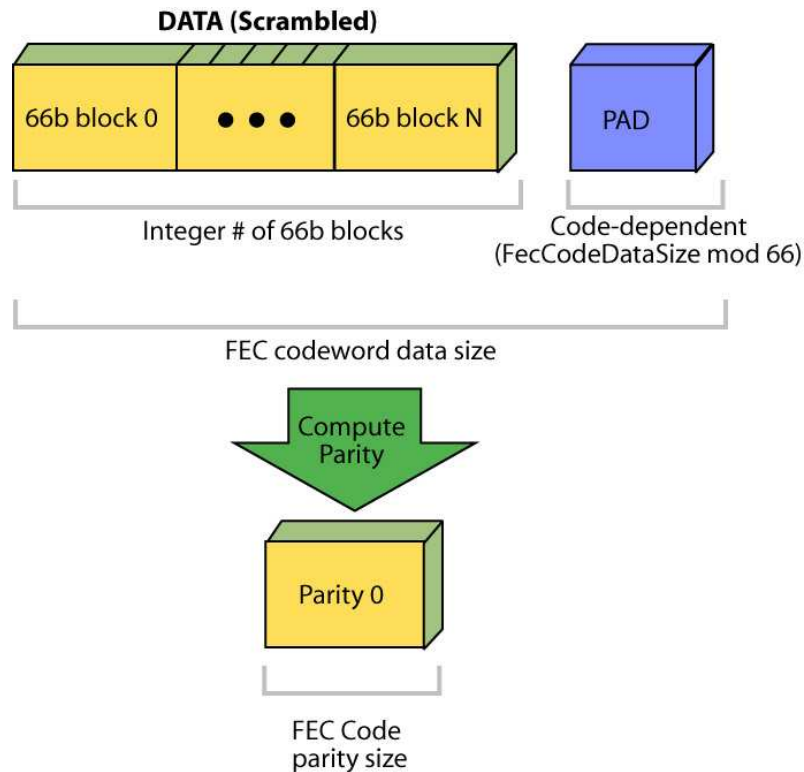
Issue

- 1) FEC Code has not yet been chosen. But the length of data input of FEC Code might not be a multiple of 66 bits.
- 2) For example, length of data in our “placeholder code” RS (239, 255) is 1912 bits
 - This is equivalent to 28 66bit blocks followed by a 64 bit pad



- 3) We need to stipulate that pad bits (if present) are used to compute FEC parity but are not themselves transmitted.

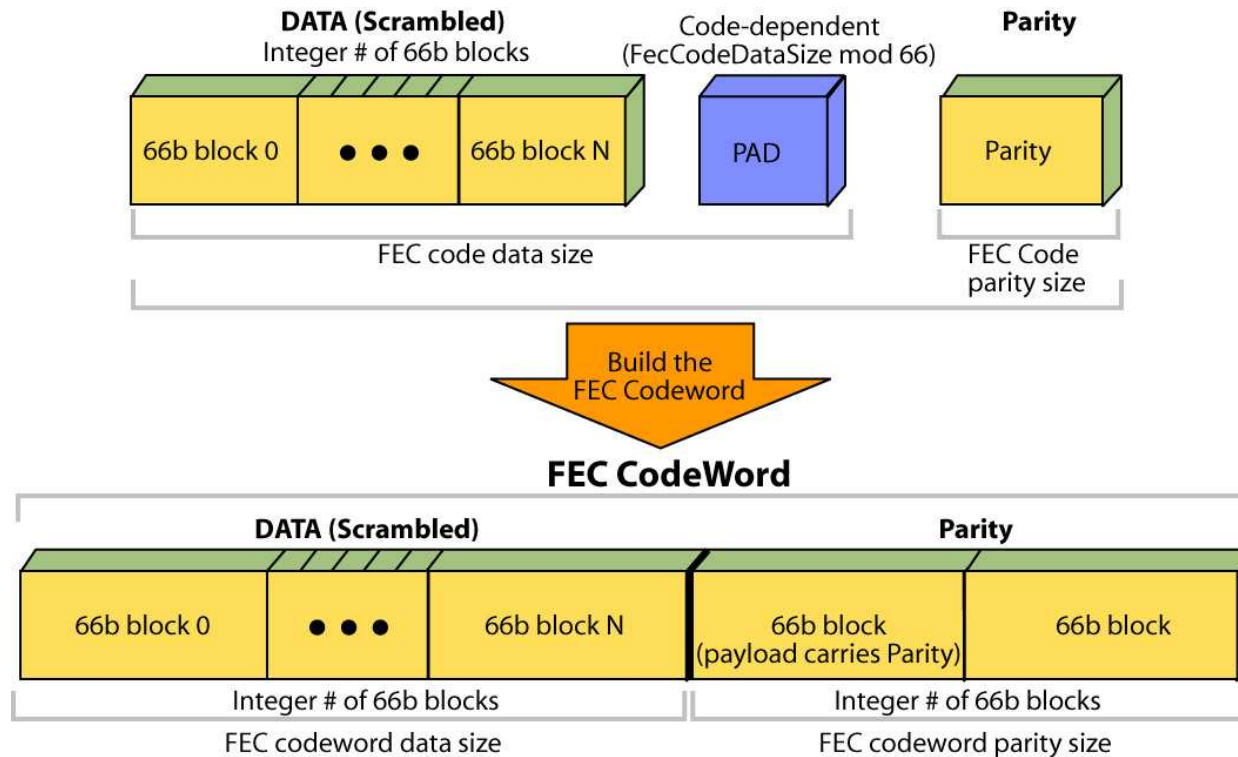
Clarification 1: Computing the FEC Parity



For the purpose of computing parity, pad of '0' bits is appended to the 66b blocks to complete the FEC codeword data size.

The pad bits are for computation only and are not transmitted.

Clarification 2: Building the FEC Codeword



The computed parity bits are carried in a series of 66b blocks called 'parity blocks'. Each parity block carries 64 bits of the computed parity in its payload.