

Table 92–1—Preamble/SFD replacement mapping

Column	Lane	Field	Preamble/SFD	Modified preamble/SFD
0	0	-	0x55	same
	1	-	0x55	same
	2	SLD	0x55	0xd5
	3	-	0x55	same
1	0	-	0x55	same
	1	LLID[15:8]	0x55	<mode,logical_link_id[14:8]> ^a
	2	LLID[7:0]	0x55	<logical_link_id[7:0]> ^b
	3	CRC8	0xd5	The 8 bit CRC calculated over offsets 3 through 7

^amode maps to TXD[7], logical_link_id[14] maps to TXD[6], logical_link_id[8] maps to TXD[0]

^blogical_link_id[7] maps to TXD[7], logical_link_id[0] maps to TXD[0]