

# 10GBase-T Active / Low-Power Idle Toggling with Sense Interval

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# Alignments with taich\_01\_0308.pdf (NEW)

- Define  $T_{\text{refresh}} = N$  LDPC frames, separate from wake up time
- Define Sense interval =  $K$  LDPC frames
- Let run-time renegotiation be TBD (in entirety)
  - Define management registers for  $N$ ,  $M$  &  $K$  to allow hooks
- Define  $N=100$ ,  $K=20$ ,  $M=4$  as base case
  - Better than 90% of savings without leakage
  - Allows for refresh block to span entire round-trip echo response
- Agree to define larger  $N$ , value TBD, subject to
  - Timing synchronization feasibility
  - Power supply recovery time
- Agree that  $T_w$  will be negotiated,  $T_w$
- Agree in principle to minimize  $N$ ,  $K$ ,  $M$  combinations
  - No further combinations than the above at this time
- Maximum recovery time with minimum  $T_w$  is  $(K+2)*320\text{nsec}$ 
  - Allows at least 2 frames for receiver to turn on and fill equalizer

pipe to avoid transient