

OIF Liaison:
CEI-25 / CEI-28 Projects
Ad Hoc:

Overview and
Initial Thoughts for Discussion

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Agenda

- Patent Policy (see <http://www.ieee802.org/3/patent.html>)
- Overview
- Presentations
- Discussion

Overview

- OIF Liaison – see http://www.ieee802.org/3/ba/public/nov09/OIF_IEEE802.3_liaison_19OCt09.pdf
- Response requested by Dec 4
- CEI-25G / CEI-28G Documents in private area
 - OIF CEI Base document - http://www.oiforum.com/public/documents/OIF_CEI_02.0.pdf
- Email from David Law, WG Chair – see <http://www.ieee802.org/3/private/email/msg00947.html>
- Note – technical discussion should be on P802.3ba reflector

Ways to Respond

1. IEEE 802.3 Response

- Technical response – will require $\geq 75\%$ at Task Force and WG
- IMHO – vehicle for macro issues where there is agreement

2. OIF Members – as encouraged by OIF, submit comments through their company's OIF voting representative

3. Non-OIF members – may submit comments to OIF (cei25comments@oiforum.com) for consideration

- Please use IEEE comment entry tool - http://www.ieee802.org/3/ba/private/Generic_Commenting_To_ol.zip

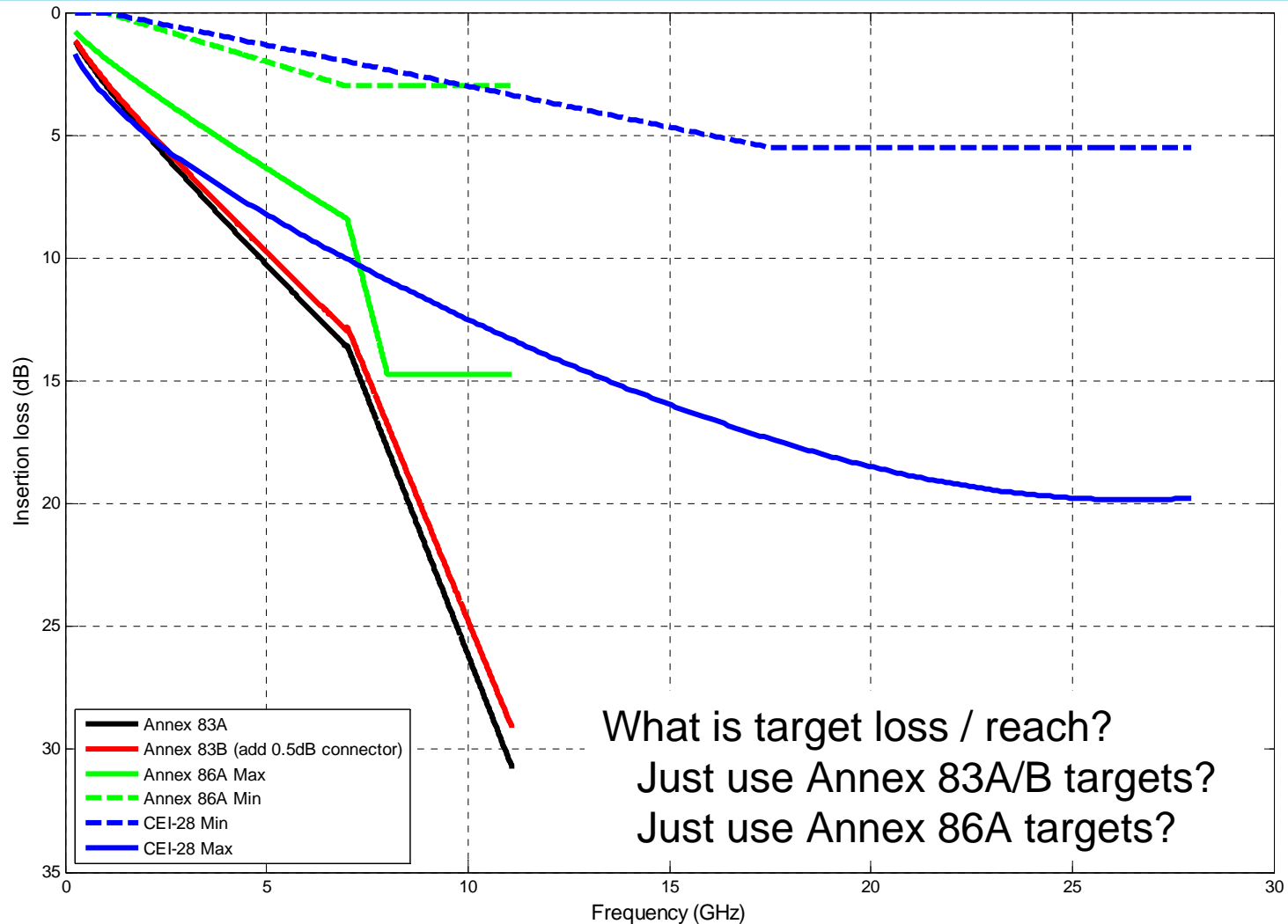
Timeline

- Ad Hoc Phone Conference – Tuesday, Nov 10, 10am – 11:30 EST
- Ad Hoc Meeting @ Plenary (pending status of p802.3ba)
- Approval of proposed liaison response on 11/19 by TF and WG
- Dec 4 for individuals to submit comments to OIF

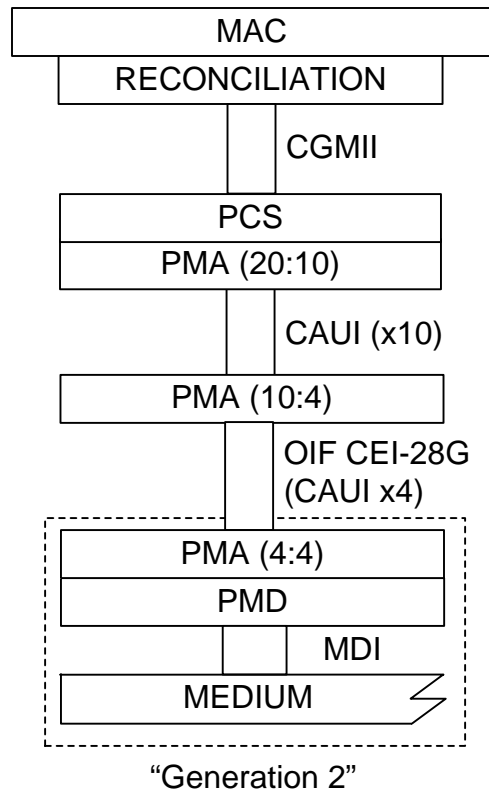
Highlight Items for Discussion

- What items can we agree on (and achieve $\geq 75\%$ consensus) by Nov for a liaison statement?
- Based on initial discussions with others-
 - Priority for development?
 - CEI-28G-SR (chip-to-chip plus one connector)
 - CEI-25G-LR (backplane applications)
 - CEI-28G-SR
 - Inclusion of chip-to-module specifications
 - Channel model
 - 25G connector discussion – defining the performance requirements
 - CEI-25G-LR
 - IMHO Getting to $\geq 75\%$ for a position in liaison response @ Nov Plenary will be difficult
 - Debates within 802.3ap may be revisited
 - » Modulation Scheme?
 - » Channel?

Comparison of Insertion Loss Specifications



Retiming & Interfaces Discussion



- Retiming for CEI was specified in prior CEI Rx model, so understood to be implied for CEI-28G-SR
- Looking at 100GBASE-L/ER4 implementations, if a CAUI-4 interface based on 25G is used, retiming at both ends is necessary
- But with CDR in module > power concerns been expressed
 - Retiming on the Tx of the module only has been suggested
 - CAUI on Tx / CPPI on Rx
 - Non standard implementation
- 16GFC defined
 - half retimed interface for SM
 - Fully retimed interface for MM