

Logic Sub-Task Force Closing Report

IEEE P802.3ba TF
New Orleans January 2009

Mark Gustlin – Cisco

Summary

- Logic Track (Clauses 00, 01, A, 4A, 80, 99, 30,30A, 30B, 45, 74, 81, 82, 83)
 - Total Comments reviewed and resolved: 141 (T/TRs)
 - All editorial comments will be handled by the editors
- All open items have been closed
- Need task force agreement to remove two left over editor's notes:
 - Location 45.2.1.7 (p. 43, l 46)
 - Location 45.2.1.1.3 (p. 41, l 50)
- Additionally, we need to remove Annex 30A & Annex 30B from the draft.

Issues Closed

- Top Issues closed:

- BIP8 added to PCS lane alignment markers (see nicholl_01_0109)

- Square wave test pattern kept, with control per lane, if not enabled send normal data

- Abstract interface connections between FEC, PMA and adjacent layers clarified

- Architectural clarifications added to the PMA

- Skew points SP5 and SP6 moved down

- MII name will be removed, always use XLGMII/CGMII

- PMA local loopback can be at any PMA

- PMA remote loopback can be at any PMA that guarantees bit transparency

- PRBS9 adopted for the short PRBS pattern

- PRBS31 lane to lane delay required