

Optical Sub-Task Force budget proposals

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Optical proposals for 40GBASE-LR4

	D 3.0	792	Agree
Table 87-7			
Total average launch power (max) dBm	8.3	8.6	
Average launch power, each lane (max) dBm	2.3	2.6	
Average launch power, each lane (min) dBm	-7	-6.7	
OMA each lane max dBm	3.5	3.8	
OMA each lane min dBm	-4	-3.7	
OMA –TDP each lane min dBm	-4.8	-4.5	
TDP each lane max dB	2.3	2.6	
RIN ₂₀ OMA (max) dB/Hz	-128	-130	
Table 87-8			
Damage threshold (min) dBm	3.3	3.6	
Average receive power, each lane (max) dBm	2.3	2.6	
Average receive power, each lane (min) dBm	-13.7	-13.4	
Receive power, each lane (OMA) (max) dBm	3.5	3.8	
Receiver sensitivity (OMA), each lane (max) dBm	-11.5	-11.5	
Stressed Rx sens OMA, each lane dBm	-9.9	-9.6	
Vertical eye closure penalty, each lane dB	1.6	1.9	

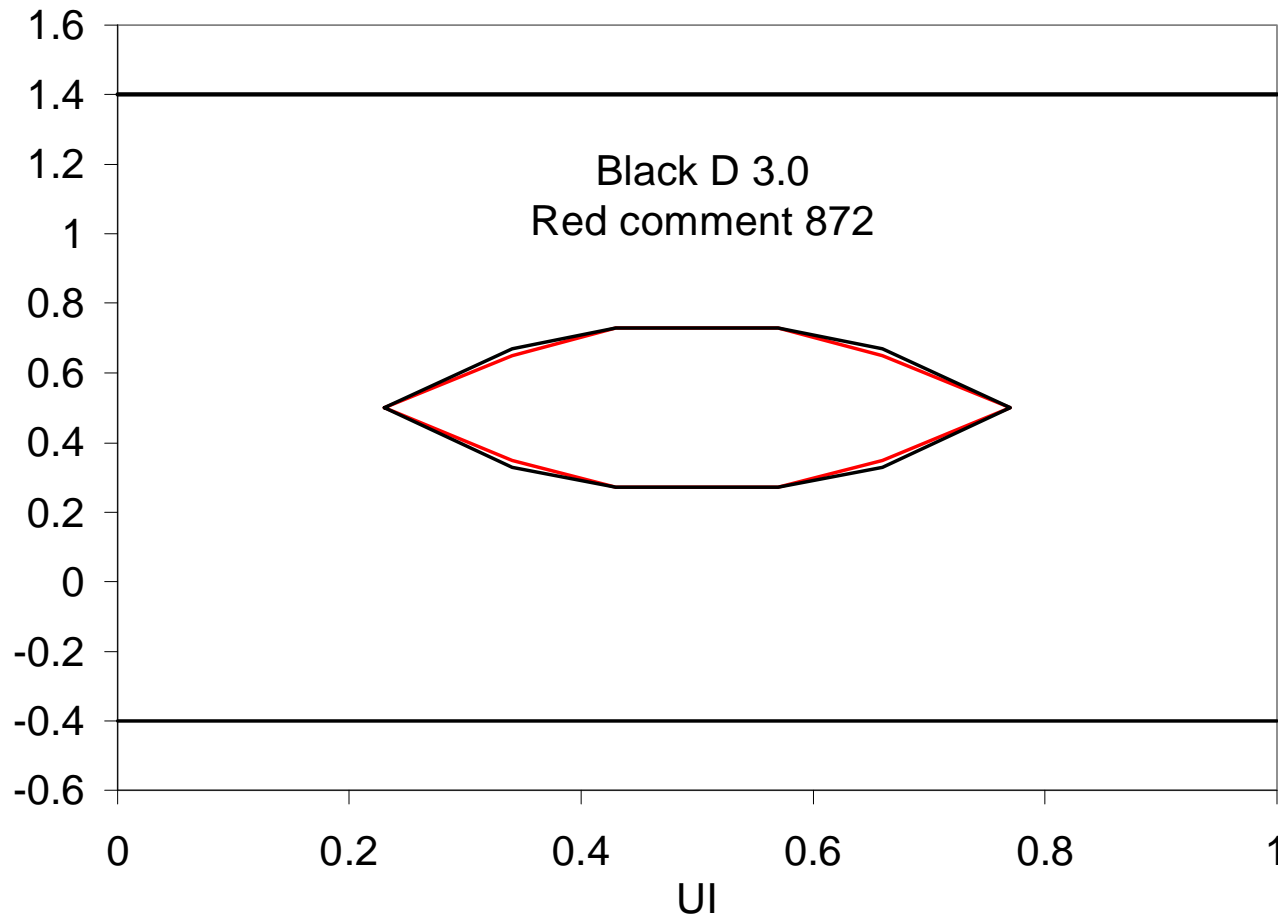
Optical proposals for 40GBASE-LR4 cont.

	D 3.0	792	Agree
Table 87-9			
Power budget (for max TDP) dB	9	9.3	
Allocation for penalties (for max TDP) dB	2.3	2.6	

Budget proposals for 40/100GBASE-SR

	D 3.0	871, 872, 873, 877	Agree
Table 86-6			
Average launch power, each lane (max) dBm	2.4	2.4	
OMA each lane (max) dBm	3	3	
TDP (max) dB	3.7	3.6	
Transmitter eye mask Y2	0.33	0.35	
Table 86-8			
Damage threshold (min) dBm	3.4	3.4	
Average power at receiver input, each lane (max) dBm	2.4	2.4	
OMA each lane (max) dBm	3	3	
Stressed eye J2 Jitter UI	0.35	0.3	
Table 86-9			
Power budget (for maximum TDP)	8.3	8.2	
Allocation for penalties (for maximum TDP)	6.4	6.3	
86.8.4.4			
Reference receiver bandwidth GHz	6.2	6.1	

Tx eye mask for 40/100GBASE-SR

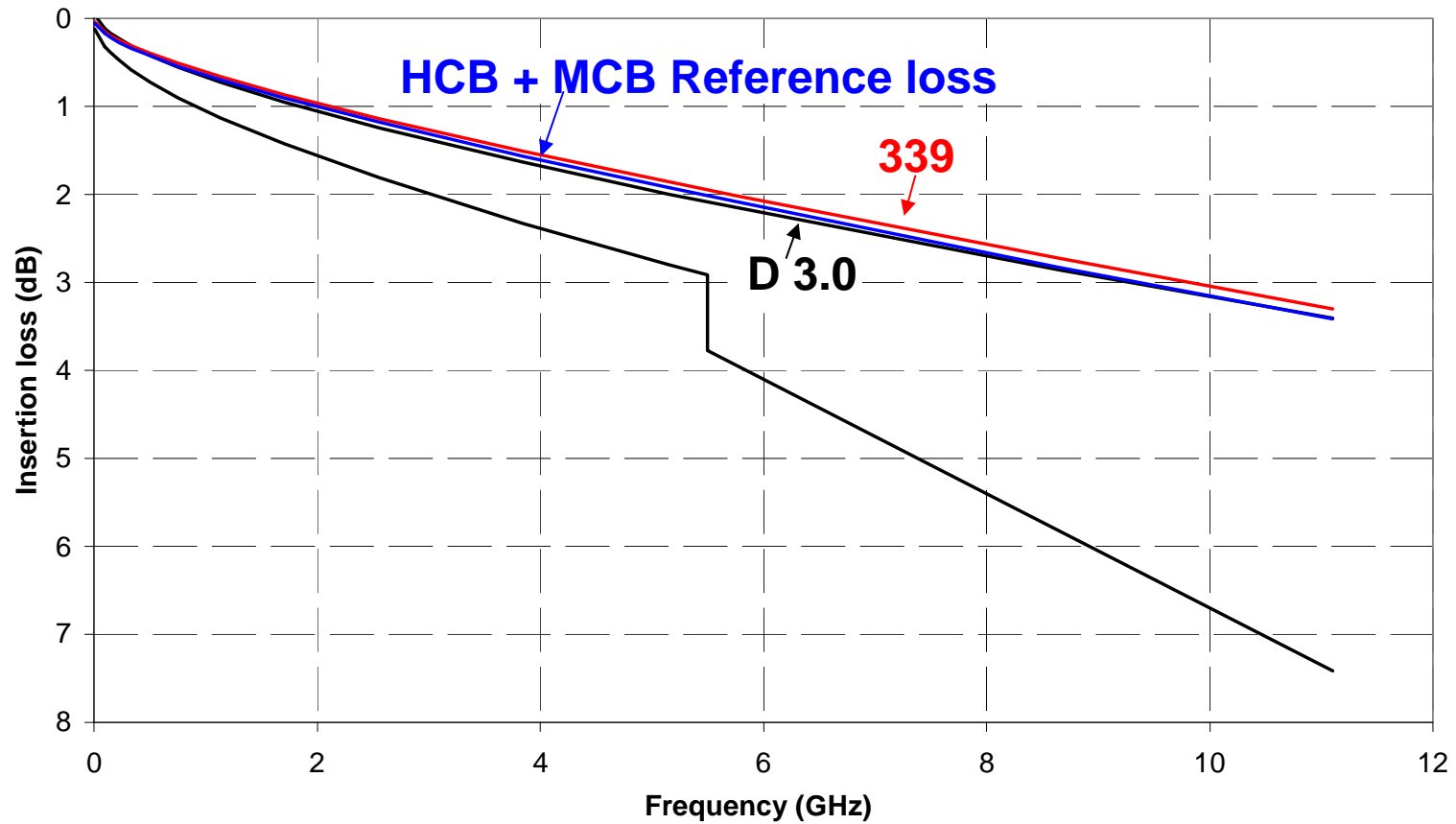


Budget proposals for nPPI

	D 3.0	793	886	Agree
Table 86A-1				
J2 Jitter output for 40G (max) UI	0.18	0.17	0.18	
J2 Jitter output for 100G (max) UI	0.18	0.18	0.18	
J9 Jitter output (max) UI	0.26	0.26	0.26	
Table 86A-2				
J2 Jitter tolerance for 40G (min) UI	0.18	0.17	0.18	
J2 Jitter tolerance for 100G (min) UI	0.18	0.18	0.18	
J9 Jitter tolerance (min) UI	0.26	0.26	0.26	
Table 86A-3				
J2 Jitter output (max) UI	0.46	0.46	0.42	
J9 Jitter output for 40G (max) UI	0.62	0.64	0.65	
J9 Jitter output for 100G (max) UI	0.62	0.62	0.65	
Table 86A-4				
J2 Jitter tolerance UI	0.46	0.46	0.42	
J9 Jitter tolerance for 40G UI	0.62	0.64	0.65	
J9 Jitter tolerance for 100G UI	0.62	0.62	0.65	

Insertion loss limits of mated HCB-MCB

This proposed change also applies to Equation 85-35



Recommended electrical channel

