

**IEEE 802.3ba OSFP Pin Mapping & Mating Faces**  
**Fogg # 537, Table 85-7**

<b>Pin</b>	<b>Symbol</b>	<b>Description</b>
1	GND	Ground
2	Tx2n	Transmitter Inverted Data Input
3	Tx2p	Transmitter Non-Inverted Data Input
4	GND	Ground
5	Tx4n	Transmitter Inverted Data Input
6	Tx4p	Transmitter Non-Inverted Data Input
7	GND	Ground
8	ModSelL	Module Select
9	ResetL	Module Reset
10	Vcc Rx	+3.3 V Power supply receiver
11	SCL	2-wire serial interface clock
12	SDA	2-wire serial interface data
13	GND	Ground
14	Rx3p	Receiver Non-inverted Data Output
15	Rx3n	Receiver Inverted Data Output
16	GND	Ground
17	Rx1p	Receiver Non-inverted Data Output
18	Rx1n	Receiver Inverted Data Output
19	GND	Ground
20	GND	Ground
21	Rx2n	Receiver Inverted Data Output
22	Rx2p	Receiver Non-inverted Data Output
23	GND	Ground
24	Rx4n	Receiver Inverted Data Output
25	Rx4p	Receiver Non-inverted Data Output
26	GND	Ground
27	ModPrsL	Module present
28	IntL	Interrupt
29	VccTx	+3.3 V Power supply transmitter
30	Vcc1	+3.3 V Power Supply
31	LPMODE	Low Power Mode
32	GND	Ground
33	Tx3p	Transmitter Non-inverted Data Input
34	Tx3n	Transmitter Inverted Data Input
35	GND	Ground
36	Tx1p	Transmitter Non-inverted Data Input
37	Tx1n	Transmitter Inverted Data Input
38	GND	Ground