

comments

Cl 31B SC 31B.3.7 P11 L 26 # 5
Thompson, Geoff GraCaSI

Comment Type ER Comment Status A

The text as shown in the draft is not true comparison text and thus does not provide balloters with an easy comparison of the modifications to the existing text.

SuggestedRemedy

Replace lines 26 through 36 with true comparison text (example provided in ballot submission)

Response Response Status U

ACCEPT IN PRINCIPLE.

The base text can be provided to the commenters on request

Cl 31B SC 31B.3.7 P11 L 30 # 4
Thompson, Geoff GraCaSI

Comment Type TR Comment Status A

(Also line 26) Unfortunately, this change not only changes the PAUSE quanta for the two PHYs, it also changes the scope of Annex 31B by deleting the text "and above". If there were existing text in the 40/100 Draft to cover "and above" in this annex, then I would have no issue with this approach. Such does not appear to be the case. It appears that the 40/100 project has chosen to NOT include it PAUSE quanta parameters in a place that does not align to the rest of the standard without even as much as acknowledgement in 31B that it is doing so. This extension the scope of this corrigendum beyond the simple fix that is required is unfortunate and will cause delay.

SuggestedRemedy

Any one of the below would be acceptable to me
(1) restore the term "and above" (This would place the burden of dealing with this text into P802.3ab, where it belongs)
(2) Add a note at line 36 that says something like: [Note: (To be removed when P802.3ba text is inserted into this Annex) For speeds above 10 Gb/s, new text and parameters will be provided by project P802.3ba.]

Response Response Status U

ACCEPT IN PRINCIPLE.

Several comments have been entered against P802.3ba to consider modifications to Annex 31B independent of this project.

Cl 31B SC 31B.3.7 P11 L 30 # 34
Ganga, Ilango Intel

Comment Type TR Comment Status R

The updated text for PAUSE reaction timing delay for 10GBASE-T is specified as 74 pause quanta.

The reaction timing delay caculations include delay for MACCTL/MAC/RS (16 PQ), XGXS/XAUI (8 PQ) and 10GBASE-T PHY (50 PQ), hence total of 74 PQ.

However in future, stations may use serial interface connections instead of XGXS/XAUI connections. Hence if the updated delay calculations include a serial interface implementation then we do not have to revisit this number in future revisions of 802.3.

Here is the rationale based on delay numbers in Table 44-2:

The reaction timing delay caculations include delay for MACCTL/MAC/RS (16 PQ), 10GBASE-R PCS (7 PQ + 7 PQ, two times), Serial R PMA and PMD (2 PQ + 2 PQ, two times - R Cu serial PMA and PMD delay is 2 PQ) and 10GBASE-T PHY (50 PQ), hence total of 84 PQ.

SuggestedRemedy

31B.3.7 Change sentence in line 30 from "seventy-four" to eighty-four" as follows:

At operating speeds of 10 Gb/s, a station with a 10GBASE-T PHY shall not begin to transmit a (new) frame more than eighty-four pause_quantum bit times after the reception of a valid PAUSE frame that contains a non-zero value of pause_time, as measured at the MDI.

Response Response Status U

REJECT.

The port type being described is not an IEEE 802.3-2008 Ethernet standard port-type and hence outside the scope of the Cor.

comments

Cl **31B** SC **31B.4.6** P **11** L **40** # **36**
Ganga, Ilango Intel

Comment Type **TR** Comment Status **A**

Update PICS in 31B.4 to be consistent with the changes to 31B.3.7

Also "MIId" missing from Major capabilities/options in 31B.4.3 (See 31B.4.6 TIM5)

SuggestedRemedy

Change 31B.4.3 last row of table as follows:

*MIId At operating speeds (strikethrough: above 100 Mb/s) of 1000 Mb/s

31B.4.3 Insert the following two rows to the end of table:

{Item} *MIId {Feature} At operating speeds of 10 Gb/s with PHY types other than 10GBASE-T {Subclause} 31B.3.7 {Status} Optional

{Item} *MIId {Feature} At operating speeds of 10 Gb/s with PHY types of 10GBASE-T {Subclause} 31B.3.7 {Status} Optional

Change 31B.4.6, TIM5 as follows:

TIM5: Measurement point for station at 10 Gb/s (strikethrough: "or greater") with PHY types other than 10GBASE-T.

Insert TIM6 as follows:

{Item} TIM6 {Feature} Measurement point for station at 10Gb/s with PHY types of 10GBASE-T. {Subclause} 31B.3.7. {Value/Comment} Delay at MDI <= 74 (or 84) x pause_quantum bits. {Status} MIId:M. Support: N/A[] Y/N

Response Response Status **U**

ACCEPT IN PRINCIPLE.

There is no need to change the major options as it is not broken. Change 31B.4.6, TIM5 as follows:

TIM5: Measurement point for station at 10 Gb/s (strikethrough: "or greater") with PHY types other than 10GBASE-T.

Insert TIM6 as follows:

{Item} TIM6 {Feature} Measurement point for station at 10Gb/s with PHY types of 10GBASE-T. {Subclause} 31B.3.7. {Value/Comment} Delay at MDI <= 74 bits. {Status} MIId:M. Support: N/A[] Y/N