C/ 80 SC 80.2.2 Lusted, Kent	P <b>33</b> Intel	L <b>8</b>	# 22	<i>Cl</i> <b>92</b> Dudek, Mi	SC <b>92.7.1</b> ke	Р <b>90</b> QLogic	L <b>48</b>	# 212
	Comment Status D se 83 as the only PMA for a 1	00GBASE-R de	vice.		e 92-4 The Test	Comment Status <b>D</b> points TP0 to TP1 and TP4 to ponector pairs between eg TF		tch the description.
see P802.3bh D3.1, se	ect6, page 62, line 53			Suggestee	dRemedy			
SuggestedRemedy				Chang	ge the test points	s on this row from TP1 to TP2	and from TP4 t	o TP3
	sentence of first paragraph fr nd the PMA specification defi <i>Response Status</i> <b>W</b> IN PRINCIPLE.			Chang	OSED ACCEPT	Response Status W w 3 from "TP0 to TP1" to "TP	0 to TP2" and fr	om "TP4 to TP5" to
Change to "and the PM	A specifications defined in C	lause 83 and C	lause 94"					
C/ 92 SC 92.7.1 Dawe, Piers	P89 IPtronics	L <b>4</b> 1	# 141	<i>Cl</i> <b>92</b> Dawe, Pie	SC <b>92.7.1</b> rs	P90 IPtronics	L <b>7</b>	# 161
Comment Type ER "Functional specification layer does. This text is	Comment Status D ons" are brief, high-level (logic s going too far into the electric beginning of the "Definitions o	cal detail which i	is better placed	so on. MCB t Suggestee Make	292-2 shows TP This is at odds trace loss, TP0 a dRemedy the arrow for TP	Comment Status <b>D</b> 0 just by the PMD transmit fu with the text: TP1-4 are offse and TP5 are not offset. 0 and TP5 point exactly at the the connectors. Thanks!	et from the conne	ector by the HCB or
	ne material between line 41 lin 2 "Annex 92A." into the chan s" subclause.			Proposed	Response	Response Status W		
Proposed Response PROPOSED REJECT.	Response Status W					P0 and TP5 as close to end o sembly text fixture loss; move		
92.7.1 text describes th in Table 92-4 100GBA	ne link block diagram and sup SE-CR4.	oports the define	ed test point definitions					

C/ 92 SC 92.7.1

/ 92 SC 92.7.8	P <b>92</b>	L16	# 165	C/ 92	SC 92.8	P <b>94</b>	L <b>1</b>	# 140		
awe, Piers	<b>IPtronics</b>			Dawe, Piers	i	IPtronics				
omment Type TR Comme	nt Status D			Comment T	ype ER	Comment Status D				
This (a PMD clause) says "Local loopback mode shall be provided by the adjacent PMA (see 83.5.8) as a test function to the device." That's impossible: only the PMA clause can tell the PMA what to do. "Device" is not a standards word (too vague).				The layout of these clauses makes them hard to use, with PMD specifications on the one hand, and measurement and definition detail on the other, muddled together.						
				SuggestedF	Remedy					
Why is this loopback needed?					,	t of a PMD clause, with subclear Definition of parameters and n				
SuggestedRemedy					e subciause. L esponse	Response Status W	leasurement men	inous.		
83.5.8, PMA local loopback mode, adjacent to the PMD for 40GBASE	-KR4, 40GBASE	CR4, and 100BAS	SE-CR10 PMDs."		SED REJECT	,				
If it's really necessary, explain in the in 83.5.8, and here in 92.7.8, chan- loopback mode (see 83.5.8) as a to Otherwise, chnage to "The PMA ac	ge to "The PMA a est function." djacent to the PM	idjacent to the PM	ID provides PMA local	subclau	ses for link se	cture follows Clause 85 provio gment parameters etcPropo ed changes to implemet in the	sal insufficently			
loopback mode (see 83.5.8) as a to Similarly for 93.7.8 and 94.2.9.	est function."			C/ 92	SC 92.8.3	P <b>94</b>	L1	# 170		
	e Status W			Dawe, Piers	i	IPtronics				
PROPOSED ACCEPT IN PRINCIP				Comment T	ype ER	Comment Status D				
The commenter correctly points out the normative requirement is already stated in 83.5.8. It sets the precedent that loopback is required for 40 Gb/s and 100 Gb/s copper PHYs. Change the first sentence of 83.5.8 as follows.			"92.8.3 Transmitter characteristics" sounds like a datasheet. Please write in normative standards language! Also follow the house style of 100GE unless improving on it. SuggestedRemedy							
									"PMA local loopback shall be provi	ded by the PMA a
KR4, 40GBASE-CR4, 100GBASE-	CR10, 100GBAS	E-KR4, and 1000	BASE-CR4 PMDs."	Proposed R	esponse	Response Status W				
Change the first sentence of 92.7.8	3 and 93.7.8 to:			PROPC	SED REJECT					
"Local loopback mode is provided by the adjacent PMA (see 83.5.8) as a test function."	Charact Table 9: Table 9: Table 9: Table 9: Table 5: Table 5:	3-4 3-6 4-4 4-6 8-3	n normative standards langua	ıge; see						

C/ 92 SC 92.8.3

C/         92         SC         92.8.3         P94         L13         # 169           Dawe, Piers         IPtronics	C/ 92         SC 92.8.4.5         P106         L49         # 171           Dawe, Piers         IPtronics
Comment Type       ER       Comment Status       D         Trying to define the nominal unit interval is not necessary, very difficult to do precisely, and not usual: most PMD clauses including 93 and 94 don't.       SuggestedRemedy         Delete this row, and in Table 92-7. In 92.8.3.9 and 92.8.4.4, change "nominally" to "approximately" or delete the sentences.	Comment Type         T         Comment Status         D           "The 100GBASE-CR4 receivers are AC coupled. AC coupling shall be part of the receiver function for Style-2 100GBASE-CR4 connectors. For Style-1 100GBASE-CR4 plug connectors, the receive lanes are AC coupled; the coupling capacitors shall be within the plug connectors."           But, isn't there only one connector type at present, with the AC coupling in the cable, therefore not needed in the receiver?
Proposed Response Response Status W PROPOSED ACCEPT IN PRINCIPLE.	SuggestedRemedy Delete the first two sentences and "Style-1".
Unit UI used extensively throughout clause. In addition, subclauses include percentage of UI e.g., 92.8.3.3 Transmitter output waveform .	Proposed Response Response Status W PROPOSED ACCEPT.
In 92.8.3.9 change "nominally" to "approximately". In 92.8.4.4 delete nominal.	Use suggested remedy.
C/         92         SC         92.8.4.5         P106         L49         # 153           Dawe, Piers         IPtronics         IPtroptrop         IPtroptrop         IPtronics <td>C/         92         SC         92.8.4.5         P106         L49         # 219           Dudek, Mike         QLogic</td>	C/         92         SC         92.8.4.5         P106         L49         # 219           Dudek, Mike         QLogic
Comment Type         T         Comment Status         D           "The low frequency 3 dB cutoff of the AC coupling shall be less than TBD kHz." On the one hand, the signalling rate is 2.5x higher. On the other, the signal integrity challenge is	Comment Type <b>T</b> Comment Status <b>D</b> The Style 2 connector isn't to be used for 100G-CR4 and we haven't defined different Style connectors.
much higher. Anyway, one would expect backwards compatibility of a passive cable. <i>SuggestedRemedy</i> 50 kHz, or perhaps lower.	SuggestedRemedy Delete the sentence "AC coupling shall be part of the receive function for Style-2 100GBASE-CR4 connectors." and delete "style 1" in the next sentence.
Proposed Response Response Status W PROPOSED ACCEPT IN PRINCIPLE.	Proposed Response Response Status W PROPOSED ACCEPT IN PRINCIPLE.
In 92.8.4.5 replace TBD with 50 kHz.	See response comment #171.

C/ 92 SC 92.8.4.5

6/ 92 SC 92-1	P <b>85</b>	L	# 187	CI 93	SC 7.1	2	P <b>130</b>	L33	# 97
ela, Oren	Mellanox Teo	chnologies		Slavick, Jef	f		Avago Techno	logies	
ela, Oren <i>comment Type</i> <b>T</b> Need to add CL72 to the treferenced to CL72 <i>buggestedRemedy</i> Add to table 92-1: 72-PMD control required	Mellanox Tec Comment Status D able due to startup protoc Response Status W ublayer is not required to be 100GBASE-CR4 PMD	col and the PMD	control which is a 100GBASE-CR4 brates a PMD control	Slavick, Jef Comment T Clause The up combin evaluat simulta clear de request Suggestedf Add the Each la receivin Proposed R PROPC It is agr parallel constra That sa The imp reports individu	f 7ype <b>T</b> 72 allows date for e e the curring if it's a neously the efinition o ts becaus Remedy e following a response DSED RE reed that coefficient ints. aid, while plication i corresponal coefficient sely, an a	R s for mu ach tap rent ove allowed hat caus f what s e it doe g text to only req onse for JECT. Clause nt updat Clause s that an nding to ient upc		logies requests to occo nother. There and are used to multiple reques nd it's operatin example servic ounds, or you co end of the first p oefficient at a t another reques us report fields peak or steady update reques annot deal with rallel coefficier	eur at the same time. are variables that by each TAP when its are made g range, there is no be one or two of the can deny all. baragraph. ime and shall wait until st. should be set when a y state voltage ts, it does not require it. a ambiguity in status at updates may send
				coefficio Therefo coefficio	ent updat ore, the in ent updat	es in pa itiator o es seria		ability to choos	se whether to send
							ot provide justification constrated remedy.	ain the impleme	entation in the manner

C/ 93 SC 7.12

C/ 93 SC 8.1 P131 L34 # 203	C/ 93 SC 93.7.12 P130 L31 # 175
Hidaka, Yasuo Fujitsu Laboratories of	Dawe, Piers IPtronics
Comment Type T Comment Status D	Comment Type T Comment Status D
Table 93-4. Total jitter excluding DDJ is defined as 0.28UI. It was defined as 0.25UI excluding DDJ in clause 85. It was defined as 0.28UI including DDJ in clause 72. OIF define it as 0.28UI including DDJ. We should change it to 0.25UI as it excludes DDJ.	This says "Each lane of the 100GBASE-KR4 PMD shall use the same control function as 10GBASE-KR, as defined in 72.6.10." and 72.6.10 says "The control channel is signaled using differential Manchester encoding (DME) at a signaling rate equal to one quarter of the 10GBASE-KR signaling rate. Since each DME symbol contains two DME transition positions and each transition position is four 10GBASE-KR UI, one control channel bit is transmitted every eight 10GBASE-KR UI. Do you mean use the same training frames run 2.5 times faster (including DME 2.5 times faster) or DME at rate stated above but PRBS 2.5x faster?
SuggestedRemedy	SuggestedRemedy
Change 0.28UI with 0.25UI.	Please make this clear.
Proposed Response Response Status W	Proposed Response Response Status W
PROPOSED REJECT.	PROPOSED ACCEPT IN PRINCIPLE.
Pending discussion by the Task Force and a measurement of the consensus to make the proposed change.	The timing parameters in 72.6.10 should be scaled by a factor of 0.4 for 100GBASE-KR4 to
C/ 93 SC 92.8.3.8 P135 L48 # 154	account for the reduction in the unit interval.
Dawe, Piers IPtronics	Add the following sentence the end of the first paragraph of 93.7.12.
Comment Type         TR         Comment Status         D           This says "the measurement bandwidth should be at least TBD GHz". But a definition needs to be precise and not biased: we can't say whether more bandwidth is "better", or less bandwidth. We give the reader the hint in the next sentence that it may not be critical. (I don't think it makes a huge difference as long as it's a reasonable linear-phase response.)	"The training frame structure used by the 100GBASE-KR4 PMD control function shall be as defined in 72.6.10 with the exception that 25.78125 GBd symbols replace 10.3125 GBd symbols and 100GBASE-KR4 UI replace 10GBASE-KR UI, i.e. all times are multiplied by a factor of 0.4."
	Make similar changes to 92.7.12.

## SuggestedRemedy

Change "For DDJ measurements, the measurement bandwidth should be at least TBD GHz." to "The waveform is observed through a fourth-order Bessel-Thomson response with a bandwidth of 33 GHz."

Proposed Response Response Status W PROPOSED ACCEPT IN PRINCIPLE.

See comment #146.

Make similar changes to 92.7.12.

C/ 93 SC 93.7.12

C/ 93         SC 93.8.1         P131         L         # 145           Dawe, Piers         IPtronics	C/         93         SC         93.8.1.2         P131         L50         # 143           Dawe, Piers         IPtronics					
Comment Type <b>T</b> Comment Status <b>D</b> For robustness, it would help if there were something like a minimum VMA spec (say 0 to 50 mV) so that the Tx would never set the signal to invert if the Rx asked for one too many tap weight changes.	Comment TypeTRComment StatusDA pattern with a 2 UI period is not a "square wave":52.9.1.2 Square wave pattern definitionA pattern consisting of four to eleven consecutive ones followed by an equal run of zerosmay be used as a square wave.					
SuggestedRemedy	Table 86-11-Test patterns					
Consider adding a minimum VMA spec, or similar, so that Tx can never invert the signal or set all its the taps to zero when still technically transmitting.	Square wave (8 ones, 8 zeros) And this is a bad choice: the true peak-to-peak voltage could be significantly larger. We					
Proposed Response Response Status W	really want to contain the VMA or steady-state voltage because more of that passes thou a lossy channel.					
PROPOSED ACCEPT IN PRINCIPLE.	SuggestedRemedy					
The PMD control function gives the receiver complete control of the transmit equalizer or,	Use a mixed frequency pattern: PRBS31 or scrambled idle, possibly PRBS9.					
stated another way, several lengths of enough rope with which to hang itself.	Proposed Response Response Status W					
While the commenter points out the extreme case where receiver forces that transmitter steady state voltage to zero, or even opposite the symbol polarity, for a given channel there likely exists other settings that yield the same effect which is the inability to effectively communicate.	PROPOSED ACCEPT IN PRINCIPLE. The test patterns that may be provided by the PMA are PRBS9, PRBS31, and a square wave test pattern with a period of 16 UI. It would be beneficial to base the requirements on one of these patterns or scrambled idle.					
<ul><li>When this happens, the receiver is given multiple escape routes such as sending preset or initialize to the transmitter in order to return to a known state.</li><li>So, while a minimum VMA specification could eliminate one problematic case, it does not solve the problem of an errant algorithm sending the transmitter into a bad state. Given</li></ul>	While there is no test pattern that is entirely alternating 1 and 0 symbols, this pattern be found in either the PRBS9 or PRBS31 test pattern. PRBS9 is a convenient test pa since it is used to test transmit equalizer compliance.					
this, it may be preferrable to not impose such a constraint since these constraints, as pointed out by comment #97, can be problematic for some algorithms.	Also note that no test pattern is defined for DC or AC common-mode output voltage and DC or AC common-mode output voltage requirements should apply regardless of the transmit equalizer setting.					
The merits of the proposed specification should be discussed by the Task Force.	Change the second and third paragraph of 93.8.1.2 to: "The peak-to-peak differential output voltage shall be less than or equal to 1200 mV regardless of the transmit equalizer setting. The peak-to-peak differential output voltage shall be less than or equal to 30 mV when the transmitter is disabled (refer to 93.7.6 and 93.7.7)."					

"The DC common-mode output voltage shall be between 0 V and TBD V with respect to signal ground. The AC common-mode output voltage shall be less than or equal to 12 mV RMS with respect to signal ground. Common-mode output voltage requirements shall be met regardless of the transmit equalizer setting."

Add the following paragraph to end of 93.8.1.2: "Differential and common-mode signal levels are measured with a PRBS9 test pattern."

C/ 93 SC 93.8.1.2

I 93         SC 93.8.1.2         P131         L 51         # 146	C/ 93 SC 93.8.1.3 P132 L21 # 85				
awe, Piers IPtronics	Moore, Charles Avago Technologies				
Comment Type TR Comment Status D	Comment Type TR Comment Status D				
At present, this and other signal parameters are specified as if observed in an infinite bandwidth. At these rates, that's just too expensive. And noisy.	Tx output return loss is TBD, we need values for equations (93-1) and (93-2)				
uggestedRemedy	SuggestedRemedy				
Define output voltage, transition time, DCD, TJ, AC common-mode output voltage and more as observed through a 33 GHz fourth-order Bessel-Thomson response. (Someone with a much faster scope can use a software filter for most parameters, which would give great accuracy.)	use: DifferentialReturnLoss(f) = 10 x log10(( 0.026 + (f/32)^2) / (1 + f/32)^2)) dB, 0.05 <f<20 (93-1)<br="">CommonModeReturnLoss(f) =</f<20>				
roposed Response Response Status W	6 dB, 0.05 <f<20 (93-2)<="" td=""></f<20>				
PROPOSED ACCEPT IN PRINCIPLE.	f in GHz				
The lack of a recommendation on measurement bandwidth does not imply that the bandwidth is prescribed to be infinite, only that no recommendation on the bandwidth (or filter shape for that matter) is made.	Proposed Response Response Status W PROPOSED ACCEPT IN PRINCIPLE.				
It is agreed that if such a filter were to be defined, it should be common to all measurements.	Pending discussion by the Task Force and a measurement of the consensus to make the proposed change.				
Task Force should discuss whether or not such a filter needs to be defined, and if so, if a 33 GHz Bessel-Thompson filter the correct filter.	C/ 93         SC 93.8.1.3         P132         L22         # 65           Mellitz, Richard         Intel Corporation				
7 93 SC 93.8.1.2 P132 L2 # 155	Comment Type TR Comment Status D Resolve Return loss TBD				
awe, Piers IPtronics	SuggestedRemedy				
Comment Type         TR         Comment Status         D           Need to define the measurement filter for AC common-mode output voltage. It is convenient (lower cost) if it is the same as for DDJ and so on.         It is the same as for DDJ and so on.	Tie return loss to channel specification proposal presentation by Mellitz, Moore, Dudek, Li, et al supported with a presentation for why the time domain method is better and how it works, by Moore, Ran, Mellitz, et al. At time of this comments file names and requestor have not been finalized.				
"The signal is observed through a fourth-order Bessel-Thomson response with a bandwidth	Proposed Response Response Status W				
of 33 GHz."	PROPOSED ACCEPT IN PRINCIPLE.				
roposed Response Response Status W PROPOSED ACCEPT IN PRINCIPLE.	Refer to comment #85.				
PROPOSED ACCEPT IN PRINCIPLE.					

C/ 93 SC 93.8.1.3

C/ 93 SC 93.8.1.5.1 Dawe, Piers	P <b>134</b> IPtronics	L19	# 147	C/ <b>93</b> Mellitz, Richa	SC 93.8.2.1 Ird	P <b>136</b> Intel Corporation	L <b>22</b> on	# 63
	omment Status D			Comment Ty		Comment Status D		
This isn't a test spec. No "sh	nall be verified" or "shal				Return loss TBE			
the thing comply - it might be 93.8.1.4 Transition time is no		or batch testing	. The wording in	SuggestedRe	emedv			
uggestedRemedy	cer.			Tie retur	n loss to channe	el specification proposal pres		
Change "The steady state vo	ltage and linear fit puls	e neak values s	hall be verified after the		ported with a pre / Moore, Ran, N	esentation for why the time of	lomain method	is better and how it
transmit equalizer coefficient	s have been set to the	"preset" values.	" to "The steady state			file names and requestor h	ave not been fir	nalized.
voltage and linear fit pulse pe when the transmit equalizer of				Proposed Re	sponse	Response Status W		
	sponse Status W	set to the prese		PROPOS	SED ACCEPT IN	N PRINCIPLE.		
PROPOSED ACCEPT IN PR				Pofor to	comment #86.			
				Kelei lu	comment #00.			
The suggested remedy adds paragraphs. Replace the text			dant with subsequent					
"The steady state voltage vf i (refer to 85.8.3.3 step 3). The and less than or equal to 0.6 "preset" values.	e steady state voltage s	shall be greater t	han or equal to 0.4 V					
The peak value of p(k) shall t have been set to the "preset"	5	f after the transm	nit equalizer coefficients					
7 93 SC 93.8.2.1	P136	L <b>21</b>	# 86					
oore, Charles	Avago Techno	biogies						
omment Type <b>TR</b> Co Rx output return loss is TBD,	omment Status D	untiona (02.2) a	rd(02.4)					
	we need values for eq	uations (95-5) a	nu (93-4)					
uggestedRemedy use:								
DifferentialReturnLoss(f) =								
10 x log10(( 0.026 + (f/32)^2)	) / (1 + (f/32)^2)) dB, 0.0	05 <f<20 (93-3)<="" td=""><td></td><td></td><td></td><td></td><td></td><td></td></f<20>						
CommonModeReturnLoss(f) 6 dB, 0.05 <f<20 (93-4)<="" td=""><td>=</td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></f<20>	=							
f in GHz								
roposed Response Res	sponse Status W							
PROPOSED ACCEPT IN PR	RINCIPLE.							
Pending discussion by the Ta proposed change.	ask Force and a measu	irement of the co	onsensus to make the					
YPE: TR/technical required ER	•	• •	I T/technical E/editorial G NSE STATUS: O/open W/	0		C/ 93 SC 93.	/	Page 8 of 13 7/23/2012 11:09:

SORT ORDER: Clause, Subclause, page, line

· · ·	
C/ 93         SC 93.8.2.2         P136         L42         # 88           Moore, Charles         Avago Technologies         Avago Technologies         # 88	C/         93         SC         93.8.2.2         P137         L19         #         61           Mellitz, Richard         Intel Corporation         Intel Corporation         Intel Corporation         Intel Corporation
Comment Type TR Comment Status D Receiver used in clause 93 is a package PHY, where clause 85 receiver is defined at a bulkhead connector. Using procedure defined in 85.8.4.2 in not appropriate, use annex 69A instead.	Comment Type <b>TR</b> Comment Status <b>D</b> Since FEC changes the minimum BER applied broad band noise should be constrained with an appropriate crest factor SuggestedRemedy
SuggestedRemedy change: "Receiver interference tolerance is characterized using the procedure defined in 85.8.4.2" to:	Add entry in table after Applied RMS noise for "Applied Crest factor" are the like. Suggested value for is erfcinv(2*minimum BER)*sqrt(2). This could go into Annex 69A. Proposed Response Response Status <b>W</b>
"Receiver interference tolerance is characterized using the procedure defined in Annex 69A." Change Annex 69A.2.2 to allow definition of channel loss either in terms of ~mTC and bTC or a0, a1, a2, and a4. Delete reference to channel noise which is not defined.	PROPOSED REJECT. The response to this comment assumes that the basis of the interference tolerance test is changed to Annex 69A (see comment #88).
Proposed Response Response Status W	The crest factor of the broadband noise is specified in 69A.2.3 to be no less than 5.
PROPOSED ACCEPT IN PRINCIPLE.	The commenter does not make it clear why the existing crest factor specification is inappropriate.
The parameters listed in the table are not an exact fit to the test procedure described in either Annex 69A or 85.8.4.2. However, Annex 69A appears to be the closer fit.	C/ 93 SC 93.8.2.2 P137 L3 # 78
Change the reference to Annex 69A as proposed in the suggested remedy and implement the following changes.	Moore, Charles     Avago Technologies       Comment Type     T     Comment Status     D       table 93-7 is technically imcomplete: full of TBD's
<ol> <li>Neither "Channel noise" nor "TX-RX re-reflection noise are defined terms so delete this row from Table 93-7 as suggested.</li> </ol>	SuggestedRemedy
2. Use the test channel calibration methodology from 85.8.4.2.3 in place of what is described in 69A.2.2. This may be accomplished by adding a new subclause to Annex 69A	replace TBD's with values from moore_02A_0312.pdf page 30. If we wish to use a_n values in the same way as 92.10.2 the numbers from moore_02A_0312.pdf page 30 which are expressed in Napier and Hz will have to be converted to dB and GHz.
or defining an exception in 93.8.2.2 (favoring the latter).	Proposed Response Response Status W
3. The "channel insertion loss at 12.89 GHz" is not used in 85.8.4.2.3 and thus its role must be defined or the parameter should be deleted.	PROPOSED ACCEPT IN PRINCIPLE.
	Pending discussion by the Task Force and a measurement of the consensus to make the proposed change.

C/ 93 SC 93.8.2.2

C/ 93 SC 93-1 Sela, Oren	P <b>123</b> Mellanox Techi	<i>L</i> nologies	# 188	C/ <b>94</b> Moore, Cha	SC <b>94.2.2.4</b> arles		P147 Avago Techno	L 40 ologies	# 80
Need to add CL72 to table 9 SuggestedRemedy Add to table 93-1: 72 - PMD control required	comment Status <b>D</b> 13-1 due to startup protoc	ol and reference	e to PMD control	receive error in multibit	ation bits compl e real benefit in r a 45 bit block. t errors, which th	eturn. If a ML Such errors ar ne termination I	g and add 2.2 receiver is use e not likely to block is less lik	ed it will allow us be what gets pas kely to correct, w	is not clear that we to correct a single bit st FEC. Most likely /ill be what cause FEC he termination bits.
Proposed Response Re PROPOSED REJECT. The 10GBASE-KR PMD sub Physical Layer. Instead, the function that is functionally e	100GBASE-KR4 PMD s	ublayer incorpor	ates a PMD control	line rate Proposed F	re termination bi e.	Response S		d overhead to str	rengthen FEC or reduce
C/       94       SC       94.2.2       P146       L18       # 48         Anslow, Pete       Ciena       Ciena       Ciena       Ciena         Comment Type       E       Comment Status       D       In Clause 94 there are several arrays of objects denoted by single letters. A useful feature of these arrays is to choose a letter that makes it easy to remember which array is which. In draft D1.0:				present have be rate in termina dabiri_(	tations brown_0 een shown to ou dabiri_01_0911, ation bits is not I	1_0312 and br itweigh the ber parthasarthy_ imited to MLSE termination bits	own_01_0512 nefit of increas 01_0911, and 0 as explained	ing the FEC ster dabiri_01_1111 in brown_01_03	the termination bits ngth or reducing the line . The utility of
<ul> <li>T() for Termination blocks</li> <li>G() for Grey-coded symbols</li> <li>P() for Precoded symbols are all easy to remember.</li> <li>C() for FEC frame bits</li> <li>F() for overhead frame bits</li> <li>Q() for PAM4 symbols are not very memorable - F(bits.</li> <li>For the overhead frame, O v</li> <li>SuggestedRemedy</li> <li>Change the letters to:</li> <li>F() for FEC frame bits</li> <li>V() for oVerhead frame bits</li> <li>V() for OVerhead frame bits</li> <li>V() for PAM4 symbols</li> </ul>	) in particular would mucl			Suggested Write a Proposed F PROPC	<i>Type</i> <b>TR</b> d descriptions o <i>Remedy</i> d de-coding sect	ion to complem <i>Response</i> S IN PRINCIPLE	oding process nent sections S Status W	are required. 94.2.2.1 to 94.2.2	# <u>236</u> 2.8.
о́ ,	esponse Status W								

C/ 94 SC 94.2.4

C/ 94         SC 94.2.5         P150         L 29         # 235           Matthew, Brown         Applied Micro	C/         94         SC         94.3.11.4         P162         L 22         #         108           Moore, Charles         Avago Technologies         Avago Technologies         Avago Technologies         Avago Technologies
Comment Type TR Comment Status D For EEE operation, a signal structure and framing mechanism for allowing the PMA/PMD to	Comment Type TR Comment Status D equation 94-3 is TBD, this is technically incomplete
remain operational during the fast wake. SuggestedRemedy	SuggestedRemedy use equation given in moore_02a_0312.pdf page 20
A proposal will be provided at the July meeting.  Proposed Response Response Status W  PROPOSED ACCEPT IN PRINCIPLE.	Proposed Response Response Status W PROPOSED ACCEPT.
Implement the changes proposed in brown_01_0712.	C/         94         SC         94.3.11.4         P162         L 22         # 57           Mellitz, Richard         Intel Corporation
C/         94         SC         94.2.5         P 150         L 29         # 234           Matthew, Brown         Applied Micro	Comment Type TR Comment Status D Resolve Return loss TBD
For EEE operation, a signal structure and framing mechanism for allowing the receiver to quickly lock to the PMA frame signal. SuggestedRemedy A proposal will be provided at the July meeting. Proposed Response Response Status W PROPOSED ACCEPT IN PRINCIPLE. Implement the changes proposed in brown_01_0712.	SuggestedRemedy         Tie return loss to channel specification proposal presentation by Mellitz, Moore, Dudek, Li, et al supported with a presentation for why the time domain method is better and how it works, by Moore, Ran, Mellitz, et al. At time of this comments file names and requestor have not been finalized.         Proposed Response       Response Status         PROPOSED REJECT.         Comment #108 provides a specific remedy.
C/ 94         SC 94.3.1 Table 94-4         P160         L8         # 107           Moore, Charles         Avago Technologies         107	The suggested remedy does not provide sufficient guidance to implement any changes. A presentation with detailed changes is expected from the commenter.
Comment Type TR Comment Status D Table 94-4 contains many TBDs making it technically incomplete.	C/         94         SC         94.3.12.2         P167         L 52         #         109           Moore, Charles         Avago Technologies         Avago Technologies         #         109
SuggestedRemedy Use values from moore_02a_0312.pdf page 18.	Comment Type <b>TR</b> Comment Status <b>D</b> Equation 94-14 is TBD, that is technically incomplete.
Proposed Response Response Status W PROPOSED ACCEPT.	SuggestedRemedy Use equation from moore_02a_0312.pdf page 20. Page 20 gives it a Tx differential return loss but the same equation can be used for Rx Proposed Response Response Status W
	PROPOSED ACCEPT.

C/ 94 SC 94.3.12.2

C/         94         SC         94.3.12.2         P167         L 52         #         64           Mellitz, Richard         Intel Corporation         Intel Corporati	C/         94         SC         94.3.12.3 table 94-7         P168         L26         #         110           Moore, Charles         Avago Technologies         Avago Technologies         10				
Comment Type TR Comment Status D Resolve Return loss TBD	Comment Type TR Comment Status D Technically incomplete: most values are TBD.				
SuggestedRemedy         Tie return loss to channel specification proposal presentation by Mellitz, Moore, Dudek, Li, et al supported with a presentation for why the time domain method is better and how it works, by Moore, Ran, Mellitz, et al.         At time of this comments file names and requestor have not been finalized.         Proposed Response       Response Status	SuggestedRemedy         use values from moore_02a_0312.pdf page 31, using the values listed under "Test 3" for test 1 and values given for "Test 4" for test 2.         Proposed Response       Response Status         W         PROPOSED ACCEPT.				
PROPOSED REJECT.	C/ 94 SC 94.4 P169 L1 # 105				
Comment #109 provides a specific remedy. The suggested remedy does not provide sufficient guidance to implement any changes.	Moore, Charles       Avago Technologies         Comment Type       T       Comment Status       D         The specifications given are probably insuficient to give high confidence that a cahnnel w be usable.       D				
A presentation with detailed changes is expected from the commenter.         C/ 94       SC 94.3.12.3         P168       L43         Hellitz, Richard       Intel Corporation	SuggestedRemedy use method defined is presentation which will be made at July meeting. Or use method defined in moore_01_0311.pdf and moore_01_0312.pdf				
Comment Type <b>TR</b> Comment Status <b>D</b> Since FEC changes the minimum BER applied broad band noise should be constrained with an appropriate crest factor	Proposed Response Response Status W PROPOSED ACCEPT IN PRINCIPLE.				
SuggestedRemedy Add entry in table after Applied RMS noise for "Applied Crest factor" are the like. Suggested value for is erfcinv(2*minimum BER)*sqrt(2). This could go into Annex 69A.	Several proposals are on the table in addition to those in the commenter's suggested remedy.				
Proposed Response Response Status W PROPOSED REJECT.					
The suggested remedy does not provide sufficient guidance to implement any changes.					
A presentation with detailed changes is expected from the commenter.					

CI 94 SC 94.4

C/ <b>94</b>	SC 94.4.1	P16	69	L <b>8</b>	# 233
Matthew, Brown		Applie	Applied Micro		
Comment	Type <b>TR</b>	Comment Status	D		
•		h is inherited from Claus quired separately for thi			second equation 94-18 to a single equation set.
Suggeste	dRemedy				
	ge the top equa 1*sqrt(f)+a2*f+a	ation in 94-17 to: 13*f^2+a4*f^3			
	ge the bottom e 6*(f-f2);	equation in 94-17 to:			
Delet	e line~17 startii	ng with "Amax".			
Delet	e lines 23 to 32				
a0 = 0 a1 = 7 a2 = 7 a3 = 2 a4 = 7	1.7372e-4 1.1554e-9 2.7795e-19 1.0423e-29 33.467				
'	Response	Response Status PT.	w		

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