

Editor's report

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IEEE P802.3bj Task Force
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Meet the team...

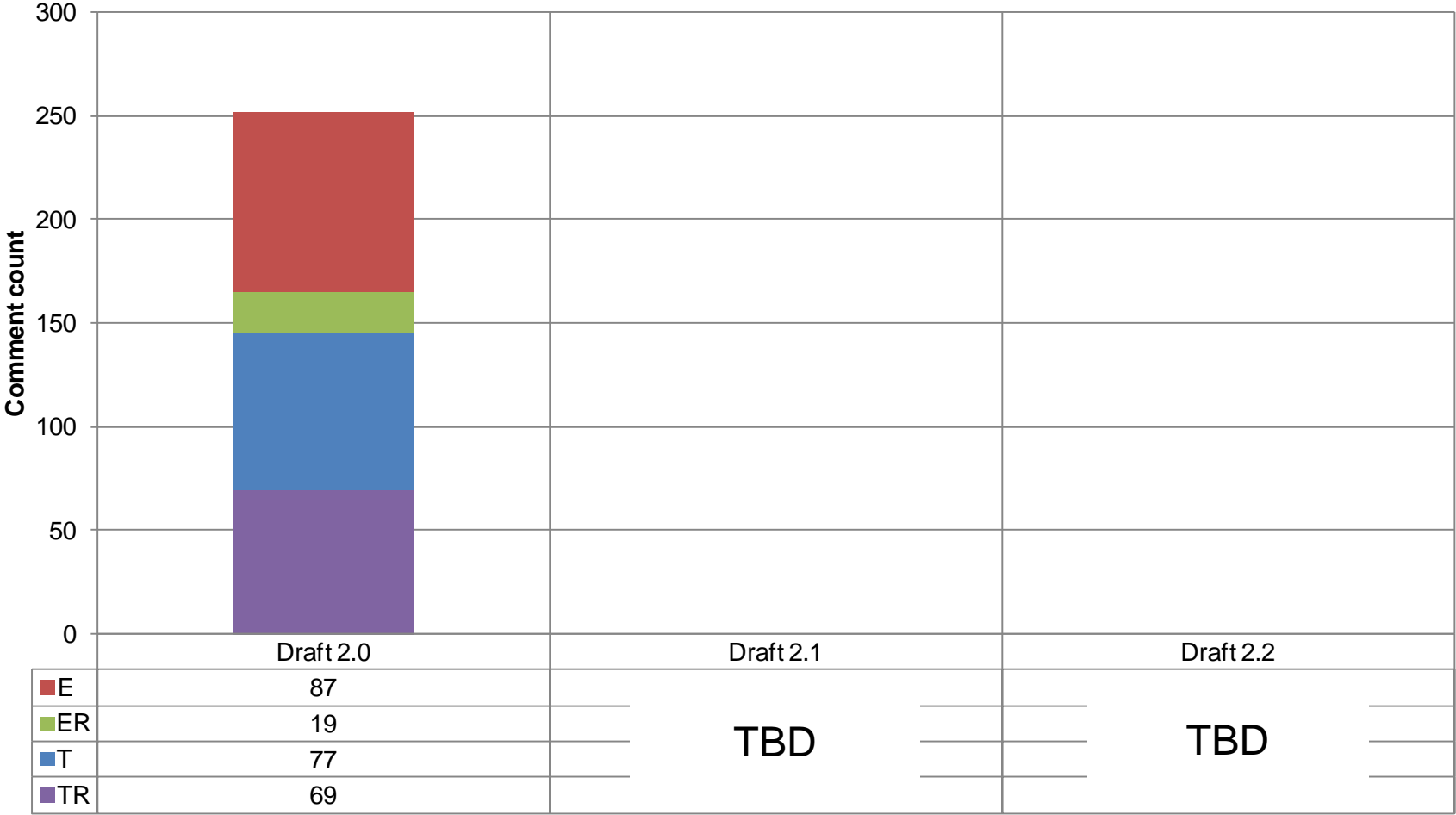
Editor	Edited
Hugh Barrass	Clauses 30, 45, 74, 78–85, Annexes 83A, 83C
Matt Brown	Clauses 1, 69, 73
Chris DiMinico	Clause 92, Annex 92A
Adam Healey	Clauses 91, 93, Annexes 91A, 93A, 93B
Arthur Marris	Clause 94

Activities since March 2013 meeting

- Draft 2.0 ballot opened March 27.
- Draft 2.0 ballot closed on April 30.
- 252 comments received from 26 participants
 - 1 late comment received
- Proposed responses prepared by editors posted May 10.

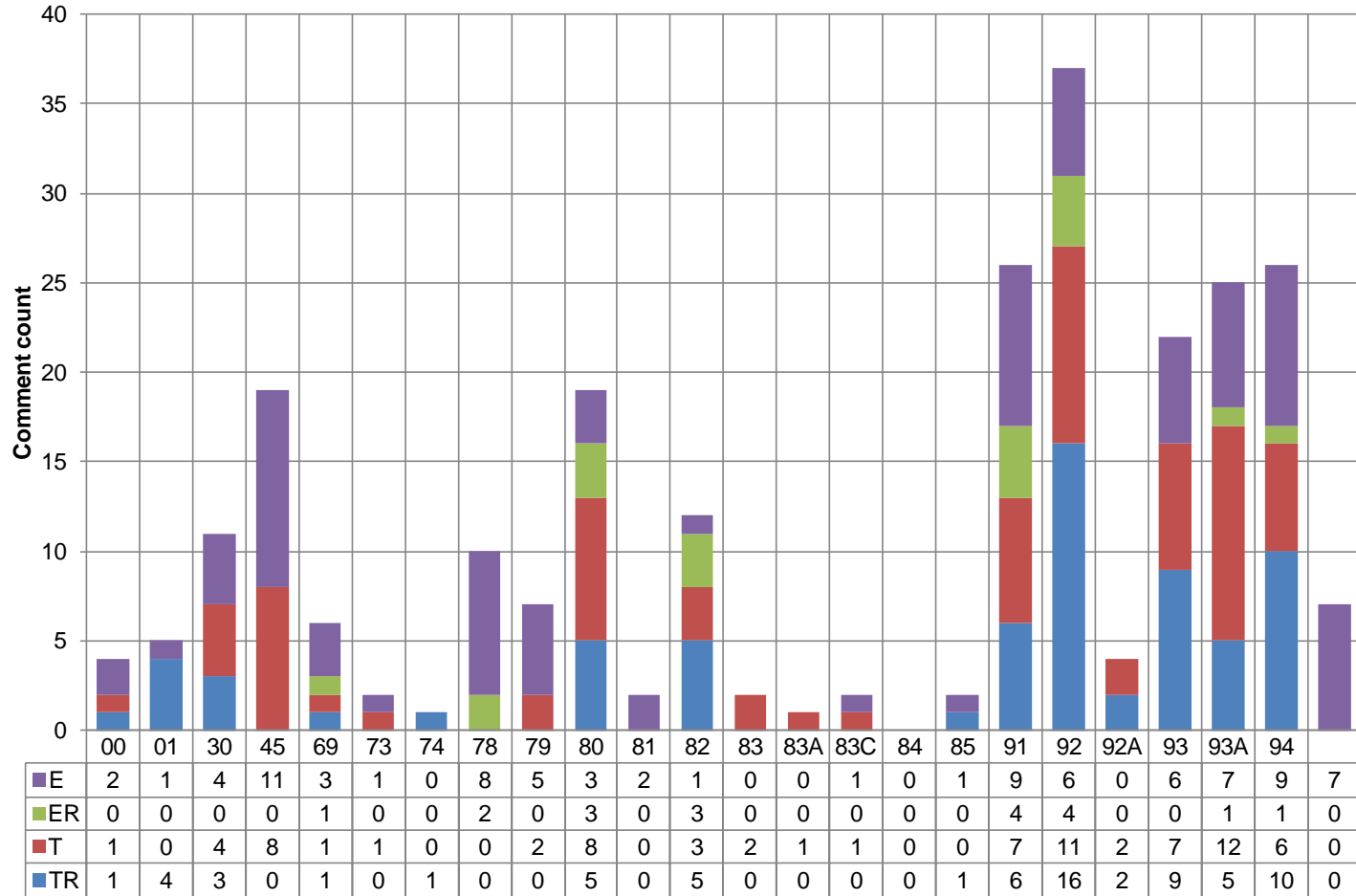
Comment history – Sponsor Ballot

IEEE P802.3bj Task Force review



Comment Summary by Clause

IEEE P802.3bjD2.0 1st Sponsor Ballot Review



ER and TR denote that the commenter indicated the comment must be satisfied in order to change a “do not approve” vote to an “approve” vote.

Objectives for this meeting

- Respond to all comments against Draft 2.0
- Generate Draft 2.1 and initiate 2nd Working Group Ballot.

Comment resolution agenda

Subject to change

Clauses 00, 1, 69, 73, 99

Cl	Topic	Comments (4)
00		No comments
01	PHY definitions	<u>179</u> , <u>180</u> , <u>181</u>
69	Backplane objectives	<u>173</u>
73		No comments
99		No comments
	Total	4

“**” indicates lead comment (resolved first). Underline indicates ER/TR. Red indicates big ticket item

BTI – PHY types^a

Leading comment	Following comments
<u>209</u>	<u>196</u> , <u>197</u> , 194, <u>195</u> , <u>178</u>
<u>215</u> , 33, 152, <u>15</u> , 249, 120	
Total	12

BTI – Objectives^a

Clause	Comment #
80	<u>174</u>
Total	1

BTI – Warning^a

Clause	Comment #
78	<u>10</u>
Total	1

(a) – multi-clause topic

“TR & ER” comments underlined

BTI – Nomenclature (multi-clause)

Clause	Comment #
78	<u>192</u>
Total	1

BTI – 40GBASE-CR4, 100GBASE-CR10 change

Clause	Comment #
85	<u>93</u>
Total	1

BTI – LPI state

Clause	Comment #
82	<u>70</u> , <u>96</u> (<u>151</u> , 95, <u>127</u>) [barrass_3bj_xx_0513]
Total	5

“TR & ER” comments underlined

Clause 30

Topic	Comment #	Preso?
format	7, 8	
FEC	<u>137</u> , 122, 123	
MMD	<u>216</u>	
PHY order	16	
Total	7	

Clause 45

Topic	Comment #	Preso?
802.3bk	29	
Align_status	1, 2	
Registers	110, 32	
Total	5	

“TR & ER” comments underlined

Clause 74

Topic	Comment #	Preso?
FEC	<u>107</u>	
Total	<u>1</u>	

Clause 79

Topic	Comment #	Preso?
LLDP	111, 112	
Total	2	

Clause 80

Topic	Comment #	Preso?
CAUI	38, 198, 199, <u>201</u>	
FEC	149	
Primitives	<u>200</u>	
Skew	<u>136</u>	
Total	7	

“TR & ER” comments underlined

Clause 81 - None

Clause 82

Topic	Comment #	Preso?
PCS scope	114	
802.3ba	<u>202</u>	
Skew	115	
BIP	<u>135</u>	
Editorial	<u>190, 191</u>	
Total	6	

Clause 83 (83A)

Topic	Comment #	Preso?
Skew	116	
EEE/CAUI (83A)	160 [healey_3bj_xx_0513]	Y
Total	2	

“TR & ER” comments underlined

Clause 84 - None

Clause 85 - None

Guided discussion on PHY types – part 1

- Comment #209
- Definitions for 100GBASE-R & 100GBASE-P in Clause 1, Clause 30, Clause 80.
- Discussion: separate concept of “100GBASE-R encoding” from the definition of 100GBASE-R
 - Comment #209 (items 1-5) make the change in Clause 1
 - Comment #179 – also deletes 2-level
 - Comment #209 (item 8) makes the change in Clause 80 – also comment #195, 196 & 197
- Discussion: 100GBASE-P is >2 level (not 4-level)
 - Comment #209 (items 5, 6 & 7)
 - Comment #194
 - (Comment #178 – deletes the definition for 100GBASE-P)

Guided discussion on PHY types – part 2

- Comment #180, #181
- Definitions for 100GBASE-R & 100GBASE-P in Clause 1, Clause 30, Clause 80.

- Comment #215
- Discussion: Clause 30 MAU type identification for unpopulated module

- Comment #15
- Discussion: Definition uses “40Gb/s MAC” – technically incorrect

- Comment #120
- Discussion: Clause 30 definitions no longer have 64B/66B explicitly in the definitions

Clause 91

#	Topic	Comments
1	PMA compatibility	<u>203</u>
1	Notes on “EEE support”	147
1	Alignment efficiency	153 [gustlin_3bj_01_0513]
5	RS symbol error monitor	<u>106</u> , 119, <u>124</u> , <u>125</u> , <u>126</u>
1	Alignment marker insertion points	<u>88</u>
3	Lane mapping	118, 117, 130
1	Reset definition ***	<u>84</u>
1	Management of co-located PCS, RS-FEC	<u>87</u>
3	FEC/PCS management variables	3, 4, <u>85</u>
1	Warn user about FEC bypass	<u>204</u>
	Total	18

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Cross-Clause for Clauses 92, 93, 94

Cls	Topic	Comments
92, 93, 94	Frame error ratio	42 (CL00) [brown_3bj_02_0513]
92, 93, 94	PHY/Channel Validation	<u>239</u> (CL00)
92, 93, 94	COM: Make "DER0" a function of the DFE profile	<u>58</u> (CL93), <u>59</u> (CL94)
	Total	4

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Clause 92/92A

Cl's	Topic	Comments
92	Training: Seed labels	<u>162</u> , <u>75</u>
92	Training: coefficient update request time	102 [EC]
92	Training: PRBS sequence text	<u>161</u> [EC] [slavick_3bj_01_0513]
92	Training: training pattern within the PMD training frame	<u>163</u> [EC]
92	Tx: PMD_signal_detect_i set to zero	183 [EC]
92	Tx: Transition time	226 [EC], 220
92	Tx Jitter: definitions	<u>165</u>[EC] [moore_3bj_01_0513]
92	Tx Jitter: tx equalization setting	227[EC]
92	Tx Jitter: DCD constraint	228[EC]
92	Tx Jitter: proper nouns	<u>231</u> [EC]
92	Tx Jitter: TJ-DDJ spec	223[EC]
92	Transmitter test points	164
	Total	14

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Clause 92/92A continued

Cls	Topic	Comments
92	Rx: amplitude tolerance	<u>232</u> [EC]
92	Rx: Interference tolerance SJ frequency	<u>83</u> [EC]
92	Rx/Cable: Frequency range (explicit use of fmax)	229[EC]
92	New 100GBASE-CR4 electrical specifications Host and Cable assembly e.g., common mode	<u>230</u> , <u>186</u> [dudek_3bj_01_0511]
92	Rx CM return loss	<u>238</u> , 184*
92	COM cable assembly	<u>188</u> [dudek_3bj_02_0513]
92A	COM target	<u>100</u>
92	Cable assembly Max and Min IL graph	219
92	Cable assembly ILD limit	<u>233</u>
92	Cable assembly RL	224
92A	Cable Maximum IL	185
92A	MHz to GHz	<u>77</u> *, 41
	Total	15

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Clause 92/92A continued

Cls	Topic	Comments
92	Test fixture IL specifications	<u>237</u>
92	Test fixture naming	<u>222</u>
92	Test fixture CM specifications	<u>236</u> , <u>187</u> *
92	PICS CA6, CA7, C14, C16	<u>170</u> , <u>171</u> , <u>69</u>
	Total	7

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Clause 93

#	Topic	Comments
2	Bit error ratio targets	212, 159
3	COM target	<u>99</u> , <u>103*</u> , <u>98</u>
1	Management variables	214
1	Combine test fixtures	<u>235</u>
1	Measurement frequency range and bandwidth	<u>240</u>
2	Calibration of interference	90 [moore_3bj_02_0513], <u>234</u>
1	Interference tolerance test channel units	<u>94</u>
1	ADD value	<u>81</u>
	Total	12

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Annex 93A

#	Topic	Comments
1	Annex title	217
1	Definition of COM	57
1	LATE – Add transmitter SNDR	252
3	Package transmission line model	172, 101*, 92 [benartsi_3bj_01_0513.pdf]
1	Reflection coefficient equation	155
1	Double-counting level-separation loss	156
1	Definition of sampling time	158 [healey_3bj_01_0513.pdf]
1	bmax for 100GBASE-KP4	80 [ran_3bj_01_0513.pdf]
1	Add all noise sources to FOM	74 [ran_3bj_01_0513.pdf]
1	Convert sigma_r to input referred noise PSD	73 [ran_3bj_01_0513.pdf]
1	Jitter model	157 [healey_3bj_01_0513.pdf]]
1	Different package model for NEXT aggressors	91
1	COM system diagram	143
	Total	15

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Clause 94

Cl's	Topic	Comments
94	Broad market potential	<u>225</u>
94	Overview wording and related PICS	211, 150, 210
94	PRBS13 seed bit ordering in Table 94-11	<u>76</u>
94	Training status field	72 [marris_3bj_01_0513]
94	Example transition from training to data	64, <u>65</u> , <u>66</u> [lusted_3bj_01_0513]
94	Insertion loss min and max values identical	<u>79</u>
94	Test points for measuring insertion loss	<u>68</u>
94	Additional receiver interference tolerance test parameter	<u>166</u>
94	Change target COM	<u>78</u>
94	Frequency variation in NSD used in ITOL test	<u>167</u>
94	Increase the minimum channel COM required	<u>104</u>
94	Correct Dual-Dirac jitter peak value	<u>82</u>
	Total	16

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Bucket Comments

Clauses 00, 1, 69, 73, 99

Non-Controversial (Bucket)

Cls	Topic	Comments (4)
00	Abbreviations	12
00	Editing instructions	34
01	802.3bk	36
69	References	48
69	Editing instructions	24
69	CAUI lanes and editing instructions	43, 23
73	AN summary	251
73	References	248
99	Front matter	5, 6, 193, 176, 175, 177, 207
	Total	16

Clauses 30, 45, 78, 79, 80, 81, 82, 85, 83C

Non-controversial (Bucket)

Clause	Comment #
30, 45	<i>121, 132, 17, 49, 18, 30, 54, 31, 19, 20, 208, 40, 46, 9, 21, 22</i>
78, 79	<i>50, 189, 51, 11, 55, 25, 37, 133, 56, 26, 27, 13, 14</i>
80, 81	<i>108, 113, 134, 109, 28, 67</i>
82, 85, 83C	<i>103, 35, 47, 39</i>
Total	39

“T” comments in italics

Clause 92, Annex 92A

Non-Controversial (Bucket)

Cls	Comments
92/92A	53, 221, 182, 168, 44
92A	No bucket comments.
Total	5

Clauses 91, 93

Non-Controversial (Bucket)

Cls	Topic	Comments
91	Minor editorial, cross-references	128, 45, 129, 241, 242
91	Location of fec_lpi_fw definition	86
91	Comparing Booleans to “true”	131
91	Clarifying text	52
91	Subtotal	8
93	Blatant PICS problems	213, 245, 246, 247, 148
93	Wordsmithing, cross-references	243, 250, 244, 218
93	Subtotal	9
	Total	17

Annex 93A

Non-Controversial (Bucket)

ClS	Topic	Comments
93A	Cross-reference from Table 93A-1	140
93A	System diagram for COM	138
93A	Blatant errors	144, 143
93A	Parameter definitions	97
93A	Transmission line length units	145
93A	Device or die?	146
93A	“Single-bit” response	139
93A	Numbered or lettered lists?	141
93A	“Package-side” to “Board-side” capacitor	89
	Total	10

Clause 94

Non-controversial (Bucket)

Cls	Topic	Comments
94	Typo	206 ,169
94	Editorial clarification (add “without inversion”)	60
94	Cosmetic change to a diagram	62, 63
94	Move location of figure	61
94	Incomplete implementation of prior comment	154
94	PICS clean up	205
94	Frequency for max. channel loss	71
	Total	8