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Making Next-Generation Networks a Reality.

Output Voltage Proposal for PAM-4 Transmitter

*IEEE 802.3bj Task Force
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Introduction

- ▶ PAM-4 PHY is expected to equalize channel insertion losses of around (-50)db at 12.5 GHz.
- ▶ At 1v peak-to-peak differential output voltage (V_{pp}), FEC with at least 7dB of coding gain may be required for 10^{-12} BER¹.
- ▶ Part of this FEC coding gain (1dB – 2dB) can be distributed to:
 - ▶ Transmitter only (Higher V_{pp})
 - Increased power, noise, and jitter
 - Process limitations
 - ▶ Transmitter and receiver (Trellis/MLSD²)
 - Increased latency and complexity
- ▶ This presentation will investigate the gain obtained from higher Tx V_{pp} in the presence of higher receiver noise and transmit jitter.

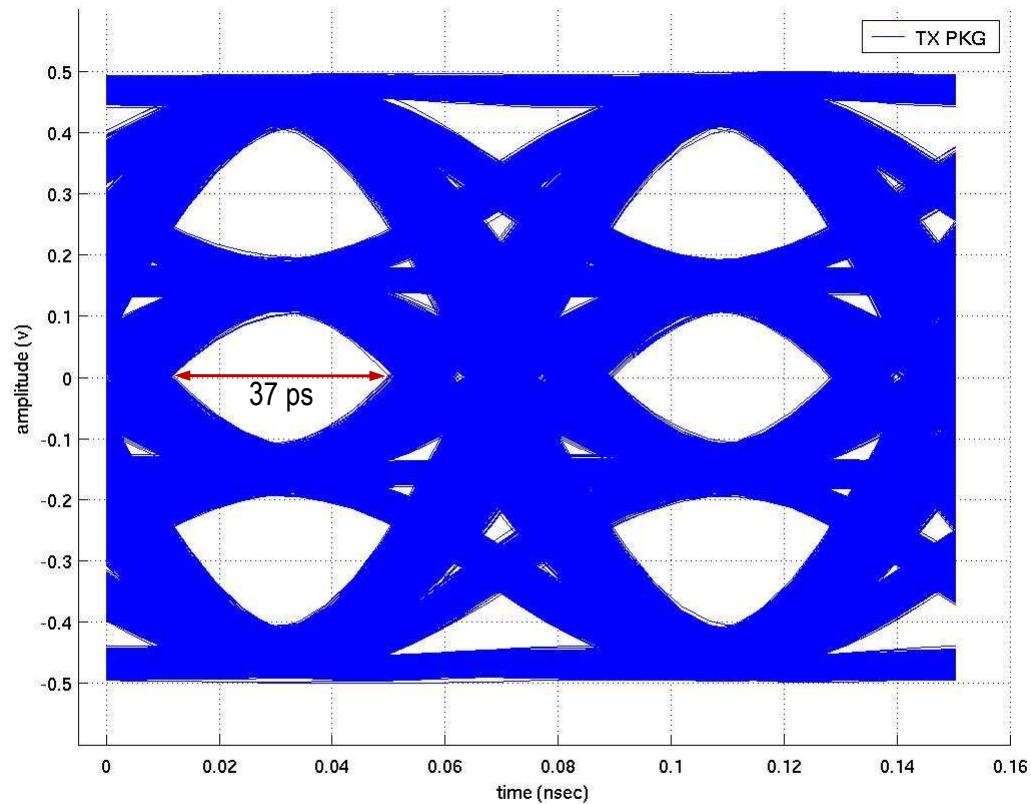
¹: http://www.ieee802.org/3/bj/public/sep11/hatab_01_0911.pdf

²: http://www.ieee802.org/3/bj/public/sep11/dabiri_01_0911.pdf

Simulations Parameters

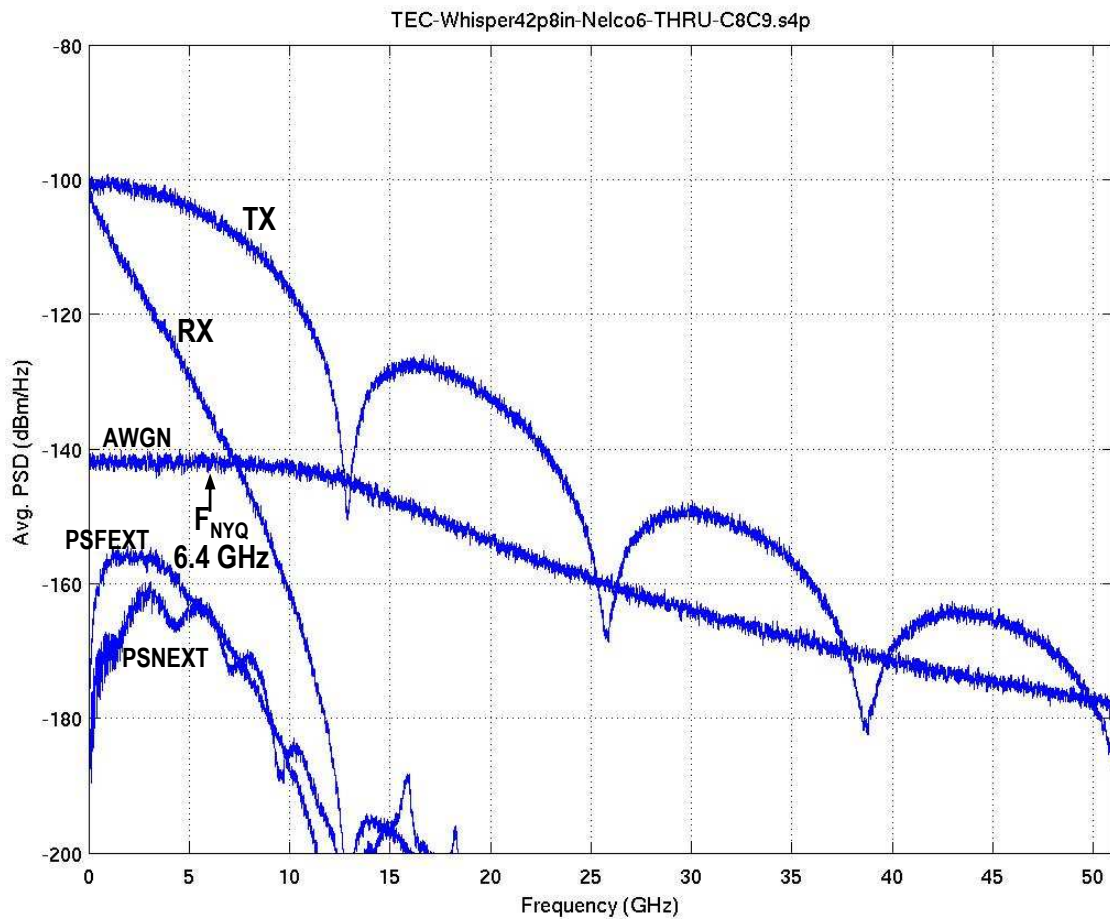
Channel	Channel	Link	Transmitter	Receiver
Transmitter	Receiver	Bit Rate: 25 Gb/s	Test Pattern: PRBS31	Random Noise: -144 dBm/Hz → -142 dBm/Hz (4.5 mv → 5.5 mv RMS)
Device Package: Pkg35mm_T21mm115ohm LoXtalk_BGALoXtalk.s8p	Device Package: Pkg35mm_T21mm115ohm LoXtalk_BGALoXtalk.s8p	Modulation: PAM-4	Bit Encoding: 64B/66B	Deterministic Jitter: NA
Device Package Crosstalk: NA	Device Package Crosstalk: NA	Signaling Rate: 12.890625 GHz	Vpp: 0.8 v → 1.2 v	Random Jitter: NA
R₁: 50 ohm	R₂: 50 ohm	Number of symbols simulated: 50000	Equalizer: NA	CT Filter: NA
C₁: 0 F	C₂: 0 F	Target symbol error ratio: 10 ⁻¹²	Deterministic Jitter: 0.05 UI (peak-to-peak) Sinusoidal at 5 MHz	Equalizer Structure: DFE: 12 FFE: 8 (T-Spaced) LMS
Channels: TEC_Whisper42p8in_Nelco6_THRU_C8C9.s4p			DCD: 0.02 UI (peak-to-peak)	CDR: Ideal
Near-End Crosstalk: 6 aggressors Asynchronous Victim Tx replicated			Random Jitter: 0.6 ps (RMS)	Performance Metrics: SNR (dB)
Far-End Crosstalk: 2 aggressors Asynchronous Victim Tx replicated			Total Jitter: 0.21 UI (peak-to-peak)	

PAM-4 Eye Opening at Transmitter with Package

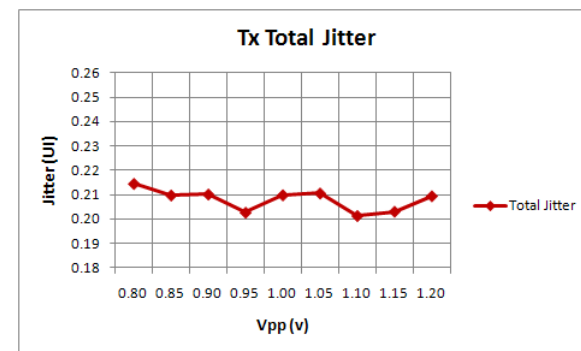
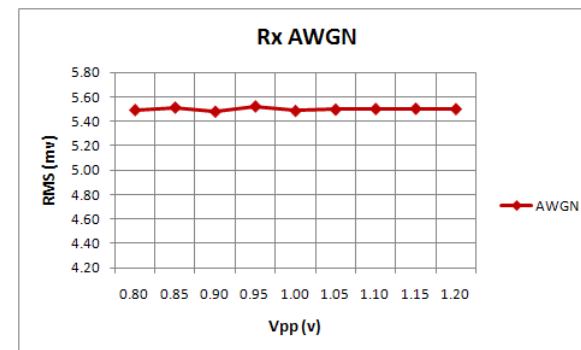
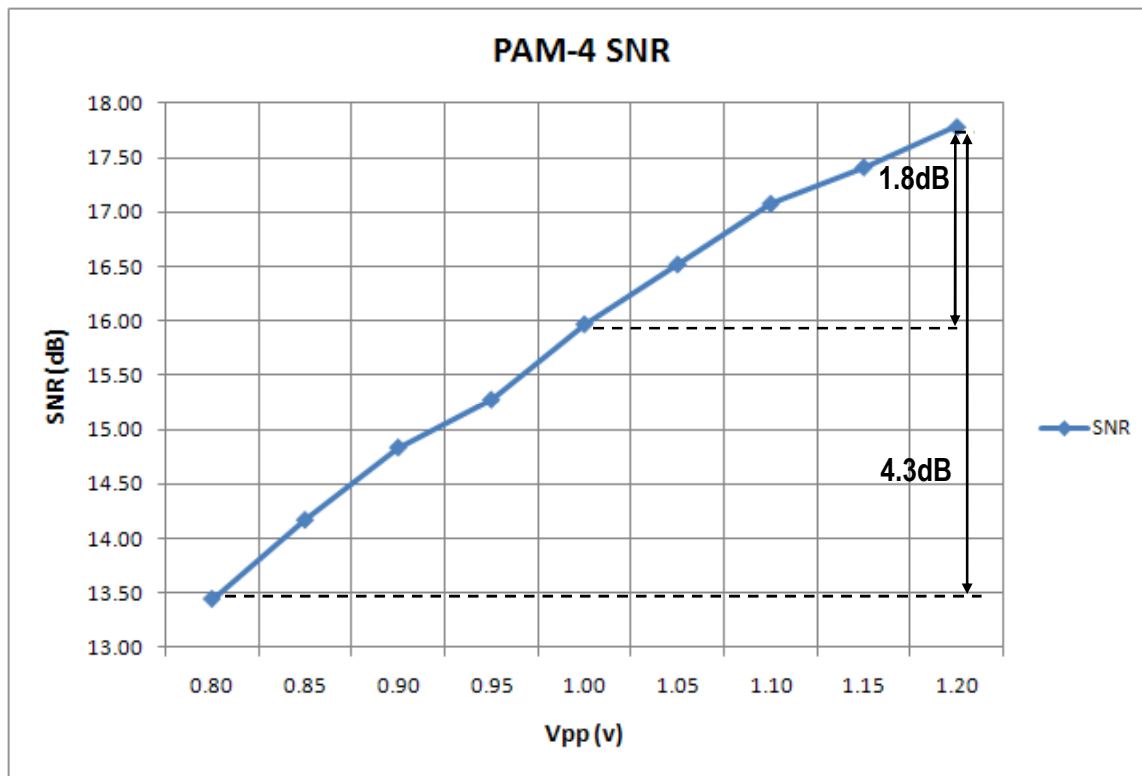


- ▶ Total Tx Jitter $\approx 1UI - 0.47UI - 0.33UI = 0.2UI$

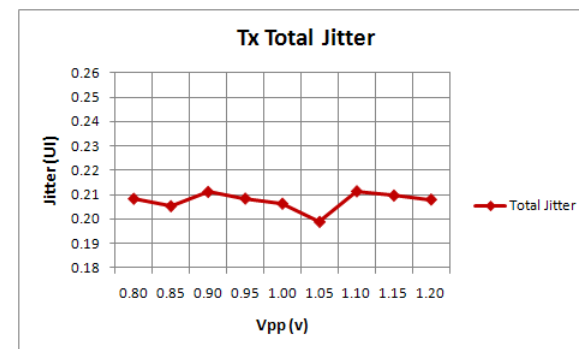
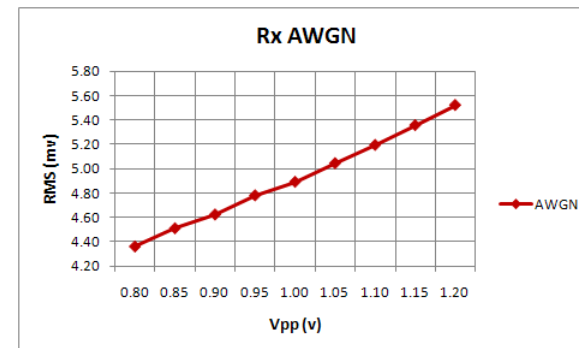
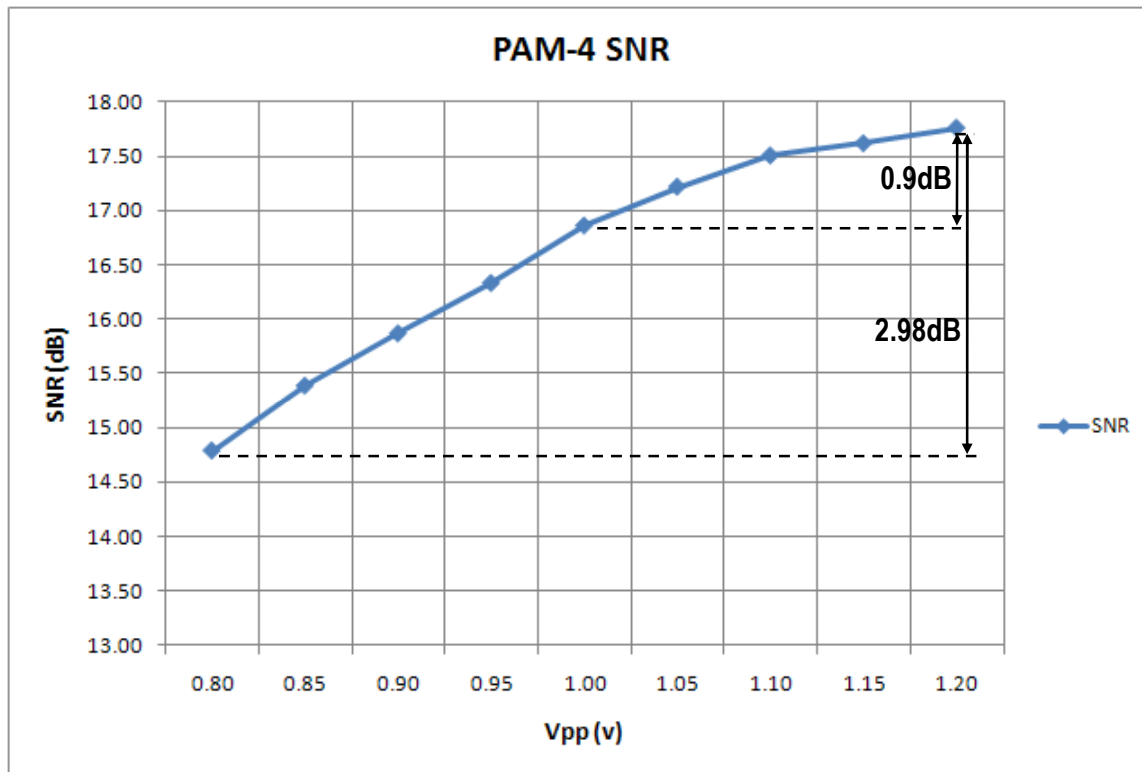
Power Spectral Densities



System SNR vs. Vpp with Constant AWGN



System SNR vs. Vpp with Higher AWGN



Conclusion

- ▶ Investigated PAM-4 output peak-to-peak differential voltage values:
 - ▶ Vpp was varied from 0.8v to 1.2v
 - With no additional Rx noise and Tx jitter
 - With additional Rx noise (AWGN up to -142 dBm/Hz):
 - AWGN is dominant noise source. Other sources can be considered:
 - Additional Tx jitter
 - Non-linear distortions
- ▶ Increasing Vpp from 1.0v to 1.2v :
 - ▶ With constant AWGN levels (5.5 mv RMS) there is about 2dB improvement in SNR margin.
 - ▶ With increasing AWGN levels (4.5mv to 5.5mv RMS) there is about 1dB improvement in SNR margin.
- ▶ Propose to adopt 1.2v output peak-to-peak differential voltage for PAM-4 PHY.