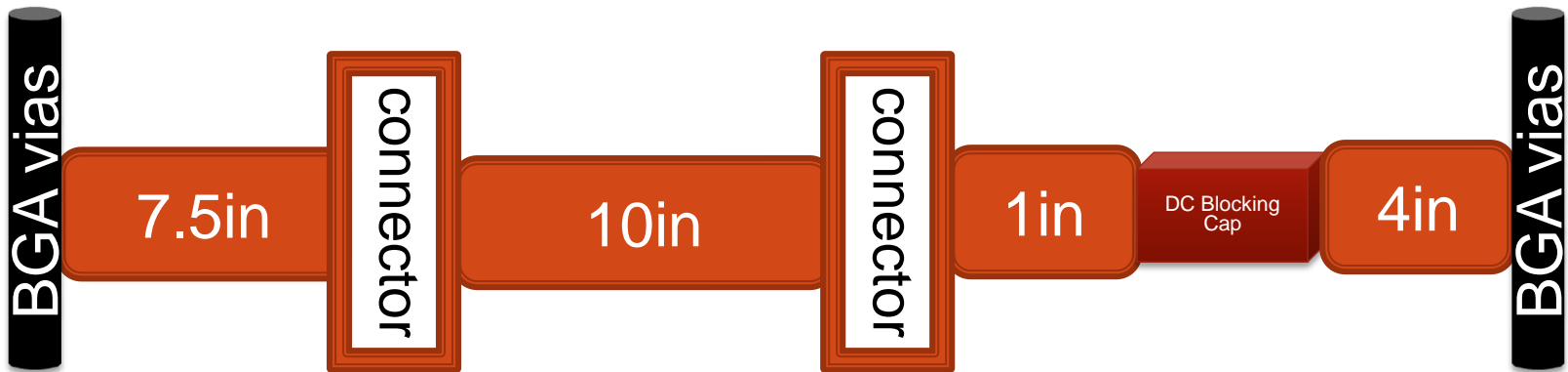


IEEE802.3bj Channel Example for Volume Microservers

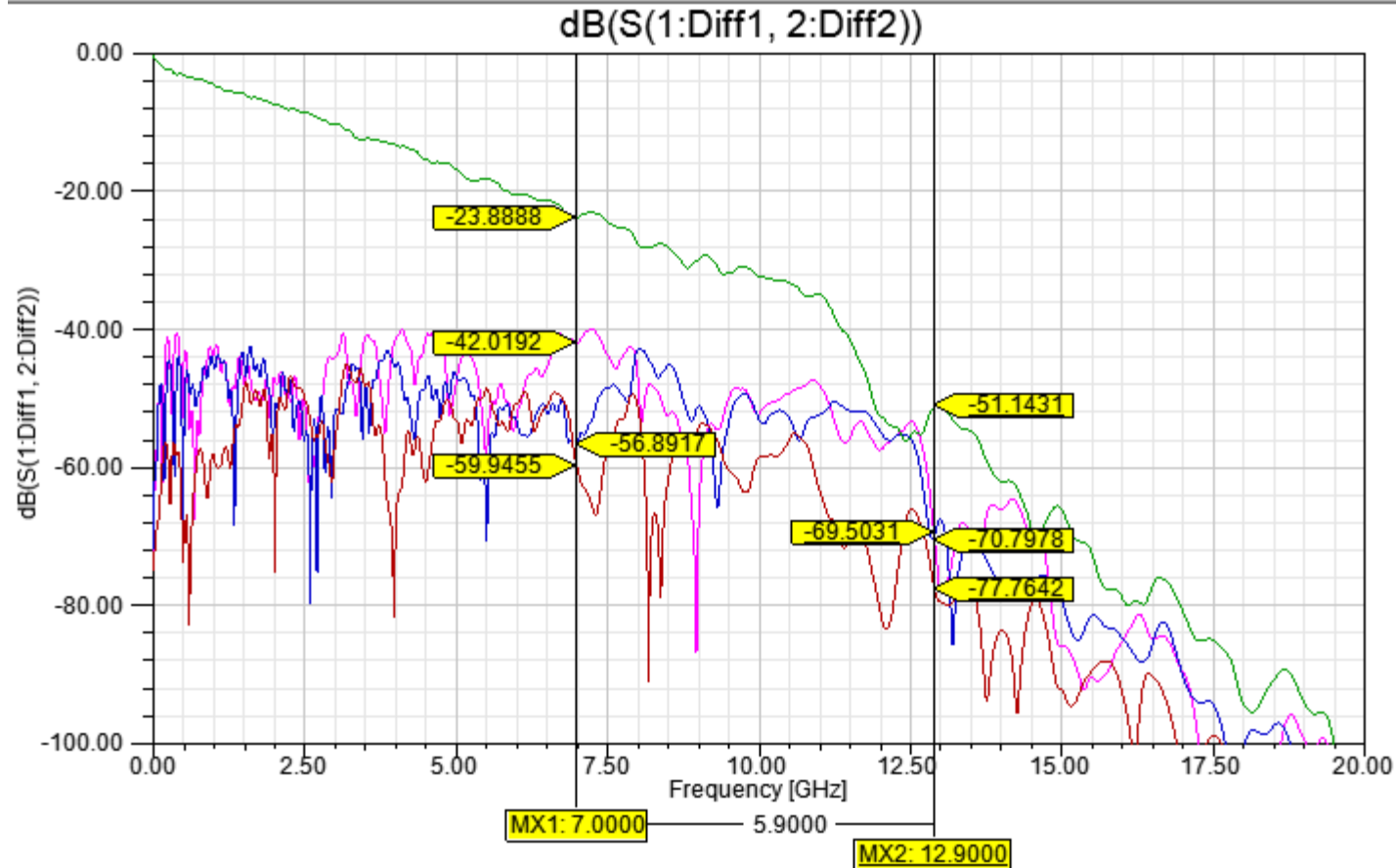
Richard Mellitz
Intel Corporation
November, 2012

Synthesized Microserver Channel

- 8 via most have less than 50 mils stubs
- 10-14 layers, Nelco 13 Si and some FR408
- DC blocking Cap models as ESR and ESL
- Full wave 3D modeling for DC blocking cap mounting and all vias
- Connector measured or model with full wave 3D tools
- NEXT eliminated due to careful pin assignment.
- Channel are 10G Base KR ready



Responses: IL and 3 FEXT



- Channel demonstrates non-log linear loss after 10GHz