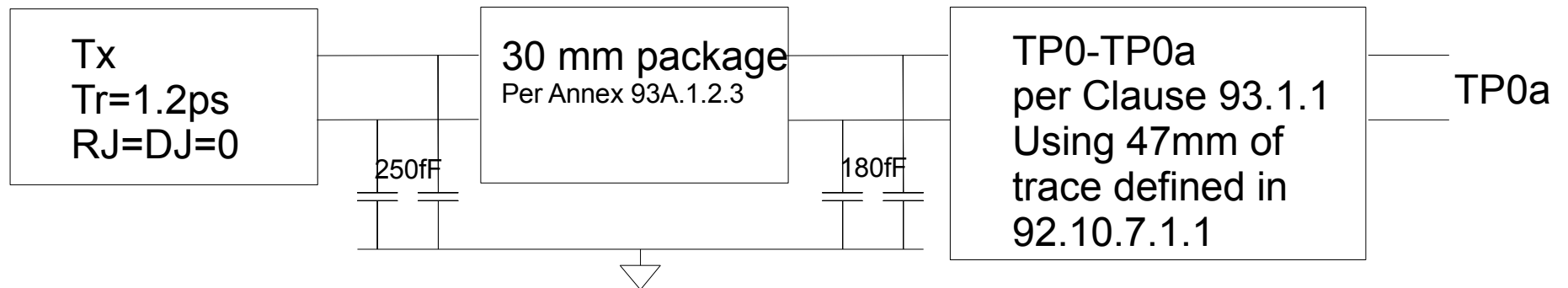


Jitter at TP0a

Charles Moore Avago Technologies
2013 August 13

Tx model to TP0a

Per Annex 93A



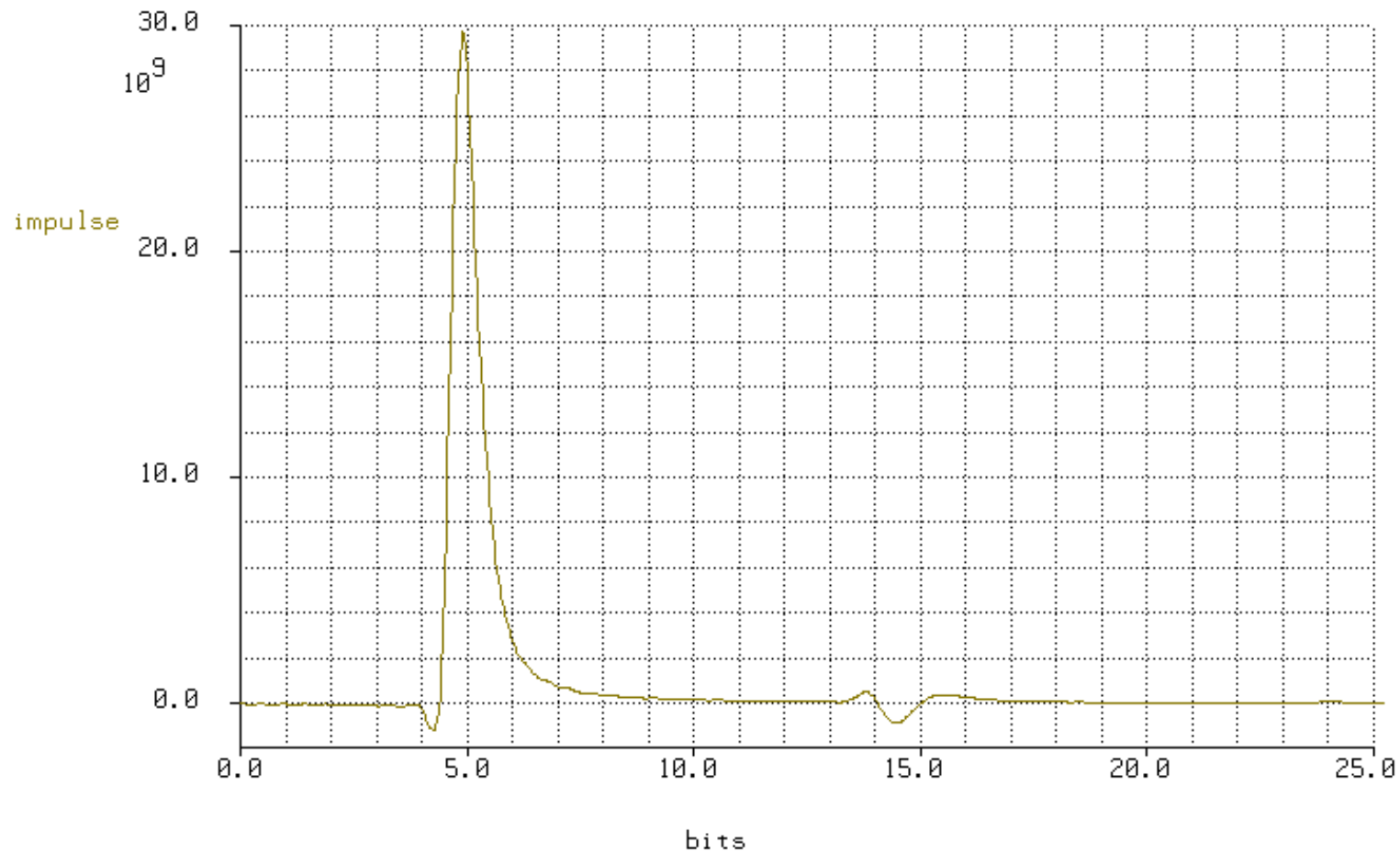
I used equalizer steps of 0.02 relative approximately 0.35 dB

Channel response

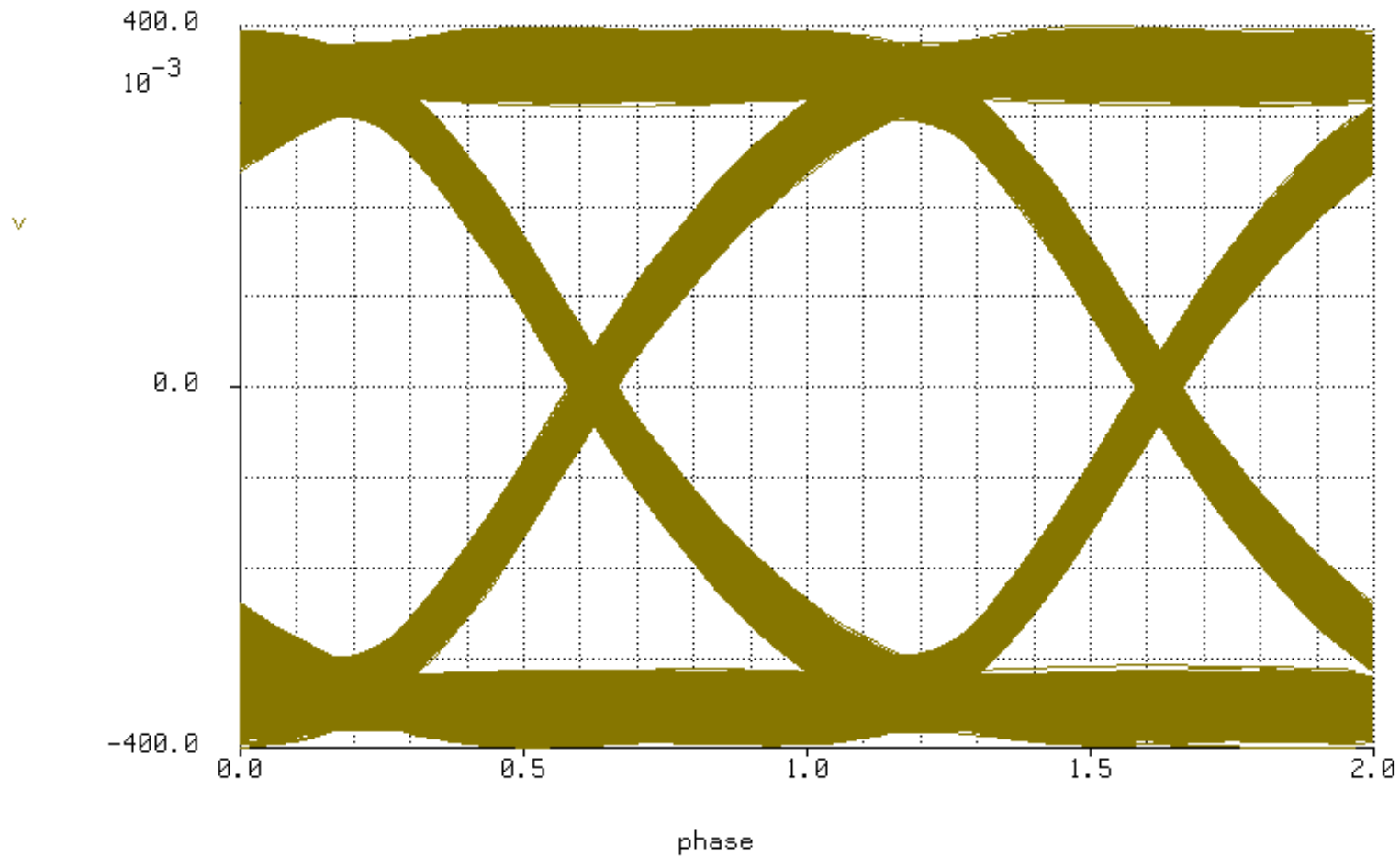


Ripple looks large because loss is so small

Channel Impulse response

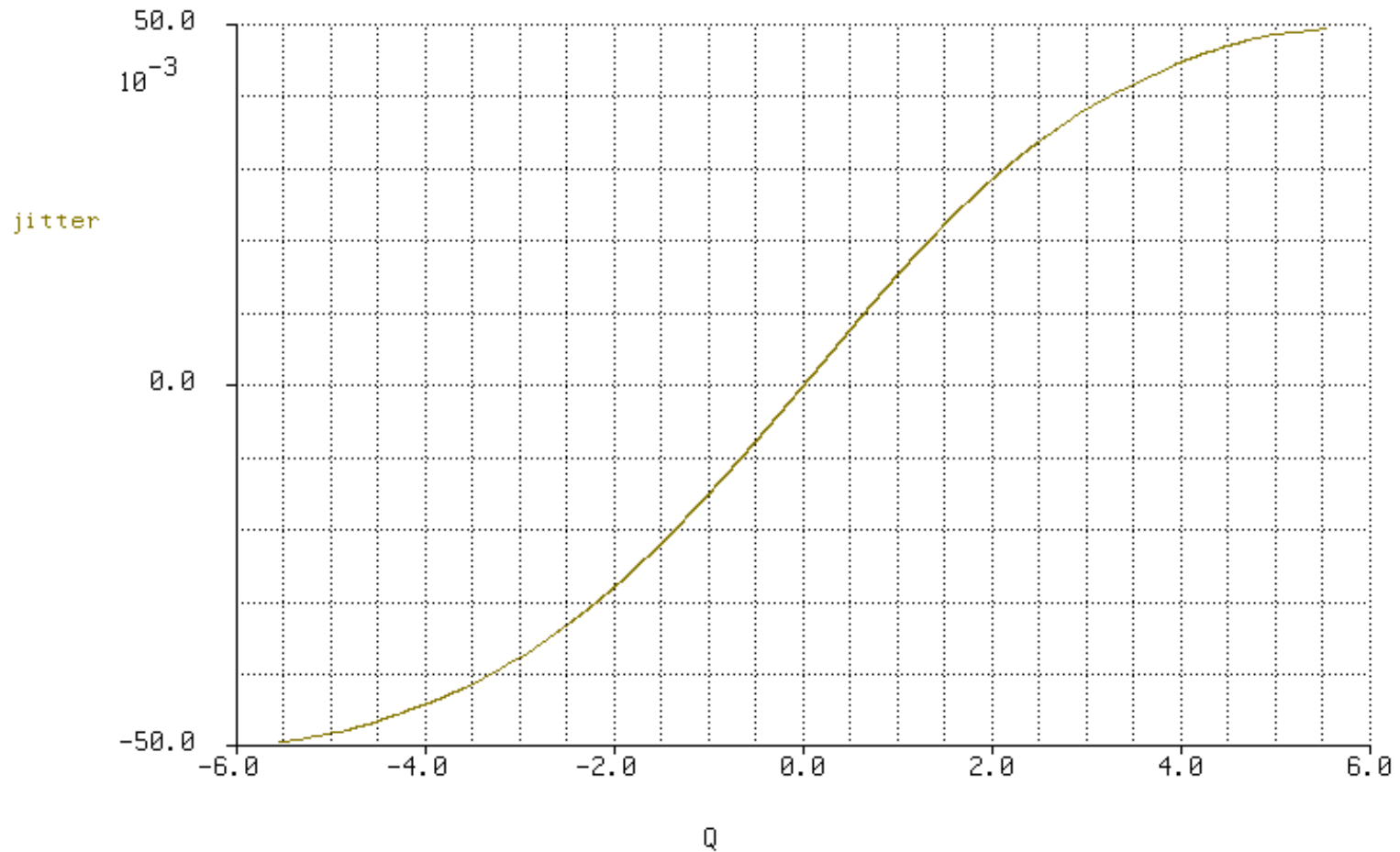


EYE diagram for PRBS15 pattern



Optimum Equalization : 0.00, 0.88, -0.12
P-p jitter for PRBS15: 87mUI

Jitter vs Q for a PRBS 26 pattern (Q=5.5)
Getting close to PRBS31 (Q=6.1)



TJ@PRBS26 = 99mUI

Conclusion:

If CAUI4 measures jitter using the method described in D1.1, more than one third of the spec will be eaten up by ISI jitter in the measurement path. It is unlikely that many transmitters will pass.