# CAUI-4 Ad hoc

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# Agenda

- Patent Policy: This meeting is an official IEEE ad hoc. Please review the patent policy at the following site prior to the meeting. http://www.ieee802.org/3/patent.html
- Presentations:
  - ran_01_060614_CAUI4 - Closed-loop TX equalization tuning
  - Comments from D3.0
- Next meeting:  TBD

# Comment 20196 on D2.2

**83E.3.4.1 Input bit error ratio**

The CAUI-4 module input is defined to operate at a bit error ratio (BER) better than $10^{-15}$ for an input signal defined by 83E.3.4.2.

- Discuss potential wording proposals:
  - "The CAUI-4 module input is defined to operate at a bit error ratio (BER) better than $10^{-15}$ for an input signal defined by 83E.3.4.2."
  - To
  - "The module CAUI-4 receiver is defined to operate at a bit error ratio (BER) better than $10^{-15}$ for an input signal defined by 83E.3.4.2."
  - Or
  - "The CAUI-4 module input characteristics and stressed electrical input signal are defined by 83E.3.4.2 at a bit error ratio (BER) of 10-15."
  - Or:
  - Create new subclause 83E.1.1 with the following text: "The bit error ratio (BER) shall be less than $1x10^{-15}$"
  - Delete 83E.3.3.1 and 83E.3.4.1, delete first row of table 83E-4 and 83E-7
  - Add footnote to the "Host stressed input test" parameter in Table 83E-4 and the "Module stressed input test" parameter in Table 83E-7: "Meets BER specified in 83E.1.1"
  - Similar to CL95

# Minutes

- Reviewed Patent Policy
- Presentations:
  - ran_01_060614_CAUI4 - Closed-loop TX equalization tuning
    - Sufficient interest in standardizing a method for closed-loop Tx equalization tuning
    - Adee to work with editorial team and interested parties on potential implementation
  - Comments from D3.0
    - Form sub-team to build consensus on if a change is required, and what that change should be
- Next meeting: TBD

Attendees:

Mike Dudek, Qlogic
Ali Ghiasi, Ghiasi Quantum
Adam Healey, Avago
Peter Anslow, Ciena
Ryan Latchman, MACOM
Adee Ran, Intel
Jonathan King, Finisar
Adee Ran, Intel
Piers Dawe, Mellanox
Rick Rabinovich, Alcatel Lucent
David Brown, Semtech
Richard Mellitz, Intel
Randy Rannow, APIC
Nathan Tracy, TE
Robert Wang, Intel