# CAUI-4 Ad hoc

Ryan Latchman, Mindspeed

# Agenda

- Patent Policy: The meeting is an official IEEE ad hoc. Please review the patent policy at the following site prior to the meeting. http://www.ieee802.org/3/patent.html
- Approval of Sept 30th 2013 CAUI-4 ad hoc minutes
- CAUI-4 chip-to-chip
  - Presentations:
    - mellitz_01_101413_CAUI
    - ran_01_101413_CAUI
    - COM update: < http://www.ieee802.org/3/bj/public/tools/ran_3bj_com_d2p3_01_1013.zip >.
- Chip-4 chip-to-module discussion

# Chip to module

- Low loss test: remove frequency dependent attenuator
  - Total loss between limiter and TP1a is the mated MCB / HCB loss
    - 2.86 to 4.6dB loss at 12.89GHz
    - Keep same EH/EW targets
- Host output compliance test:
  - 2) Apply respective reference receiver CTLE to captured signal. Any single CTLE setting which meets both eye width and eye height requirements is acceptable for the module compliance . For host compliance, the CTLE peaking in the reference receiver shall be set at one of three values. These are: a) the recommended CTLE peaking value provided by the host, b) the value 1 dB higher if present in Table 83E-2 and the value 1 dB lower if present in Table 83E-2. Any of the three CTLE settings that meets both the eye width and eye height defined in Table 83E-1 is acceptable.
- Next: mechanism for host to provide value

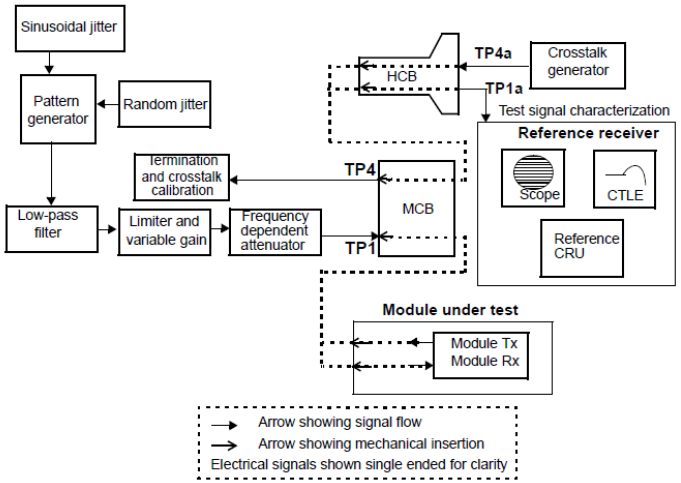


Figure 83E–15—Example module stressed input test

# Minutes

- Reviewed Patent policy
- mellitz_01_101413_CAUI
  - Limiting of tap weights in COM shown to avert error propagation
- ran_01_101413_CAUI
  - PCS update to detect problematic links in field
  - Also discussed specifying limits with respect to actual DFE values (before equipment is shipped)
- Next meeting: Nov 4th at 9am
  - Topics include:
    - New COM (code available – see link on slide 2)
    - Chip-chip MTTFPA path forward
    - Chip-module CTLE communication