

CAUI-4 Ad hoc

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Agenda

- Patent Policy: The meeting is an official IEEE ad hoc. Please review the patent policy at the following site prior to the meeting.
<http://www.ieee802.org/3/patent.html>
- TBD for CAUI-4 chip-to-chip
 - Presentations:
 - Sli_01_091613_caui
 - anslow_03_0913_optx
 - Current chip-chip summary
 - Transmitter with eye mask and programmable Tx Equalization (evaluated leveraging CL72)
 - 15dB channel evaluated using COM with ideal Rx package, reference EQ
 - Interference tolerance using reference receiver eye opening
 - Discussions from York
 - 15dB and/or 20dB channel
 - DFE & MTTFPA
 - COM results with package
 - Corresponding changes to interference tolerance

Minutes

Attendees:

Shaohua Li – Brocade

Jeff Slavick – Avago

Adee Ran – Intel

Beth Kochuparambil, Cisco Systems

Ali Ghiasi, Broadcom

Pete Anslow, Ciena

Mike Dudek QLogic Corporation

Rick Rabinovich Alcatel Lucent

Richard Mellitz Intel

Matt Brown – Applied Micro

Charles Moore Avago Technologies

Megha Shanbhag TE Connectivity, Ltd.

Vinu Arumugham Cisco

Minutes:

Call for patents

Discussed anslow_03_0913_optx, MTTFPA restrictions and evaluation

Reviewed Sli_01 and need for higher than 15dB loss

- Includes connector, material loss assumptions
- Desire for lower power, lower latency solution relative to KR4

Reviewed paths forward

- DFE based CAUI-4 chip-to-chip leveraging KR4 without FEC, transmitter training
 - MTTFPA analysis required for channels of interest
 - Power consumption for DFE
- CTLE based CAUI-4 chip-to-chip
 - potential channel/package limitations with respect to reflections

Action for system vendors to provide sample channels: less than 15dB, ~15dB, 20dB, and 20dB+

Action for Ryan / Kapil to link current channels with associated presentation material

Next meeting: Sept 30, 9am PT