

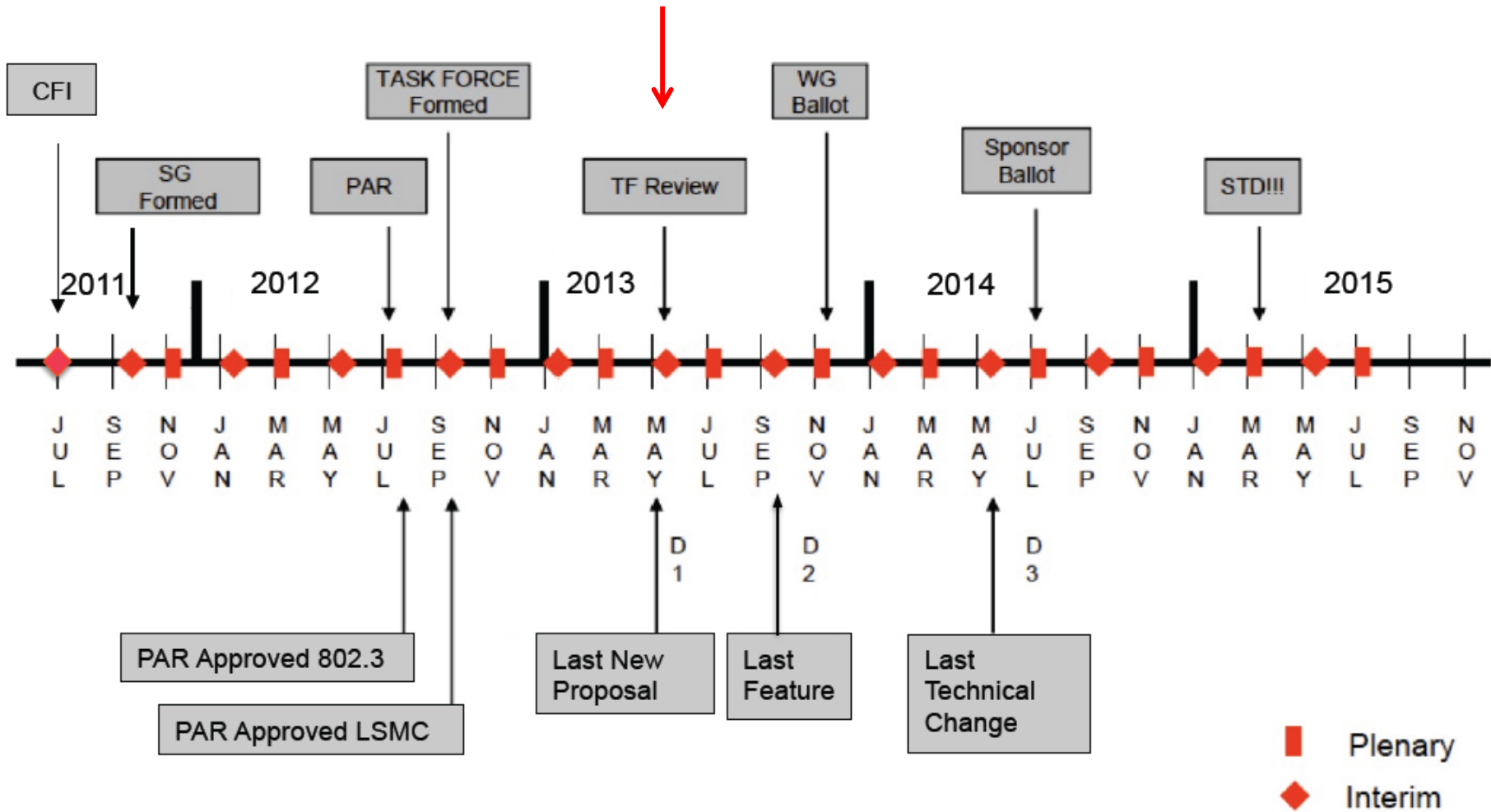
# Editor's closing report

Pete Anslow, Ciena, P802.3bm Chief Editor

IEEE P802.3bm Task Force, Victoria, May 2013

# Adopted Task Force timeline

We are here



# Draft review schedule

Initial Task Force review proposed to be 30 days.

Dates shown are subject to change

	Mon	Tue	Wed	Thu	Fri	Sat	Sun	Mon	Tue	Wed	Thu	Fri	Sat	Sun	Mon	Tue	Wed	Thu	Fri	Sat	Sun	Mon	Tue	Wed	Thu	Fri	Sat	Sun	Mon	Tue	Wed	Thu	Fri	Sat	Sun	Mon	Tue									
May			1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31			May										
5															Victoria																					Draft 1.0 review										
Jun								1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30			Jun						
6								Draft 1.0 review																																						
Jul	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31			Jul												
7															Geneva																															
Aug								1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31			Aug					
8								Draft 1.1 review																																						
Sep								1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30			Sep						
9								York																																Draft 1.2 review						
Oct								1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31			Oct					
10								Draft 1.2 review																																						
Nov								1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30			Nov						
11								pre							Dallas																											Draft 2.0 ballot				
Dec								1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31			Dec					
12								Draft 2.0 ballot																																						

# Major items to be resolved

- 100 Gb/s PHY for operation up to at least 500 m of SMF
- 100GBASE-SR4
  - stressed sensitivity measurement
  - TDP measurement
  - Eye mask
- CAUI-4
  - chip-module eye height and total jitter
  - chip-chip channel loss target
  - chip-chip output waveform and de-emphasis range
  - chip-chip COM model modifications

Thanks!