

MMF Ad Hoc meeting minutes

13th Oct 2014

Unapproved minutes
recorded by jonathan king

MMF ad hoc meeting minutes, 13th October 2014

- **Meeting started** at 9 am Pacific, chaired by Jonathan King.
- **Attendee list** was taken from the Webex attendee list, 12 attendees were noted.
- **Presentations** shared in the MMF ad hocs can be found at the MMF ad hoc web page.
 - <http://www.ieee802.org/3/bm/public/mmfadhoc/meetings/index.html>
- **IEEE patent policy:** Attendees were reminded of the IEEE patent policy
 - <http://www.ieee802.org/3/patent.html>
- **House keeping:** The agenda was approved.
- **Discussion topics:**
 - Use of OIF SSPR (Short Stress Pattern Random) as a test pattern in Clause 95 – Greg LeCheminant (Ref: http://www.oiforum.com/public/documents/OIF_CEI_03.1.pdf)
- **Presentations:**
 - ‘Stressed Rx input signal observations’ – John Petrilla
 - ‘SRS generator update’ – Piers Dawe
- **Discussion:**
 - Greg asked if, for TDEC and SEC tests, Cl.95 should also include SSPR as an acceptable pattern. SSPR would allow ‘pattern lock’ on some oscilloscopes, which may speed up the test. There was some discussion; the general consensus was not to add the pattern, since it was not clear that SSPR was equivalent to PRBS31 for all relevant parameters. Pete Anslow said that SSPR was equivalent to a long period of random data but not as long as PRBS31 (~10000 years) for baseline wander, clock content and digital sum variation statistics.

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- **Discussion continued:**
 - John Petrilla showed stressed receiver test signal results meeting the 4.9dB SEC spec, but just under the J2 and J4 specs . Transmission lines were used to control the transition time of the source, PRBS31 was used for OMA and SEC measurements. SEC was measured using 2×10^6 and 127×10^6 samples, with consistent results.
 - Piers Dawe presented an update of simulations of stressed receiver test signal generation. Using BT4 filters, Piers found that the restriction on PWS (pulse width shrinkage) of 0.1UI prevented him from meeting the jitter specs. Piers also showed that a TDEC of 4.4 dB was more representative of the value to be expected from a transmitter with worst case rise time and jitter when passed through a filter representing a worst case link.
- **Meeting ended** at 10.33 am
- **Next meeting:** Thursday 23rd October , 9 am to 10.30 am Pacific

Attendees

Pete Anslow, Ciena

Piers Dawe, Mellanox

Ali Ghiasi, Ghiasi Quantum LLC

Rita Horner, Synopsis

Richard Johnson, Finisar

Jonathan King, Finisar

Greg LeCheminant, Agilent

Karen Liu, Kaia Corp

Petar Pepeljugski, IBM

John Petrilla, Avago Tech

Shoahua, Brocade

Kuang-yi Wu, HP