

Figure 0-1—Functional blocks within 10GPASS-XR-D CLT PCS, PMA, and PMD sublayers



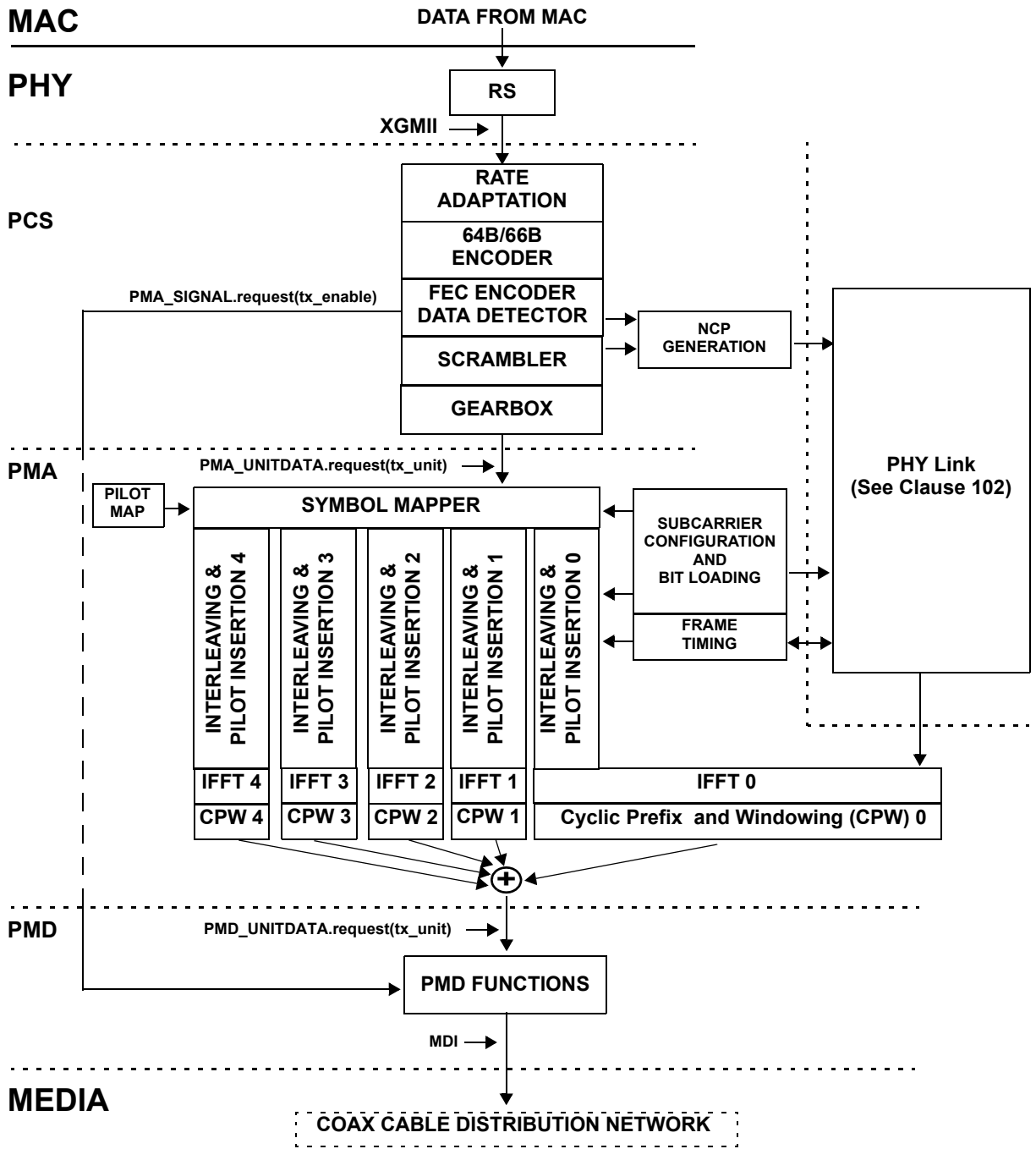


Figure 0-2—Functional blocks within 10GPASS-XR-D CLT PCS, PMA, and PMD sublayers